RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RX*-A172A/E	Rev.	1.00
Title	Note on Using the DMA/DTC Transfer in the USB 2.0 Host/Function Module (USBA)		Information Category	Technical Notification		
Applicable Product	RX71M Group, RX64M Group	Lot No.	Reference Document	RX71M Group User's Manual: Hardware Rev.1.00 (R01UH0493EJ0100) RX64M Group User's Manual: Hardware Rev.1.10 (R01UH0377EJ0110)		
		All				

1. Note

This document describes possible errata with the USB 2.0 Full-Speed Host/Function Module (USBA) in RX64M Group and the USB 2.0 High-Speed Host/Function Module (USBAa) in RX71M Group in which the DMA/DTC transfer request is not generated if the following condition is satisfied:

• The DnFIFOSEL.DREQE bit is set to 1 from 0 while the DnFIFOCTR.FRDY flag is 1.

2. Countermeasure

When enabling the DMA/DTC transfer request, set the DnFIFOSEL.DREQE bit to 1 while the DnFIFOCTR.FRDY flag is 0.

For example, follow the procedure below.

(1) Set the DnFIFOSEL.CURPIPE[3:0] bits to 0000b.

(2) Confirm that the DnFIFOSEL.CURPIPE[3:0] bits are 0000b.

(3) Set the DnFIFOSEL.DREQE bit to 1.

(4) Set the pipe number to be transferred by the DMAC/DTC to the DnFIFOSEL.CURPIPE[3:0] bits. [1]

Note 1.   The DREQE bit does not need to be 0 when the value of the DnFIFOSEL.CURPIPE[3:0] bits is changed.

3. USB Driver Software

The USB sample driver software that reflects above procedure is to be released. Please replace the current version with the revised version when released.

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