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On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

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MSC TECHNICAL NEWS

No. M16C-05-9612

Note on setting the chip select control register of the M16C/60 group and the M16C/61 group (Rev.B)

1. Related devices

M16C/60 group or M16C/61 group

2. Note

Use the MOV instruction when writing to the chip select control register (address 0008₁₆).

3. Reason

When reading the chip select control register in the single-chip mode, "00₁₆" must be read out.

Therefore, when using the read-modify-write instruction for the chip select control register in the single-chip mode, all bits become "0" except the bits set to be "1" by read-modify-write instruction.

This symptom does **not** happen under the following conditions:

- Memory expansion mode in the mask ROM version
- Memory expansion mode in the one time PROM version
- Memory expansion mode in the EPROM version
- Microprocessor mode in the mask ROM version
- Microprocessor mode in the one time PROM version
- Microprocessor mode in the EPROM version
- Memory expansion mode using a development tool
- Microprocessor mode using a development tool
- Single-chip mode using a development tool