To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

Send any inquiries to http://www.renesas.com/inquiry.

Date: Nov.23.2004

RENESAS TECHNICAL UPD

Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan
RenesasTechnology Corp.

Product Category	MPU&MCU		Document No.	TN-H8*-A288A/E	Rev.	1.0
Title	Limitation on SSU module stop mode for H8SX/1520 Group and H8SX/1582		Information Category	Technical Notification		
Applicable Product	H8SX/1520 Group H8SX/1582	Lot No. All lots	Reference Document	H8SX/1520Group Hardware Manual (REJ09B0104-0100Z Rev.1.00) H8SX/1582 Hardware Manual (REJ09B0199-0100Z Rev.1.00)		

Thank you for your consistent patronage of Renesas semiconductor products.

We would like to inform you the limitation on SSU Module Stop Mode.

1. Content

The module stop mode of SSU_1(or SSU_2) is cleared only when the following two module stop bits are cleared.

1. Module stop bit of SSU_1; MSTPC9 (or Module stop bit of SSU_2; MSTPC10)
2. Module stop bit of SSU_0; MSTPC8

Whereas, for E6000H, the module stop mode of SSU_1(or SSU_2) is cleared when the module stop bit of SSU_1; MSTPC9 (or the module stop bit of SSU_2; MSTPC10) is cleared.

2. Background

The module stop bits of SSU_1 and SSU_2 are masked by the module stop bit of SSU_0. Therefore, the module stop bits of SSU_1 and SSU_2 are valid when the module stop bit of SSU_0 is cleared.

3. Countermeasure

In case SSU_1 (or SSU_2) is to be used without using SSU_0, please clearing not only the module stop bit of SSU_1; MSTPC9 (or the module stop bit of SSU_2; MSTPC10) but also module stop bit of SSU_0; MSTPC8.

If SSU_0 is not used as SSU and the module stop bit of SSU_0 is cleared, the multiplexed corresponding I/O port of SSU_0 can be used as I/O port except for the case that SSU_0 data transmission and(or) reception setting is executed.