

NEC Microcomputer Technical Information

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IE-780354-NS-EM1 Emulation Board for μ PD780344, 780354, 780344Y, 780354Y Subseries Upgrade		Document No.	SBG-TT-0189-E	1/1
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Related documents	User's manual: SUD-TT-0021-2-E		√	Upgrade
				Document modification
				Other notification

1. Affected product

Product	Outline	Control code ^{Note}
IE-780354-NS-EM1	Emulation board μ PD780344, 780354, 780344Y, 780354Y Subseries	A, B

It is not necessary to upgrade a control code C product.

2. Details of upgrade

The restriction described below will be corrected. After upgrading, the control code will be C.

No.2 Interrupt request flag ADIF0 of the A/D converter function cannot be set.

Control code: A, B

3. Upgrade petition period

From October 1, 2002.

The upgrade described herein will be provided for free for a period of one year from the above date. After the free upgrade period expires, an upgrade will be available for a fee. We therefore recommend that you take advantage of the free upgrade offer during the free upgrade period.

Note The "control code" is the second digit from the left in the 10-digit serial number starting with E in the warranty supplied with the product you purchased. If the product has been upgraded, a label indicating the new version is attached to the product and the x in V-UP LEVEL x on this label indicates the control code.

Notes on Using IE-780354-NS-EM1

1. Production History

No.	Bugs and Changes/Additions to Specifications	Control Code ^{Note}		
		A	B	C
1	IE-78K0-NS-A and IE-78K0-NS-PA cannot be connected.	×	√	√
2	Interrupt request flag ADIF0 of the A/D converter function cannot be set.	×	×	√

×: Applicable, √: Not applicable or already corrected

Note The “control code” is the second digit from the left in the 10-digit serial number in the warranty supplied with the product you purchased (if it has not been upgraded). If the product has been upgraded, a label indicating the new version is attached to the product and the x in V-UP LEVEL x on this label indicates the control code.

2. Details of Bugs and Added Specifications

No. 1 IE-78K0-NS-A and IE-78K0-NS-PA cannot be connected.

[Description]

Since the parts soldered on the underside of the IE-780354-NS-EM1 come into contact with the parts surface of the IE-78K0-NS-A and IE-78K0-NS-PA, connection of these products is physically impossible.

The IE-78K0-NS can be connected without problem.

[Workaround]

There is no workaround.

This specification applies to products with control code B.

No.2 Interrupt request flag ADIF0 of the A/D converter function cannot be set.

[Description]

The interrupt request flag ADIF0 (bit 6 of IF1L) of the 8-/10-bit A/D converter function cannot be set.

[Workaround]

There is no workaround.

This bug has been corrected in control code C, but restriction No.8 in “3. Other Restrictions” is still applicable.

3. Other Restrictions

- No.1 Set data to the SCT flag (bit 0 of the SSCK register) after reset release and before setting the CSS flag (bit 4 of the PCC register) to 1. The SCT flag can only be cleared by a reset.
- No.2 When the CPU is operating at x4 subsystem clock, a wait equivalent to 1 subsystem clock cycle is not inserted in the emulator after HALT instruction release. (A wait equivalent to 1 subsystem clock cycle is inserted in the target device.)
- No.3 When a target device is not connected, the initial value of P17 to P10, P30, P31, and P73 to P70 is undefined.
- No.4 The port output (write timing) is one clock earlier than that in the target device.
- No.5 When the 8-bit timer (TM51) and watch timer (WTN0) are operated with the subsystem clock, an interrupt may occur one clock earlier than the timing in the target device specification.
- No.6 The pin status of ports 0, 2, 3,4, 7, 8, 9, 10, and 11 in the period from when power is applied to the IE system until when the integrated debugger ID78K0-NS is activated is 5 V high level due to pull-up connection in input mode (resistance value: 10 to 100 k Ω) set by an on-chip circuit of the μ PD7880, the device for emulation.
- No.7 If the blinking cycle of the LCD controller/driver function is set to 1.0 s, the blinking display cannot be emulated.
- No.8 When the 8-/10-bit A/D converter function is used, the interrupt mask flag register ADMK0 (bit 6 of MK1L) must be set to 0 or 1 as the initial setting.

4. Other Cautions

- No.1 Connection with the IE-78001-R-A is not supported.
(If connected, the port trace result will not be displayed correctly.)
- No.2 Use device file E1.00j or later.
- In E1.00h or earlier, the mask option function for P30, P31, P70 to P73 cannot be used.
 - In E1.00j or earlier, the blinking display of the LCD controller/driver function does not operate correctly.