

CUSTOMER NOTIFICATION  ID850NWC Ver. 2.52 Update Module	SUD-DT-03-0525-E
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This is to inform you of the release of an update module for the integrated debugger ID850NWC V2.52. The V850E/IA4 and V850E/ME2 can be supported by the ID850NWC as a result of installing the update module.

[Target product]

Integrated debugger ID850NWC (part number: ID703000-GC) V2.52

[Related documents]

- ID850NWC Ver. 2.51 Integrated Debugger Operation User's Manual  
Document number: U16525EJ1V0UM00 (1st edition)
- ID850NWC Ver. 2.51 Changes in Operation User's Manual  
Document number: SUD-DT-03-0297-E
- Integrated Debugger ID850NWC V2.52 Operating Precautions  
Document number: SUD-DT-03-0298-E

[Caution]

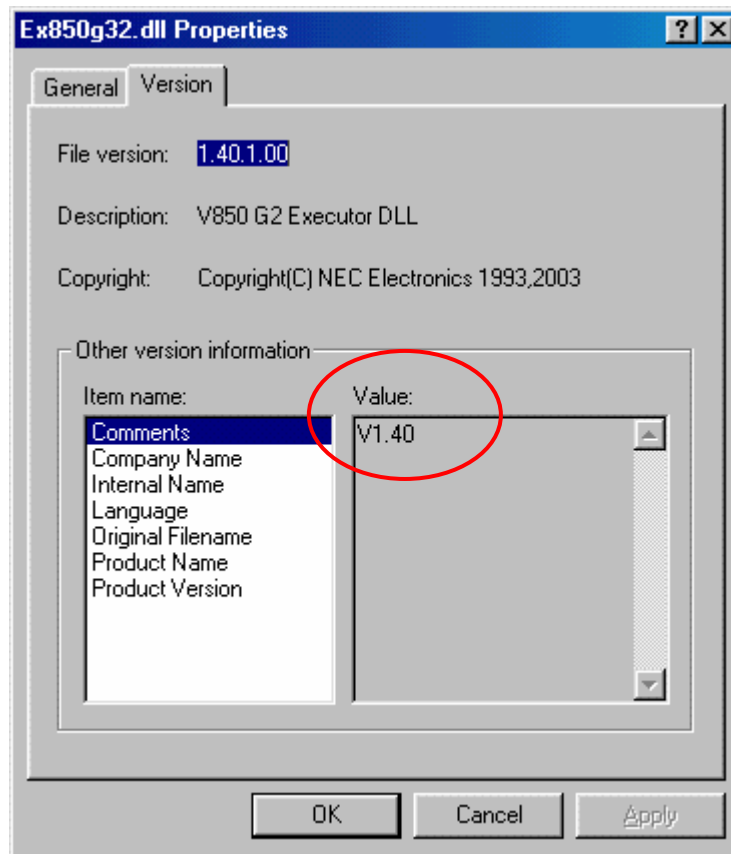
Be sure to install the update module (update\_module.exe) in ID850NWC V2.52.

Customers who use ID850NWC V2.51 are required to upgrade their product to V2.52 before installing the update module.

## 1. How to Install Update Module

### 1.1 Installation procedure

- (1) Copy the update module update\_module.exe to a temporary directory.
- (2) Execute update\_module.exe copied in the temporary directory.  
ex850g32.dll is unzipped in the temporary directory.
- (3) Overwrite ex850g32.dll to the “drive where development tool was installed:\Nectools32\bin” if the NEC Electronics development tool has been installed in the default directory.
- (4) Delete the files in the temporary directory; they are no longer necessary.
- (5) Confirm that the version of ex850g32.dll is 1.40 by opening the Ex850g32.dll Properties window using Explorer.



## 2. Addition to Cautions

Cautions have been added to **6.2 Cautions** in the document “Integrated Debugger ID850NWC V2.52 Operating Precautions” (document number: SUD-DT-03-0298-E) in line with support for the V850E/IA4 and V850E/ME2.

There are no changes in restrictions.

### 2.1 Cautions

The added cautions No. 73, No. 74, and No. 75 are shown below.

73	<p>When the V850E/ME2, which supports on-chip instruction RAM, is used, downloading a program or memory dumping is possible during a break regardless of the IRAMM register value.</p> <p>The following processing is performed internally on the on-chip instruction RAM.</p> <ul style="list-style-type: none"><li>• When downloading a program, IRAMM is set to write mode and the IRAMM value before download is restored after download is complete.</li><li>• When displaying the memory contents, IRAMM is set to read mode and the IRAMM value before displaying the memory is restored after displaying the memory contents is complete.</li></ul> <p>Be sure to confirm that IRAMM is in read mode when executing an instruction whose start address is in the on-chip instruction RAM after changing the PC value.</p> <p>IRAMM is set to write mode after reset according to the device specifications.</p>
74	<p>When setting a software break to the on-chip instruction RAM area, be sure to write the program before setting the break so that the software break instruction is not overwritten.</p>
75	<p>The PC value after reset is 100000H in the V850E/ME2.</p>