

NEC Microcomputer Technical Information

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<p style="text-align: center;">ID850 V850 Series Integrated Debugger Usage Restrictions</p>	Document No.	ZBG-CD-04-0033	1/2
	Date issued	August 11, 2004	
	Issued by	Development Tool Group Multipurpose Microcomputer Systems Division 3rd Systems Operations Unit NEC Electronics Corporation	
<p>Related documents ID850 V2.50 Integrated Debugger Operation User's Manual: U16217EJ1V0UM Integrated Debugger ID850 V2.51 Operating Precautions: SBG-TT-0210-E</p>	Notification classification	√	Usage restriction
			Upgrade
			Document modification
			Other notification

1. Affected product

ID850 V2.51 (part number: ID703000)

2. New restrictions

- No.26 Break occurs during program execution if variable is registered in Watch window and the window is open during program execution
- No.27 Bug that relay continues to switch the power on/off

3. Workarounds

- No.26 Close the Watch window during program execution
- No.27 There is no workaround.

4. Modification schedule

Modified products are scheduled for release as follows.

ID850 V2.52 (part number: ID703000): Planned for release on August 18, 2004 (registration to ODS).

* Note that this schedule is subject to change without notice. For the detailed release schedule of modified products, contact an NEC Electronics sales representative.

5. List of restrictions

A list of restrictions in the ID850, including the revision history and detailed information, is described on the following pages.

6. Revision history

ID850 V850 Series Integrated Debugger Usage Restrictions

Document Number	Issued on	Description
SBG-T-2501-E	September 28, 2001	Newly created.
SBG-DT-04-0126	August 11, 2004	Addition of new restrictions (No.26 and No.27)

List of Restrictions in ID850

1. Product History

No.	Description	Version	
		V2.51	V2.52
1	Even if an area that is not the target memory is set to I/O protected, an error message is not displayed.	×	×
2	When setting an I/O protected area, if an area is set that is not aligned with the access size, the protect function is not effective.	×	×
3	Up to six programs (load module files) can be loaded in the download dialog box.	√	√
4	When the PC is in the prologue of a function during step execution in the mixed display mode in the source text window, the PC marker and current PC line (yellow) are not displayed.	×	×
5	When the Source window is in mixed display mode, if the cursor is moved in the downward direction, it may inadvertently jump. Also, in mixed display mode, the end of the source line cannot be displayed without using the scroll.	×	×
6	"divh r0, register name" instruction cannot be line-assembled correctly. An illegal code "dbint" or "dbtrap" is displayed.	×	×
7	The setting of the radix display of the item in the nest of a variable with nest cannot be correctly downloaded from a file on the watch window.	×	×
8	If the data in the Local Variable window is moved by the cursor while emulation is being executed, the display changes to "***".	×	×
9	Non-map area and IOR area cannot be avoided in memory search.	×	×
10	If the specified copy destination start address is in the internal ROM area, the copy destination start address and copy source start address are aligned in 4-byte units. The copy destination end address is the address at which copying the memory of the size starting from the start address aligned in 4-byte units ends.	√	√
11	If the cursor is placed in the ASCII display area of the Memory window and then control is shifted to another window, the cursor will inadvertently return to the data display area.	×	×
12	The tracer is not displayed unless tracer and coverage is stopped during program execution.	×	×
13	An event does not occur even if the status of the event used is changed from Execution to R/W (or vice versa).	×	×
14	Timer stop/start on the Run menu does not work.	×	×
15	Period (.) must not be used in a symbol name.	×	×
16	[IE-703002-MC] If the halt instruction is Stepped, the debugger hangs up for a while (for 30 seconds).	×	×
17	If step or execution is carried out on the last line ^{Note} of the source, an error occurs. Note The last line of main() in C source or the last line of the code in an assembler source.	×	×
18	If one line contains too many instructions (more than 1000 assembler instructions) in source line step, the processing may be aborted even in the middle of step execution.	×	×

No.	Description	Version	
		V2.51	V2.52
19	If a control statement ^{Note} in a loop is written at the beginning of a function in a C source, the following symptom occurs because of influence of optimization. This symptom appears if -Os or -Ot is specified as a compile option. (1) An error occurs if a line corresponding to an address has been deleted as a result of the above optimization. Note for and while statements. do while () statement is not included.	√	√
20	In the ROM/flash memory relink function supported by the compiler, it is not possible to perform step-in to a function on the flash memory side from the boot side.	×	×
21	Debugging of programs that use program code copied to the internal RAM after being passed through a ROMization processor is not supported. There is no problem in the ROMization of data.	×	×
22	A breakpoint cannot be set in the Source window if there is a source file that includes a "-" (hyphen) or "+" (plus) in its name.	×	×
23	A variable other than global (structure/union member, array member, bit field member, etc.) cannot be specified for an access break in the Watch window.	×	×
24	A variable other than global (structure/union member, array member, bit field member, etc.) cannot be specified for an access break in the Source window.	×	×
25	[IE-V850ES-G1, IE-V850ESK1-ET] Restriction on trace data when operating with the subclock	×	√
26	Break occurs during program execution if variable is registered in Watch window and the window is open during program execution	×	√
27	An error dialog box is displayed when step execution is performed during subclock operation	×	√

×: Applicable, √: Not applicable

No.10 and No.19 are marked "×" in the document "Integrated Debugger ID850 V2.51 Operating Precautions" (SBG-TT-0210-E), but actually they should be "√".

2. Details of Usage Restrictions

No.26 Break occurs during program execution if variable is registered in Watch window and the window is open during program execution

[Description]

A break occurs during program execution if a variable outside the range of real-time RAM monitoring by the internal RAM is registered in the Watch window and the window is open during program execution.

As a result, the time display of the time measurement function may be incorrect.

[Workaround]

Close the Watch window during program execution.

[Action]

This bug will be corrected in V2.52.

No.27 An error dialog box is displayed when step execution is performed during subclock operation

[Description]

A "Trace enabled" error dialog box is displayed when step execution is performed during subclock operation.

[Workaround]

There is no workaround.

[Action]

This bug will be corrected in V2.52.