Microcomputer Technical Information

		Document No.	SBG-DT-03-0034-E	1/1
EV-703210GC Upgrade		Date issued	February 3, 2003	
		Issued by	Microcomputer Group 2nd Solutions Division Solutions Operations Unit NEC Electronics Corporation	
Related documents	User's manual: SUD-DT-02-0034	Document classification		Usage restriction
			√	Upgrade
				Document modification
				Other notification

1. Affected product

Product	Outline	Old Control Code[Note]/Version	New Control Code/Version
EV-703210GC	Socket for V850ES/KF1 (GC package) development	A	B

2. Details of upgrade

   The bug described on the following page will be corrected (addition of specifications). After upgrading, the control code will be B. See the attachment for details.

   • No.1 Illegal recognition of target voltage   Control code: A

3. Upgrade petition period

   Newly shipped products: From the shipment of February 2003

   Upgrade for already shipped products*: Available from February 24, 2003

   * For already shipped products, the upgraded product will be provided for free.

Note The "control code" is the second digit from the left in the 10-digit serial number in the warranty supplied with the product you purchased (if it has not been upgraded).

Notes on Using EV-703210GC

1. Product Version

Control Code	Remark
A	Part number of conversion adapter (GEA): GEA-14-0.65
B	Part number of conversion adapter (GEA): 703210GC-EA-B

2. Product History

No.	Bugs and Changes/Additions to Specifications	Control Code[Note]	
		A	B
1	Illegal recognition of target voltage	×	√

×: Applicable, √: Not applicable or already corrected

> **Note** The "control code" is the second digit from the left in the 10-digit serial number in the warranty supplied with the product you purchased (if it has not been upgraded).

3. Details of Bugs and Added Specifications

No.1 Illegal recognition of target voltage

[Description]

(1) The following bugs occur when the target is connected and used in combination with the IE-703217-G1-EM1.

- The following pins operate at 5 V, not at the voltage of EVDD.

Pin Number	Pin Name
47, 48	PCS0/_CS0, PCS1/_CS1
49 to 52	PCM0/_WAIT to PCM3/_HLDRQ
53 to 56	PCT0/_WR0 to PCT6/ASTB
57 to 72	PDL0/AD0 to PDL15/AD15

- _RESET, _HOLDRQ, _WAIT, and NMI input from the target are not acknowledged.

(2) The following bugs occur when the target is connected and used in combination with the IE-V850ESK1-ET.

- When the external memory space is mapped in the debugger, the error message "W100f: Target is not turned on." is displayed and mapping cannot be set.
- _RESET, _HOLDRQ, _WAIT, and NMI input from the target are not acknowledged.

[Workaround]

(1) When using the IE-703217-G1-EM1

Short the TP5 (TEVDD) and TP7 (TBVDD) pins on the IE-703217-G1-EM1 when the power supply to the emulator is off.

(2) When using the IE-V850ESK1-ET

Short the TP26 (TEVDD) and TP28 (TBVDD) pins on the IE-V850ESK1-ET when the power supply to the emulator is off.