

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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RENESAS TECHNICAL UPDATE

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Product Category	MPU&MCU		Document No.	TN-SH7-A692A/E	Rev.	1.00
Title	The error correction about register address of Serial Sound Interface (SSI).		Information Category	Technical Notification		
Applicable Product	SH7203 Group SH7263 Group	Lot No.	Reference Document	SH7263 Group Hardware Manual Rev.2.00 (REJ09B0290-0200) SH7203 Group Hardware Manual Rev.2.00 (REJ09B0313-0200)		
		ALL				

We would like to inform you of the following error correction about register address of Serial Sound Interface (SSI) included in the above-mentioned applicable products.

[Error]

Table 18.2 Register Description

Channel	Register Name	Abbreviation	R/W	Initial Value	Address	Access Size
0	Control register 0	SSICR_0	R/W	H'00000000	H'FFFE0000	32
	Status register 0	SSISR_0	R/W*	H'02000003	H'FFFE0004	32
	Transmit data register 0	SSITDR_0	R/W	H'00000000	H'FFFE0008	32
	Receive data register 0	SSIRDR_0	R	H'00000000	H'FFFE000C	32
1	Control register 1	SSICR_1	R/W	H'00000000	H'FFFE0800	32
	Status register 1	SSISR_1	R/W*	H'02000003	H'FFFE0804	32
	Transmit data register 1	SSITDR_1	R/W	H'00000000	H'FFFE0808	32
	Receive data register 1	SSIRDR_1	R	H'00000000	H'FFFE080C	32
2	Control register 2	SSICR_2	R/W	H'00000000	H'FFFE1000	32
	Status register 2	SSISR_2	R/W*	H'02000003	H'FFFE1004	32
	Transmit data register 2	SSITDR_2	R/W	H'00000000	H'FFFE1008	32
	Receive data register 2	SSIRDR_2	R	H'00000000	H'FFFE100C	32
3	Control register 3	SSICR_3	R/W	H'00000000	H'FFFE1800	32
	Status register 3	SSISR_3	R/W*	H'02000003	H'FFFE1804	32
	Transmit data register 3	SSITDR_3	R/W	H'00000000	H'FFFE1808	32
	Receive data register 3	SSIRDR_3	R	H'00000000	H'FFFE180C	32

[Correction]

Table 18.2 Register Description

Channel	Register Name	Abbreviation	R/W	Initial Value	Address	Access Size
0	Control register 0	SSICR_0	R/W	H'00000000	H'FFFFC000	32
	Status register 0	SSISR_0	R/W*	H'02000003	H'FFFFC004	32
	Transmit data register 0	SSITDR_0	R/W	H'00000000	H'FFFFC008	32
	Receive data register 0	SSIRDR_0	R	H'00000000	H'FFFFC00C	32
1	Control register 1	SSICR_1	R/W	H'00000000	H'FFFFC800	32
	Status register 1	SSISR_1	R/W*	H'02000003	H'FFFFC804	32
	Transmit data register 1	SSITDR_1	R/W	H'00000000	H'FFFFC808	32
	Receive data register 1	SSIRDR_1	R	H'00000000	H'FFFFC80C	32
2	Control register 2	SSICR_2	R/W	H'00000000	H'FFFFD000	32
	Status register 2	SSISR_2	R/W*	H'02000003	H'FFFFD004	32
	Transmit data register 2	SSITDR_2	R/W	H'00000000	H'FFFFD008	32
	Receive data register 2	SSIRDR_2	R	H'00000000	H'FFFFD00C	32
3	Control register 3	SSICR_3	R/W	H'00000000	H'FFFFD800	32
	Status register 3	SSISR_3	R/W*	H'02000003	H'FFFFD804	32
	Transmit data register 3	SSITDR_3	R/W	H'00000000	H'FFFFD808	32
	Receive data register 3	SSIRDR_3	R	H'00000000	H'FFFFD80C	32