

RENESAS TECHNICAL UPDATE

1753, Shimonumabe, Nakahara-ku, Kawasaki-shi, Kanagawa 211-8668 Japan
Renesas Electronics Corporation

Product Category	MPU/MCU		Document No.	TN-RX*-A007A/E	Rev.	1.00
Title	Errata to List of Pins and Pin Functions (100-Pin TFLGA and 100-Pin LQFP) of RX630 Group User's Manual: Hardware		Information Category	Technical Notification		
Applicable Product	RX630 Group	Lot No.	Reference Document	RX630 Group User's Manual: Hardware Rev. 1.11 (R01UH0040EJ0111)		
		All				

This document describes corrections to Table 1.9 "List of Pins and Pin Functions (100-Pin TFLGA)" and Table 1.10 "List of Pins and Pin Functions (100-Pin LQFP)" of RX630 Group User's Manual: Hardware Rev. 1.11.

<Corrections>

- Deleted the following signals of TPU6 to TPU11 and RSPI2 from Tables 1.9 and 1.10:
TIOCA6, TIOCB6, TIOCC6, TIOCD6, TIOCA7, TIOCB7, TIOCA8, TIOCB8,
TIOCA9, TIOCB9, TIOCC9, TIOCD9, TIOCA10, TIOCB10, TIOCA11, TIOCB11,
TCLKE, TCLKF, TCLKG, TCLKH,
SSLC0, SSLC1, SSLC2, and SSLC3
- Added the following signals of CAN0 to Table 1.9:
B6 pin: CTX0
C7 pin: CRX0
- Added the following signals of CAN0 to Table 1.10:
84 pin: CRX0
85 pin: CTX0

The following pages describe corrected Tables 1.9 and 1.10.

Deleted signals and added signals are shown in strikethrough and boldface, respectively, in the tables.

Table 1.9 List of Pins and Pin Functions (100-Pin TFLGA) (1/5)

Pin Number	Power Supply Clock System Control	I/O Port	Bus	Timer (MTU, TPU, TMR, PPG, RTC, POE)	Communications (SC1c, SC1d, RSPI, RIIC, CAN, IEB, USB)	Interrupt	S12AD, AD, DA
A1		P05				IRQ13	DA1
A2	VREFH						
A3		P07				IRQ15	ADTRG0
A4	VREFL0						
A5		P43				IRQ11-DS	AN003
A6		PD0	D0[A0/D0]	TI0CA7		IRQ0	AN008
A7		PD4	D4[A4/D4]	POE3#	SSLC0	IRQ4	AN012
A8		PE0	D8[A8/D8]	TI0CC9	SCK12 SSLB1		ANEX0
A9		PE1	D9[A9/D9]	MTI0C4C TI0CD9 PO18	TXD12 SMOSI12 SSDA12 TXDX12 SIOX12 SSLB2 RSPCKB		ANEX1
A10		PE2	D10[A10/D10]	MTI0C4A TI0CA9 PO23	RXD12 SMISO12 SSCL12 RXDX12 SSLB3 MOSIB	IRQ7-DS	AN0
B1	EMLE						
B2	AVSS0						
B3	AVCC0						
B4		P40				IRQ8-DS	AN000
B5		P44				IRQ12-DS	AN004
B6		PD1	D1[A1/D1]	MTI0C4B TI0CB7 TCLKG	CTX0*1	IRQ1	AN009
B7		PD3	D3[A3/D3]	TI0CB8 TCLKH POE8#		IRQ3	AN011
B8		PD6	D6[A6/D6]	MTIC5V POE1#	SSLC2	IRQ6	AN6
B9		PD7	D7[A7/D7]	MTIC5U POE0#	SSLC3	IRQ7	AN7
B10		PE3	D11[A11/D11]	MTI0C4B TI0CB9 PO26 POE8#	CTS12# RTS12# SS12# MISOB		AN1
C1	VCL						
C2	VREFL						
C3		PJ3		MTI0C3C	CTS6# RTS6# CTS0# RTS0# SS6# SS0#		
C4	VREFH0						
C5		P42				IRQ10-DS	AN002
C6		P47				IRQ15-DS	AN007
C7		PD2	D2[A2/D2]	MTI0C4D TI0CA8	CRX0*1	IRQ2	AN010
C8		PD5	D5[A5/D5]	MTIC5W POE2#	SSLC1	IRQ5	AN013
C9		PE5	D13[A13/D13]	MTI0C4C MTI0C2B TI0CB10	RSPCKB	IRQ5	AN3

Table 1.9 List of Pins and Pin Functions (100-Pin TFLGA) (2/5)

Pin Number	Power Supply Clock System Control	I/O Port	Bus	Timer (MTU, TPU, TMR, PPG, RTC, POE)	Communications (SCIC, SCID, RSPI, RIIC, CAN, IEB, USB)	Interrupt	S12AD, AD, DA
C10		PE4	D12[A12/D12]	MTIOC4D MTIOC1A TIOCA10 PO28	SSLB0		AN2
D1	XCIN						
D2	XCOUT						
D3	MD FINED						
D4	VBATT						
D5		P45				IRQ13-DS	AN005
D6		P46				IRQ14-DS	AN006
D7		PE6	D14[A14/D14]	TIOCA11	MOSIB	IRQ6	AN4
D8		PE7	D15[A15/D15]	TIOCB11	MISOB	IRQ7	AN5
D9		PA1	A1	MTIOC0B MTCLKC TIOCB0 PO17	SCK5 SSLA2	IRQ11	
D10		PA0	A0 BC0#	MTIOC4A TIOCA0 PO16	SSLA1		
E1	XTAL	P37					
E2	VSS						
E3	RES#						
E4	TRST#	P34		MTIOC0A TMC13 PO12 POE2#	SCK6 SCK0	IRQ4	
E5		P41				IRQ9-DS	AN001
E6		PA2	A2	PO18	RXD5 SMISO5 SSCL5 SSLA3		
E7		PA6	A6	MTIC5V MTCLKB TIOCA2 TMC13 PO22 POE2#	CTS5# RTS5# SS5# MOSIA		
E8		PA4	A4	MTIC5U MTCLKA TIOCA1 TMR10 PO20	TXD5 SMOSI5 SSDA5 SSLA0	IRQ5-DS	
E9		PA5	A5	TIOCB1 PO21	RSPCKA		
E10		PA3	A3	MTIOC0D MTCLKD TIOCD0 TCLKB PO19	RXD5 SMISO5 SSCL5	IRQ6-DS	
F1	EXTAL	P36					
F2	VCC						
F3		P35				NMI	
F4		P32		MTIOC0C TIOCC0 TMO3 PO10 RTCOUT RTCIC2	TXD6 TXD0 SMOSI6 SMOSI0 SSDA6 SSDA0 CTX0 ^{*1}	IRQ2-DS	

Table 1.9 List of Pins and Pin Functions (100-Pin TFLGA) (3/5)

Pin Number	Power Supply Clock System Control	I/O Port	Bus	Timer (MTU, TPU, TMR, PPG, RTC, POE)	Communications (SCIC, SCID, RSPI, RIIC, CAN, IEB, USB)	Interrupt	S12AD, AD, DA
F5		P12		TMC1	RXD2 SMISO2 SSCL2 SCL0[FM+]	IRQ2	
F6		PB3	A11	MTIOC0A MTIOC4A TIOCD3 TCLKD TMO0 PO27 POE3#	SCK6		
F7		PB2	A10	TIOCC3 TCLKC PO26	CTS6# RTS6# SS6#		
F8		PB0	A8	MTIC5W TIOCA3 PO24	RXD6 SMISO6 SSCL6 RSPCKA	IRQ12	
F9		PA7	A7	TIOCB2 PO23	MISOA		
F10	VSS						
G1		P33		MTIOC0D TIOCD0 TMRI3 PO11 POE3#	RXD6 RXD0 SMISO6 SMISO0 SSCL6 SSCL0 CRX0 ^{*1}	IRQ3-DS	
G2	TMS	P31		MTIOC4D TMC12 PO9 RTCIC1	CTS1# RTS1# SS1# SSLB0	IRQ1-DS	
G3	TDI	P30		MTIOC4B TMRI3 PO8 RTCIC0 POE8#	RXD1 SMISO1 SSCL1 MISOB	IRQ0-DS	
G4	TCK FINEC	P27	CS7#	MTIOC2B TMC13 PO7	SCK1 RSPCKB		
G5		P53 ^{*2}	BCLK				
G6		P52	RD#		RXD2 SMISO2 SSCL2 SSLB3		
G7		PB5	A13	MTIOC2A MTIOC1B TIOCB4 TMRI1 PO29 POE1#	SCK9		
G8		PB4	A12	TIOCA4 PO28	CTS9# RTS9# SS9#		
G9		PB1	A9	MTIOC0C MTIOC4C TIOCB3 TMC10 PO25	TXD6 SMOSI6 SSDA6	IRQ4-DS	
G10	VCC						
H1	TDO	P26	CS6#	MTIOC2A TMO1 PO6	TXD1 CTS3# RTS3# SMOSI1 SS3# SSDA1 MOSIB		

Table 1.9 List of Pins and Pin Functions (100-Pin TFLGA) (4/5)

Pin Number	Power Supply Clock System Control	I/O Port	Bus	Timer (MTU, TPU, TMR, PPG, RTC, POE)	Communications (SCIC, SCID, RSPI, RIIC, CAN, IEB, USB)	Interrupt	S12AD, AD, DA
H2		P25	CS5#	MTIOC4C MTCLKB TIOCA4 PO5	RXD3 SMISO3 SSCL3		ADTRG0#
H3		P16		MTIOC3C MTIOC3D TIOCB1 TCLKC TMO2 PO14 RTCOUT	TXD1 RXD3 SMOSI1 SMISO3 SSDA1 SSCL3 MOSIA SCL2-DS IERXD USB0_VBUS	IRQ6	ADTRG0#
H4		P15		MTIOC0B MTCLKB TIOCB2 TCLKB TMCI2 PO13	RXD1 SCK3 SMISO1 SSCL1 CRX1-DS	IRQ5	
H5		P55	WAIT#	MTIOC4D TMO3	CRX1	IRQ10	
H6		P54	ALE	MTIOC4B TMCI1	CTS2# RTS2# SS2# CTX1		
H7		PC7	A23 CS0#	MTIOC3A MTCLKB TIOCB6 TMO2 PO31	TXD8 SMOSI8 SSDA8 MISOA	IRQ14	
H8		PC6	A22 CS1#	MTIOC3C MTCLKA TIOCA6 TMCI2 PO30	RXD8 SMISO8 SSCL8 MOSIA	IRQ13	
H9		PB6	A14	MTIOC3D TIOCA5 PO30	RXD9 SMISO9 SSCL9		
H10		PB7	A15	MTIOC3B TIOCB5 PO31	TXD9 SMOSI9 SSDA9		
J1		P24	CS4#	MTIOC4A MTCLKA TIOCB4 TMRI1 PO4	SCK3		
J2		P21		MTIOC1B TIOCA3 TMCI0 PO1	RXD0 SMISO0/SSCL0	IRQ9	
J3		P17		MTIOC3A MTIOC3B TIOCB0 TCLKD TMO1 PO15 POE8#	SCK1 TXD3 SMOSI3 SSDA3 MISOA SDA2-DS IETXD	IRQ7	ADTRG#
J4		P13		MTIOC0B TIOCA5 TMO3 PO13	TXD2 SMOSI2 SSDA2 SDA0[FM+]	IRQ3	ADTRG#
J5	VSS_USB						
J6	VCC_USB						
J7		P50	WR0# WR#		TXD2 SMOSI2 SSDA2 SSLB1		

Table 1.9 List of Pins and Pin Functions (100-Pin TFLGA) (5/5)

Pin Number	Power Supply Clock System Control	I/O Port	Bus	Timer (MTU, TPU, TMR, PPG, RTC, POE)	Communications (SClC, SClD, RSPI, RIIC, CAN, IEB, USB)	Interrupt	S12AD, AD, DA
J8		PC4	A20 CS3#	MTIOC3D MTCLKC TIOCC6 TCLKE TMC11 PO25 POE0#	SCK5 CTS8# RTS8# SS8# SSLA0		
J9		PC0	A16	MTIOC3C TCLKC PO17	CTS5# RTS5# SS5# SSLA1	IRQ14	
J10		PC1	A17	MTIOC3A TCLKD PO18	SCK5 SSLA2	IRQ12	
K1		P23		MTIOC3D MTCLKD TIOCD3 PO3	TXD3 CTS0# RTS0# SMOSI3 SS0# SSDA3		
K2		P22		MTIOC3B MTCLKC TIOCC3 TMO0 PO2	SCK0		
K3		P20		MTIOC1A TIOCB3 TMRI0 PO0	TXD0 SMOSI0 SSDA0	IRQ8	
K4		P14		MTIOC3A MTCLKA TIOCB5 TCLKA TMRI2 PO15	CTS1# RTS1# SS1# CTX1 USB_DPUPE	IRQ4	
K5					USB0_DM		
K6					USB0_DP		
K7		P51	WR1# BC1# WAIT#		SCK2 SSLB2		
K8		PC5	A21 CS2# WAIT#	MTIOC3B MTCLKD TIOCD6 TCLKF TMRI2 PO29	SCK8 RSPCKA		
K9		PC3	A19	MTIOC4D TCLKB PO24	TXD5 SMOSI5 SSDA5 IETXD		
K10		PC2	A18	MTIOC4B TCLKA PO21	RXD5 SMISO5 SSCL5 SSLA3 IERXD		

Note 1. Enabled only for the on-chip ROM capacity of 768 Kbytes or more

Note 2. The BCLK function is multiplexed with the I/O port function for pin P53, so the port function is not available if the external bus is enabled.

Table 1.10 List of Pins and Pin Functions (100-Pin LQFP) (1/5)

Pin Number	Power Supply Clock System Control	I/O Port	Bus	Timer (MTU, TPU, TMR, PPG, RTC, POE)	Communications (SClC, SClD, RSPI, RIIC, CAN, IEB, USB)	Interrupt	S12AD, AD, DA
1	VREFH						
2	EMLE						
3	VREFL						
4		PJ3		MTIOC3C	CTS6# RTS6# CTS0# RTS0# SS6# SS0#		
5	VCL						
6	VBATT						
7	MD FINED						
8	XCIN						
9	XCOU						
10	RES#						
11	XTAL	P37					
12	VSS						
13	EXTAL	P36					
14	VCC						
15		P35				NMI	
16	TRST#	P34		MTIOC0A TMCi3 PO12 POE2#	SCK6 SCK0	IRQ4	
17		P33		MTIOC0D TIOCD0 TMRi3 PO11 POE3#	RXD6 RXD0 SMISO6 SMISO0 SSCL6 SSCL0 CRX0 ^{*1}	IRQ3-DS	
18		P32		MTIOC0C TIOCC0 TMO3 PO10 RTCOU RTCIC2	TXD6 TXD0 SMOSI6 SMOSI0 SSDA6 SSDA0 CTX0 ^{*1}	IRQ2-DS	
19	TMS	P31		MTIOC4D TMCi2 PO9 RTCIC1	CTS1# RTS1# SS1# SSLB0	IRQ1-DS	
20	TDI	P30		MTIOC4B TMRi3 PO8 RTCIC0 POE8#	RXD1 SMISO1 SSCL1 MISOB	IRQ0-DS	
21	TCK FINEC	P27	CS7#	MTIOC2B TMCi3 PO7	SCK1 RSPCKB		
22	TDO	P26	CS6#	MTIOC2A TMO1 PO6	TXD1 CTS3# RTS3# SMOSI1 SS3# SSDA1 MOSIB		
23		P25	CS5#	MTIOC4C MTCLKB TIOCA4 PO5	RXD3 SMISO3 SSCL3		ADTRG0#

Table 1.10 List of Pins and Pin Functions (100-Pin LQFP) (2/5)

Pin Number	Power Supply Clock System Control	I/O Port	Bus	Timer (MTU, TPU, TMR, PPG, RTC, POE)	Communications (SClC, SClD, RSPI, RIIC, CAN, IEB, USB)	Interrupt	S12AD, AD, DA
24		P24	CS4#	MTIOC4A MTCLKA TIOCB4 TMRI1 PO4	SCK3		
25		P23		MTIOC3D MTCLKD TIOCD3 PO3	TXD3 CTS0# RTS0# SMOSI3 SS0# SSDA3		
26		P22		MTIOC3B MTCLKC TIOCC3 TMO0 PO2	SCK0		
27		P21		MTIOC1B TIOCA3 TMCI0 PO1	RXD0 SMISO0 SSCL0	IRQ9	
28		P20		MTIOC1A TIOCB3 TMRI0 PO0	TXD0 SMOSI0 SSDA0	IRQ8	
29		P17		MTIOC3A MTIOC3B TIOCB0 TCLKD TMO1 PO15 POE8#	SCK1 TXD3 SMOSI3 SSDA3 MISOA SDA2-DS IETXD	IRQ7	ADTRG#
30		P16		MTIOC3C MTIOC3D TIOCB1 TCLKC TMO2 PO14 RTCOUT	TXD1 RXD3 SMOSI1 SMISO3 SSDA1 SSCL3 MOSIA SCL2-DS IERXD USB0_VBUS	IRQ6	ADTRG0#
31		P15		MTIOC0B MTCLKB TIOCB2 TCLKB TMCI2 PO13	RXD1 SCK3 SMISO1 SSCL1 CRX1-DS	IRQ5	
32		P14		MTIOC3A MTCLKA TIOCB5 TCLKA TMRI2 PO15	CTS1# RTS1# SS1# CTX1 USB0_DPUPE	IRQ4	
33		P13		MTIOC0B TIOCA5 TMO3 PO13	TXD2 SMOSI2 SSDA2 SDA0[FM+]	IRQ3	ADTRG#
34		P12		TMCI1	RXD2 SMISO2 SSCL2 SCL0[FM+]	IRQ2	
35	VCC_USB						
36					USB0_DM		
37					USB0_DP		
38	VSS_USB						
39		P55	WAIT#	MTIOC4D TMO3	CRX1	IRQ10	

Table 1.10 List of Pins and Pin Functions (100-Pin LQFP) (3/5)

Pin Number	Power Supply Clock System Control	I/O Port	Bus	Timer (MTU, TPU, TMR, PPG, RTC, POE)	Communications (SCIC, SCID, RSPI, RIIC, CAN, IEB, USB)	Interrupt	S12AD, AD, DA
40		P54	ALE	MTIOC4B TMCI1	CTS2# RTS2# SS2# CTX1		
41		P53 ^{*2}	BCLK				
42		P52	RD#		RXD2 SMISO2 SSCL2 SSLB3		
43		P51	WR1# BC1# WAIT#		SCK2 SSLB2		
44		P50	WR0# WR#		TXD2 SMOSI2 SSDA2 SSLB1		
45		PC7	A23 CS0#	MTIOC3A MTCLKB TIOCB6 TMO2 PO31	TXD8 SMOSI8 SSDA8 MISOA	IRQ14	
46		PC6	A22 CS1#	MTIOC3C MTCLKA TIACA6 TMCI2 PO30	RXD8 SMISO8 SSCL8 MOSIA	IRQ13	
47		PC5	A21 CS2# WAIT#	MTIOC3B MTCLKD TIOD6 TCLKF TMRI2 PO29	SCK8 RSPCKA		
48		PC4	A20 CS3#	MTIOC3D MTCLKC TIOD6 TCLKE TMCI1 PO25 POE0#	SCK5 CTS8# RTS8# SS8# SSLA0		
49		PC3	A19	MTIOC4D TCLKB PO24	TXD5 SMOSI5 SSDA5 IETXD		
50		PC2	A18	MTIOC4B TCLKA PO21	RXD5 SMISO5 SSCL5 SSLA3 IERXD		
51		PC1	A17	MTIOC3A TCLKD PO18	SCK5 SSLA2	IRQ12	
52		PC0	A16	MTIOC3C TCLKC PO17	CTS5# RTS5# SS5# SSLA1	IRQ14	
53		PB7	A15	MTIOC3B TIOCB5 PO31	TXD9 SMOSI9 SSDA9		
54		PB6	A14	MTIOC3D TIOCA5 PO30	RXD9 SMISO9 SSCL9		
55		PB5	A13	MTIOC2A MTIOC1B TIOCB4 TMRI1 PO29 POE1#	SCK9		

Table 1.10 List of Pins and Pin Functions (100-Pin LQFP) (4/5)

Pin Number	Power Supply Clock System Control	I/O Port	Bus	Timer (MTU, TPU, TMR, PPG, RTC, POE)	Communications (SC1c, SC1d, RSPI, RIIC, CAN, IEB, USB)	Interrupt	S12AD, AD, DA
56		PB4	A12	TIOCA4 PO28	CTS9# RTS9# SS9#		
57		PB3	A11	MTIOC0A MTIOC4A TIOCD3 TCLKD TMO0 PO27 POE3#	SCK6		
58		PB2	A10	TIOCC3 TCLKC PO26	CTS6# RTS6# SS6#		
59		PB1	A9	MTIOC0C MTIOC4C TIOCB3 TMCI0 PO25	TXD6 SMOSI6 SSDA6	IRQ4-DS	
60	VCC						
61		PB0	A8	MTIC5W TIOCA3 PO24	RXD6 SMISO6 SSCL6 RSPCKA	IRQ12	
62	VSS						
63		PA7	A7	TIOCB2 PO23	MISOA		
64		PA6	A6	MTIC5V MTCLKB TIOCA2 TMCI3 PO22 POE2#	CTS5# RTS5# SS5# MOSIA		
65		PA5	A5	TIOCB1 PO21	RSPCKA		
66		PA4	A4	MTIC5U MTCLKA TIOCA1 TMRI0 PO20	TXD5 SMOSI5 SSDA5 SSLA0	IRQ5-DS	
67		PA3	A3	MTIOC0D MTCLKD TIOCD0 TCLKB PO19	RXD5 SMISO5 SSCL5	IRQ6-DS	
68		PA2	A2	PO18	RXD5 SMISO5 SSCL5 SSLA3		
69		PA1	A1	MTIOC0B MTCLKC TIOCB0 PO17	SCK5 SSLA2	IRQ11	
70		PA0	A0/BC0#	MTIOC4A TIOCA0 PO16	SSLA1		
71		PE7	D15[A15/D15]	TIOCB11	MISOB	IRQ7	AN5
72		PE6	D14[A14/D14]	TIOCA11	MOSIB	IRQ6	AN4
73		PE5	D13[A13/D13]	MTIOC4C MTIOC2B TIOCB10	RSPCKB	IRQ5	AN3
74		PE4	D12[A12/D12]	MTIOC4D MTIOC1A TIOCA10 PO28	SSLB0		AN2

Table 1.10 List of Pins and Pin Functions (100-Pin LQFP) (5/5)

Pin Number	Power Supply Clock System Control	I/O Port	Bus	Timer (MTU, TPU, TMR, PPG, RTC, POE)	Communications (SCIC, SCID, RSPI, RIIC, CAN, IEB, USB)	Interrupt	S12AD, AD, DA
75		PE3	D11[A11/D11]	MTIOC4B TIOCB9 PO26 POE8#	CTS12# RTS12# SS12# MISOB		AN1
76		PE2	D10[A10/D10]	MTIOC4A TIOCA9 PO23	RXD12 SMISO12 SSCL12 RXDX12 SSLB3 MOSIB	IRQ7-DS	AN0
77		PE1	D9[A9/D9]	MTIOC4C TIOC49 PO18	TXD12 SMOSI12 SSDA12 TXDX12 SIOX12 SSLB2 RSPCKB		ANEX1
78		PE0	D8[A8/D8]	TIOC49	SCK12 SSLB1		ANEX0
79		PD7	D7[A7/D7]	MTIC5U POE0#	SSLC3	IRQ7	AN7
80		PD6	D6[A6/D6]	MTIC5V POE1#	SSLC2	IRQ6	AN6
81		PD5	D5[A5/D5]	MTIC5W POE2#	SSLC1	IRQ5	AN013
82		PD4	D4[A4/D4]	POE3#	SSLC0	IRQ4	AN012
83		PD3	D3[A3/D3]	TIOCB8 TCLKH POE8#		IRQ3	AN011
84		PD2	D2[A2/D2]	MTIOC4D TIOCA8	CRX0 ^{*1}	IRQ2	AN010
85		PD1	D1[A1/D1]	MTIOC4B TIOCB7 TCLKG	CTX0 ^{*2}	IRQ1	AN009
86		PD0	D0[A0/D0]	TIOCA7		IRQ0	AN008
87		P47				IRQ15-DS	AN007
88		P46				IRQ14-DS	AN006
89		P45				IRQ13-DS	AN005
90		P44				IRQ12-DS	AN004
91		P43				IRQ11-DS	AN003
92		P42				IRQ10-DS	AN002
93		P41				IRQ9-DS	AN001
94	VREFL0						
95		P40				IRQ8-DS	AN000
96	VREFH0						
97	AVCC0						
98		P07				IRQ15	ADTRG0
99	AVSS0						
100		P05				IRQ13	DA1

Note 1. Enabled only for the on-chip ROM capacity of 768 Kbytes or more

Note 2. The BCLK function is multiplexed with the I/O port function for pin P53, so the port function is not available if the external bus is enabled.