

Customer Notification

QB-78K0RFX3

In-Circuit Emulator

Operating Precautions

Target Devices
78K0R/Fx3 Series

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QB-78K0RFX3

(A) Table of Operating Precautions

No.	Outline	Control Code ^{Note}	QB-78K0RFX3		
			A	B	C
1	Self-programming support (Technical Limitation)		x	x	✓
2	Data Flash support (Technical Limitation)		x	x	o
3	AD Converter (Technical Limitation)		x	x	✓
4	Dead lock condition (Technical Limitation)		x	✓	✓
5	EEPROM emulation in Code Flash (Direction of use)		x	x	x

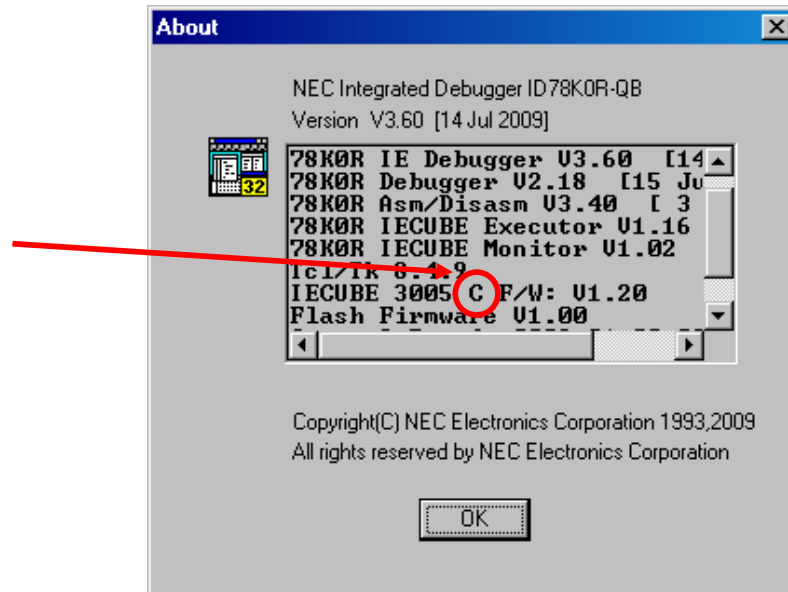
✓: Not applicable

x: applicable

o: Please refer to details in the description part

Notes:

- The "control code" is the second digit from the left in the 10-digit serial number in the warranty supplied with the product you purchased (if it has not been upgraded). If the product has been upgraded, a label indicating the new version is attached to the product and the x in V-UP LEVEL x on this label indicates the control code.
- The control code can be checked by selecting [About] from the [Help] menu when the ID78K0R-QB is running. "X" in version information "IECUBE **** X F/W **.***" is the control code.



(B) Description of Operating Precautions

No. 1	Self-programming support (Technical Limitation)
	<p><u>Details</u> Self-programming is not supported</p> <p><u>Workaround</u> Will be corrected in the version up.</p>
No. 2	Data Flash support (Technical Limitation)
	<p><u>Details</u> Data Flash is not supported</p> <p><u>Workaround</u> Will be corrected in the version up.</p> <p>Note: For QB-78K0RFX3 with Control Code “C” and later the access to the Data Flash area is supported (i.e. write, erase block, blank check read, ..), although the EEPROM Emulation and Data Flash Access library are still under development.</p>
No. 3	AD Converter (Technical Limitation)
	<p><u>Details</u> The following issues exists on the AD Converter: a) When the conversion time is short in SCAN mode the switching noise for the analog input selector will cause an inaccurate ADC result. b) Due to some high capacitance on the I/O Board, the ADC accuracy might become worse in High-Speed conversion mode</p> <p><u>Workaround</u> Will be corrected in the version up.</p>
No. 4	Dead lock condition (Technical Limitation)
	<p><u>Details</u> The CPU will went in a Dead lock when the following condition is met: When the STOP instruction is executed direct after clearing PLLON the CPU will enter in a dead lock.</p> <p><u>Workaround</u> Will be corrected in the version up.</p>
No. 5	EEPROM emulation in Code Flash (Direction of use)
	<p><u>Details</u> While performing EEPROM emulation in the Code Flash and using one of the following error emulation settings: ‘EEPROM_Write Error(Verify)’ or ‘FlashBlockIVerify Error(verify)’ In case an error occur, both errors will be generated.</p>

(C) Valid Specification

Item	Date published	Document No.	Document Title
1	December 2008 or later	ZUD-CD-08-0148 or later	Preliminary User's Manual
2	August 2009 or later	ZUD-CD-09-0139 or later	Preliminary User's Manual

(D) Revision History

Item	Date published	Document No.	Comment
1	February 9, 2009	U19675EE1V0IF00	1 st Release
2	September 21, 2009	U19675EE2V0IF00	1 st update (2 nd edition) - Corrections of description for item 1, 2 and 3 - Addition of item 4 and 5 - Addition of Control Code B and C