

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-H8*-A424A/E	Rev.	1.00
Title	Correction of the vector addresses on the table, Interrupt Sources, DTC Vector Addresses, and Corresponding DTCEs, in the data transfer controller (DTC)		Information Category	Technical Notification		
Applicable Product	H8S/20223, H8S/20235 Group	Lot No.	Reference Document	H8S/20103, H8S/20203, H8S/20223, H8S/20115, H8S/20215, H8S/20235 Group Hardware Manual (REJ09B0465-0200 Rev.2.00)		
		All lots				

We would like to inform you of the correction of the vector addresses on the table, Interrupt Sources, DTC Vector Addresses, and Corresponding DTCEs, in the data transfer controller (DTC).

- Correction

The table, Interrupt Sources, DTC Vector Addresses, and Corresponding DTCEs, is corrected as follows.

[Before correction]

Origin of Activation Source	Activation Source	Vector Number	Vector Address	DTCE	Priority
A/D converter unit 2	IADEND_2 (conversion completion)	32	H' 442 to H' 443	DTCEB5	
	IADCMP_2 (compare condition match)	33	H' 444 to H' 445	DTCEB4	

[After correction]

Origin of Activation Source	Activation Source	Vector Number	Vector Address	DTCE	Priority
A/D converter unit 2	IADEND_2 (conversion completion)	32	H' 440 to H' 441	DTCEB5	
	IADCMP_2 (compare condition match)	33	H' 442 to H' 443	DTCEB4	