

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

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Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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HITACHI SEMICONDUCTOR TECHNICAL UPDATE

Classification of Production	MCU&MPU		No	TN-SH7-407A/E	Rev	1
THEME	Correction of SH7616 Hardware manual	Classification of Information	<ul style="list-style-type: none"> 1. Spec change 2. Supplement of Documents 3. Limitation of Use 4. Change of Mask 5. Change of Production Line 			
PRODUCT NAME	HD6417616RF	Lot No.	Reference Documents	SH7616 Hardware Manual (ADE-602-243)Rev.1.0	Effective Date	
		ALL			Permanent	

There are errors in writing SH7616 Hardware Manual as follows.

1. Section 7 Bus State Controller (BSC)

7.5.11 64Mbit Synchronous DRAM (2Mword×32Bit)Connection : page321

<Error>

Synchronous DRAM Mode Settings:To make mode settings for the synchronous DRAM, write to address X+H'FFBF0000 or X+H'FFBF8000 from the CPU.(X represents the setting value.)
Whether to use X+H'FFBF0000 or X+H'FFBF8000 determines on the synchronous DRAM used.

<Correct>

Synchronous DRAM Mode Settings:To make mode settings for the synchronous DRAM, write to address X+H'FFFF0000 or X+H'FFFF8000 from the CPU.(X represents the setting value.)
Whether to use X+H'FFFF0000 or X+H'FFFF8000 determines on the synchronous DRAM used.

2. Section 14 Serial Communication Interface with FIFO (SCIF)

14.3.4 Operation in Synchronous Mode : page610

<Error>

In synchronous mode, the SCIF receives data in synchronization with the fall of the serial clock.

<Correct>

In synchronous mode, the SCIF receives data in synchronization with the rise of the serial clock.