Date: Dec. 13, 2024

RENESAS TECHNICAL UPDATE

TOYOSU FORESIA, 3-2-24, Toyosu, Koto-ku, Tokyo 135-0061, Japan
Renesas Electronics Corporation

Product Category	MPU/MCU		Document No.	TN-RA*-A0128A/E	Rev.	1.00
Title	Correction of Security Attribution Monitor Register		Information Category	Technical Notification		
Applicable Product	Each Group of RA4E1, RA4M2, RA4M3, RA6E1, RA6M4, RA6M5, RA6T2	Lot No. All	Reference Document	Refer the table 1		

Table 1 Reference Document List

No	Reference Document Name	Rev	Document Control Number
1	RA4E1 Group User's Manual Hardware	1.20	R01UH0929EJ0120
2	RA4M2 Group User's Manual Hardware	1.30	R01UH0892EJ0130
3	RA4M3 Group User's Manual Hardware	1.40	R01UH0893EJ0140
4	RA6E1 Group User's Manual Hardware	1.20	R01UH0930EJ0120
5	RA6M4 Group User's Manual Hardware	1.40	R01UH0890EJ0140
6	RA6M5 Group User's Manual Hardware	1.30	R01UH0891EJ0130
7	RA6T2 Group User's Manual Hardware	1.40	R01UH0951EJ0140

1. The changes to the RA4E1 microcontroller group are as follows.

1.1 42.5.6 CFSAMONA : Code Flash Security Attribution Monitor Register A

＜Current description＞ page 1380

Base address: PSCU = 0x400E_0000

Offset address: 0x18

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	CFS2[8:0]								—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	The value set by user								0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x18

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	CFS2[8:0]								—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0x1FF. It is set to the value written by your application.

1.2 42.5.7 CFSAMONB : Code Flash Security Attribution Monitor Register B

＜Current description＞ page 1380

Base address: PSCU = 0x400E_0000

Offset address: 0x1C

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	CFS1[13:0]														—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	The value set by user														0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x1C

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	CFS1[13:0]														—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0x3FFF. It is set to the value written by your application.

1.3 42.5.8 DFSAMON : Data Flash Security Attribution Monitor Register

＜Current description＞ page 1380

Base address: PSCU = 0x400E_0000

Offset address: 0x20

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	DFS[5:0]						—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	The value set by user						0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x20

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	DFS[5:0]						—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0x3F. It is set to the value written by your application.

1.4 42.5.9 SSAMONA : SRAM Security Attribution Monitor Register A

＜Current description＞ page 1381

Base address: PSCU = 0x400E_0000

Offset address: 0x24

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	SS2[7:0]									—	—	—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	The value set by user									0	0	0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x24

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	SS2[7:0]									—	—	—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	*1	*1		0	0	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0xFF. It is set to the value written by your application.

1.5 42.5.10 SSAMONB : SRAM Security Attribution Monitor Register B

＜Current description＞ page 1381

Base address: PSCU = 0x400E_0000

Offset address: 0x28

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	SS1[10:0]											—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	The value set by user											0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x28

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	SS1[10:0]											—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0x7FF. It is set to the value written by your application.

2. The changes to the RA4M2 microcontroller group are as follows.

2.1 46.5.6 CFSAMONA : Code Flash Security Attribution Monitor Register A

＜Current description＞ page 1606

Base address: PSCU = 0x400E_0000

Offset address: 0x18

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	CFS2[8:0]								—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	The value set by user								0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x18

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	CFS2[8:0]								—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0x1FF. It is set to the value written by your application.

2.2 46.5.7 CFSAMONB : Code Flash Security Attribution Monitor Register B

＜Current description＞ page 1607

Base address: PSCU = 0x400E_0000

Offset address: 0x1C

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	CFS1[13:0]														—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	The value set by user														0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x1C

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	CFS1[13:0]														—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0x3FFF. It is set to the value written by your application.

2.3 46.5.8 DFSAMON : Data Flash Security Attribution Monitor Register

＜Current description＞ page 1607

Base address: PSCU = 0x400E_0000

Offset address: 0x20

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	DFS[5:0]						—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	The value set by user						0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x20

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	DFS[5:0]						—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0x3F. It is set to the value written by your application.

2.4 46.5.9 SSAMONA : SRAM Security Attribution Monitor Register A

＜Current description＞ page 1607

Base address: PSCU = 0x400E_0000

Offset address: 0x24

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	SS2[7:0]								—	—	—	—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	The value set by user								0	0	0	0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x24

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	SS2[7:0]								—	—	—	—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0xFF. It is set to the value written by your application.

2.5 46 .5.10 SSAMONB : SRAM Security Attribution Monitor Register B

＜Current description＞ page 1608

Base address: PSCU = 0x400E_0000

Offset address: 0x28

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	SS1[10:0]										—	—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	The value set by user										0	0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x28

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	SS1[10:0]										—	—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0x7FF. It is set to the value written by your application.

3. The changes to the RA4M3 microcontroller group are as follows.

3.1 46.5.6 CFSAMONA : Code Flash Security Attribution Monitor Register A

＜Current description＞ page 1635

Base address: PSCU = 0x400E_0000

Offset address: 0x18

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	CFS2[8:0]								—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	The value set by user								0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x18

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	CFS2[8:0]								—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0x1FF. It is set to the value written by your application.

3.2 46.5.7 CFSAMONB : Code Flash Security Attribution Monitor Register B

＜Current description＞ page 1636

Base address: PSCU = 0x400E_0000

Offset address: 0x1C

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	CFS1[13:0]														—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	The value set by user														0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x1C

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	CFS1[13:0]														—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0x3FFF. It is set to the value written by your application.

3.3 46.5.8 DFSAMON : Data Flash Security Attribution Monitor Register

＜Current description＞ page 1636

Base address: PSCU = 0x400E_0000

Offset address: 0x20

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	DFS[5:0]						—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	The value set by user						0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x20

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	DFS[5:0]						—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0x3F. It is set to the value written by your application.

3.4 46.5.9 SSAMONA : SRAM Security Attribution Monitor Register A

＜Current description＞ page 1636

Base address: PSCU = 0x400E_0000

Offset address: 0x24

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	SS2[7:0]													—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	The value set by user													0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x24

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	SS2[7:0]													—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0xFF. It is set to the value written by your application.

3.5 46.5.10 SSAMONB : SRAM Security Attribution Monitor Register B

＜Current description＞ page 1637

Base address: PSCU = 0x400E_0000

Offset address: 0x28

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	SS1[10:0]													—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	The value set by user													0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x28

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	SS1[10:0]													—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0x7FF. It is set to the value written by your application.

4. The changes to the RA6E1 microcontroller group are as follows.

4.1 46.6.6 CFSAMONA : Code Flash Security Attribution Monitor Register A

＜Current description＞ page 1628

Base address: PSCU = 0x400E_0000

Offset address: 0x18

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	CFS2[8:0]								—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	The value set by user								0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x18

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	CFS2[8:0]								—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0x1FF. It is set to the value written by your application.

4.2 46.6.7 CFSAMONB : Code Flash Security Attribution Monitor Register B

＜Current description＞ page 1628

Base address: PSCU = 0x400E_0000

Offset address: 0x1C

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	CFS1[13:0]														—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	The value set by user														0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x1C

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	CFS1[13:0]														—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0x3FFF. It is set to the value written by your application.

4.3 46.6.8 DFSAMON : Data Flash Security Attribution Monitor Register

＜Current description＞ page 1629

Base address: PSCU = 0x400E_0000

Offset address: 0x20

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	DFS[5:0]						—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	The value set by user						0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x20

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	DFS[5:0]						—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0x3F. It is set to the value written by your application.

4.4 46.6.9 SSAMONA : SRAM Security Attribution Monitor Register A

＜Current description＞ page 1629

Base address: PSCU = 0x400E_0000

Offset address: 0x24

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	SS2[7:0]																				
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	The value set by user											0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x24

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	SS2[7:0]																				
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0xFF. It is set to the value written by your application.

4.5 46.6.10 SSAMONB : SRAM Security Attribution Monitor Register B

＜Current description＞ page 1629

Base address: PSCU = 0x400E_0000

Offset address: 0x28

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	SS1[10:0]																				
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	The value set by user											0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x28

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	SS1[10:0]																				
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0x7FF. It is set to the value written by your application.

5. The changes to the RA6M4 microcontroller group are as follows.

5.1 49.6.6 CFSAMONA : Code Flash Security Attribution Monitor Register A

＜Current description＞ page 1813

Base address: PSCU = 0x400E_0000

Offset address: 0x18

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	CFS2[8:0]								—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	The value set by user								0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x18

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	CFS2[8:0]								—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0x1FF. It is set to the value written by your application.

5.2 49.6.7 CFSAMONB : Code Flash Security Attribution Monitor Register B

＜Current description＞ page 1813

Base address: PSCU = 0x400E_0000

Offset address: 0x1C

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	CFS1[13:0]														—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	The value set by user														0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x1C

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	CFS1[13:0]														—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0x3FFF. It is set to the value written by your application.

5.3 49.6.8 DFSAMON : Data Flash Security Attribution Monitor Register

＜Current description＞ page 1813

Base address: PSCU = 0x400E_0000

Offset address: 0x20

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	DFS[5:0]						—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	The value set by user						0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x20

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	DFS[5:0]						—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0x3F. It is set to the value written by your application.

5.4 49.6.9 SSAMONA : SRAM Security Attribution Monitor Register A

＜Current description＞ page 1814

Base address: PSCU = 0x400E_0000

Offset address: 0x24

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	SS2[7:0]																				
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	The value set by user																				

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x24

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	SS2[7:0]																				
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0xFF. It is set to the value written by your application.

5.5 49.6.10 SSAMONB : SRAM Security Attribution Monitor Register B

＜Current description＞ page 1814

Base address: PSCU = 0x400E_0000

Offset address: 0x28

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	SS1[10:0]																				
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	The value set by user																				

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x28

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	SS1[10:0]																				
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0x7FF. It is set to the value written by your application.

6. The changes to the RA6M5 microcontroller group are as follows.

6.1 52.6.6 CFSAMONA : Code Flash Security Attribution Monitor Register A

＜Current description＞ page 2175

Base address: PSCU = 0x400E_0000

Offset address: 0x18

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	CFS2[8:0]								—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	The value set by user								0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x18

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	CFS2[8:0]								—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0x1FF. It is set to the value written by your application.

6.2 52.6.7 CFSAMONB : Code Flash Security Attribution Monitor Register B

＜Current description＞ page 2175

Base address: PSCU = 0x400E_0000

Offset address: 0x1C

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	CFS1[13:0]														—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	The value set by user														0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x1C

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	CFS1[13:0]														—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0x3FFF. It is set to the value written by your application.

6.3 52.6.8 DFSAMON : Data Flash Security Attribution Monitor Register

＜Current description＞ page 2176

Base address: PSCU = 0x400E_0000

Offset address: 0x20

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	DFS[5:0]						—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	The value set by user						0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x20

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	DFS[5:0]						—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0x3F. It is set to the value written by your application.

6.4 52.6.9 SSAMONA : SRAM Security Attribution Monitor Register A

＜Current description＞ page 2176

Base address: PSCU = 0x400E_0000

Offset address: 0x24

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	SS2[7:0]								—	—	—	—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	The value set by user								0	0	0	0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x24

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	SS2[7:0]								—	—	—	—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0xFF. It is set to the value written by your application.

6.5 52.6.10 SSAMONB : SRAM Security Attribution Monitor Register B

＜Current description＞ page 2176

Base address: PSCU = 0x400E_0000

Offset address: 0x28

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	SS1[10:0]										—	—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	The value set by user										0	0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x28

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	SS1[10:0]										—	—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0x7FF. It is set to the value written by your application.

7. The changes to the RA6T2 microcontroller group are as follows.

7.1 45.5.6 CFSAMONA : Code Flash Security Attribution Monitor Register A

＜Current description＞ page 1711

Base address: PSCU = 0x400E_0000

Offset address: 0x18

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	CFS2[8:0]								—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	The value set by user								0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x18

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	CFS2[8:0]								—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0x1FF. It is set to the value written by your application.

7.2 45.5.7 CFSAMONB : Code Flash Security Attribution Monitor Register B

＜Current description＞ page 1711

Base address: PSCU = 0x400E_0000

Offset address: 0x1C

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	CFS1[13:0]														—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	The value set by user														0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x1C

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	CFS1[13:0]														—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0x3FFF. It is set to the value written by your application.

7.3 45.5.8 DFSAMON : Data Flash Security Attribution Monitor Register

＜Current description＞ page 1711

Base address: PSCU = 0x400E_0000

Offset address: 0x20

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	DFS[5:0]						—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	The value set by user						0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x20

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	DFS[5:0]						—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0x3F. It is set to the value written by your application.

7.4 45.5.9 SSAMONA : SRAM Security Attribution Monitor Register A

＜Current description＞ page 1712

Base address: PSCU = 0x400E_0000

Offset address: 0x24

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	SS2[7:0]								—	—	—	—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	The value set by user								0	0	0	0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x24

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	SS2[7:0]								—	—	—	—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0xFF. It is set to the value written by your application.

7.5 45.5.10 SSAMONB : SRAM Security Attribution Monitor Register B

＜Current description＞ page 1712

Base address: PSCU = 0x400E_0000

Offset address: 0x28

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	SS1[10:0]										—	—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	The value set by user										0	0	0	0	0	0	0	0	0	0	0

＜Changed description＞

Base address: PSCU = 0x400E_0000

Offset address: 0x28

Bit position:	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Bit field:	—	—	—	—	—	—	—	—	—	—	—	SS1[10:0]										—	—	—	—	—	—	—	—	—	—	—
Value after reset:	0	0	0	0	0	0	0	0	0	0	0	*1	*1	*1	*1	*1	*1	*1	*1	*1	*1	0	0	0	0	0	0	0	0	0	0	0

Note 1. The value in a blank product is 0x7FF. It is set to the value written by your application.

- That's all -