

# RENESAS TECHNICAL UPDATE

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 Renesas Electronics Corporation

Product Category	MPU/MCU	Document No.	TN-RA*-A0130A/E	Rev.	1.00
Title	Correction of LOCOUTCR description		Information Category	Technical Notification	
Applicable Product	RA2A2 Group	Lot No.	Reference Document	RA2A2 Group User's Manual: Hardware Rev.1.20 (R01UH1005EJ0120)	
		All			

Correction of 9.2.24 LOCOUTCR : LOCO User Trimming Control Register description.

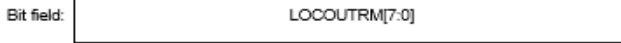
(Before)

## 9.2.24 LOCOUTCR : LOCO User Trimming Control Register

Base address: SYSC = 0x4001\_E000

Offset address: 0x492

Bit position: 7 6 5 4 3 2 1 0



Value after reset: 0 0 0 0 0 0 0 0

Bit	Symbol	Function	R/W
7:0	LOCOUTRM[7:0]	LOCO User Trimming 0xE8: -8 0xE9: -7 ⋮ 0xFF: -1 0x00: 0 0x01: +1 ⋮ 0x06: +6 0x07: +7	R/W

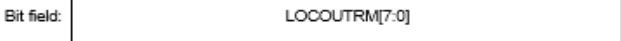
(After)

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