

# RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RL*-A0097A/E	Rev.	1.00
Title	Correction for Incorrect Description Notice RL78/G13A Descriptions in the User's Manual: Hardware Rev. 1.00 Changed		Information Category	Technical Notification		
Applicable Product	RL78/G13A Group	Lot No.	Reference Document	RL78/G13A User's Manual: Hardware Rev. 1.00 R01UH0856EJ0100 (Mar. 2020)		
		All lots				

This document describes misstatements found in the RL78/G13A User's Manual: Hardware Rev. 1.00 (R01UH0856EJ0100).

## Corrections

Applicable Item	Applicable Page	Contents
Figure 21-4. Format of User Option Byte (000C1H/010C1H) (2/2)	Page 763	Incorrect descriptions revised
Figure 24-2. Format of User Option Byte (000C1H/010C1H) (2/2)	Page 802	Incorrect descriptions revised

## Document Improvement

The above corrections will be made for the next revision of the User's Manual: Hardware.

Corrections in the User's Manual: Hardware

No.	Corrections and Applicable Items			Pages in this document for corrections
	Document No.	English	R01UH0856EJ0100	
1	Figure 21-4. Format of User Option Byte (000C1H/010C1H) (2/2)		Page 763	Page 3
2	Figure 24-2. Format of User Option Byte (000C1H/010C1H) (2/2)		Page 802	Page 4

**Incorrect:** **Bold with underline:** **Correct:** Gray hatched

**Revision History**

RL78/G13A Correction for incorrect description notice

Document Number	Issue Date	Description
TN-RL*-A0097A/E	Apr. 22, 2020	First edition issued Corrections No.1 to No.2 revised (this document)

1. **CHAPTER 21 VOLTAGE DETECTOR**

**Figure 21-4. Format of User Option Byte (000C1H/010C1H) (2/2)**

Incorrect:

- LVD off (use of external reset input via  $\overline{\text{RESET}}$  pin)

Detection voltage		Option byte setting value						
$V_{\text{LVD}}$		VPOC2	VPOC1	VPOC0	LVIS1	LVIS0	Mode setting	
Rising edge	Falling edge						LVIMDS1	LVIMDS0
-	-	1	⊗	⊗	⊗	⊗	⊗	1
-		Setting of values other than above is prohibited.						

Correct:

- LVD off (use of external reset input via  $\overline{\text{RESET}}$  pin)

Detection voltage		Option byte setting value						
$V_{\text{LVD}}$		VPOC2	VPOC1	VPOC0	LVIS1	LVIS0	Mode setting	
Rising edge	Falling edge						LVIMDS1	LVIMDS0
-	-	1	1	1	0	0	1	1
-		Setting of values other than above is prohibited.						

2. **CHAPTER 24 OPTION BYTE**

**Figure 24-2. Format of User Option Byte (000C1H/010C1H) (2/2)**

Incorrect:

- LVD off (use of external reset input via  $\overline{\text{RESET}}$  pin)

Detection voltage		Option byte setting value						
V <sub>LVD</sub>		VPOC2	VPOC1	VPOC0	LVIS1	LVIS0	Mode setting	
Rising edge	Falling edge						LVIMDS1	LVIMDS0
-	-	1	⊗	⊗	⊗	⊗	⊗	1
-		Setting of values other than above is prohibited.						

Correct:

- LVD off (use of external reset input via  $\overline{\text{RESET}}$  pin)

Detection voltage		Option byte setting value						
V <sub>LVD</sub>		VPOC2	VPOC1	VPOC0	LVIS1	LVIS0	Mode setting	
Rising edge	Falling edge						LVIMDS1	LVIMDS0
-	-	1	1	1	0	0	1	1
-		Setting of values other than above is prohibited.						