

## HITACHI MICROCOMPUTER TECHNICAL UPDATE

DATE	7 November 2001	No.	TN-SH7-375A/E
THEME	Correction of errors on SH3, SH3-DSP hardware manual (IOIS16 etc.)		
CLASSIFICATION	<input type="checkbox"/> Spec change <input checked="" type="checkbox"/> Supplement of Documents		<input type="checkbox"/> Limitation on Use
PRODUCTNAME	SH7709A,SH7729		
REFERENCE DOCUMENTS	SH7729 Hardware manual SH7709A Hardware manual	Effective Date	Permanent
		Lot No.:	All
<p>1. SH7709A ADE-602-187D Rev.5.0: p.14 SH7729 ADE-602-157C Rev.4.0: p.14</p> <p>Description of IOIS16/PTG[7] pin</p> <p>[Error] area 6 16-bit input /output / input port G</p> <p>[Correction] IOIS16 (PCMCIA) / input port G</p> <p>2. SH7709A ADE-602-187D Rev.5.0: p.341 SH7729 ADE-602-157C Rev.4.0: p.389</p> <p>[Error] When a 16-bit bus width is set for area 6, if the IOIS16 signal is high during a word-size I/O bus cycle, the I/O port is recognized as being 8 bits in width.</p> <p>[Correction] When a 16-bit bus width is set for area 5 or area 6, if the IOIS16 signal is high during a word-size I/O bus cycle, the I/O port is recognized as being 8 bits in width.</p> <p>3. SH7709A ADE-602-187D Rev.5.0: p.657 SH7729 ADE-602-157C Rev.4.0: p.717</p> <p>Description of TMS</p> <p>[Error] IEEE Std. 1140.1</p> <p>[Correction] IEEE Std. 1149.1</p>			