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CUSTOMER NOTIFICATION  Bug When Writing to $\mu$ PD78F9850 Flash Memory  Bug Information	WBB-T-01260
	May 29, 2001
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Thank you for your continued support of NEC products.

This notice is to inform customers of the following bug.

We sincerely apologize for any inconvenience this may cause.

1. Affected products

$\mu$ PD78F9850MC-5A4

2. Descriptions

- Details of bug

If the maximum communication speed allowed for UART communication (76800 bps) is used (under the conditions below) when writing to the above product using a flash memory programming device (FL-PR2, FL-PR3, etc.), the message 'Target is closed' will be displayed and the operation will not complete normally. (i.e. Writing cannot be performed.)

Supplement)

- This bug occurs only in the flash memory programming mode; it does not occur in the normal operating mode.

Condition)

- When the setting of the programming device is as follows:

'CPU CLOCK': 4.0 to 4.7 MHz<sup>\* 1</sup>

'COMM PORT': UART selected

'UART BPS': 76800 bps

\* 1: Applicable both when the CPU clock is supplied from the programming device and when it is supplied from the target.

- Workaround

Write to the flash memory under conditions other than the one above.

(Please consider using SIO communication or UART communication at a speed other than 76800 bps.)

## 2. Future support

This bug was caused by the firmware (incorporated in the target product) that was controlling flash memory writing, etc.

NEC is planning to supply corrected products (samples) from the following date.

From the beginning of December 2001