To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

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RENESAS TECHNICAL UPDATE

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Renesas Technology Corp.

Product Category	MPU&MCU		Document No.	TN-SH7-A690A/E	Rev.	1.00
Title	Amendments of sending a break signal of a SCIF of a hardware manual		Information Category	Technical Notification		
Applicable Product	SH7727 Group SH7709 Group SH7709A Group SH7709S Group SH7729 Group SH7729R Group SH7706 Group	Lot No. All	Reference Document	SH7727 hardware manual Rev5.00 (REJ09B0254-0500) SH7709 hardware manual Rev2.00 (ADE-602-123B) SH7709A hardware manual Rev5.00 (ADE-602-187D) SH7709S hardware manual Rev5.00 (REJ09B0081-0500) SH7729 hardware manual Rev4.00 (ADE-602-157C) SH7729R hardware manual Rev5.00 (REJ09B0091-0500) SH7706 hardware manual Rev5.00 (REJ09B0146-0500)		

There are the amendments of sending a break signal of a SCIF of the SH7727, the SH7709, the SH7709A, the SH7709S, the SH7729, the SH7729R, and the SH7706 hardware manual.

1. SH7727 hardware manual Page 601 19.5.4 Sending a Break Signal
2. SH7709 hardware manual Page 515 16.5.4 Sending a Break Signal
3. SH7709A hardware manual Page 573 16.5.4 Sending a Break Signal
4. SH7709S hardware manual Page 551 16.5.4 Sending a Break Signal
5. SH7729 hardware manual Page 627 17.5.4 Sending a Break Signal
6. SH7729R hardware manual Page 595 17.5.4 Sending a Break Signal

Original:

then set the SCP4MD0 and SCP4MD1 bits to 0 and 1, respectively, and finally clear the TE bit to 0(halting transmission).

Amended:

then set the SCP4MD0 and SCP4MD1 bits to 1 and 0, respectively, and finally clear the TE bit to 0(halting transmission).

7. SH7706 hardware manual Page 481 16.5.4 Sending a Break Singal

Original:

then set the SCP2MD0 and SCP2MD1 bits to 0 and 1, respectively, and finally clear the TE bit to 0(halting transmission).

Amended:

then set the SCP2MD0 and SCP2MD1 bits to 1 and 0, respectively, and finally clear the TE bit to 0(halting transmission).