

RENESAS TECHNICAL UPDATE

TOYOSU FORESIA, 3-2-24, Toyosu, Koto-ku, Tokyo 135-0061, Japan
Renesas Electronics Corporation

Product Category	MPU/MCU		Document No.	TN-RX*-A0288A/E	Rev.	1.00
Title	Amendments to Outline of Specification Regarding the SD Host Interface (SDHI)		Information Category	Technical Notification		
Applicable Product	RX231 Group RX23W Group	Lot No. All	Reference Document	RX230 Group, RX231 Group User's Manual: Hardware Rev.1.20 (R01UH0496EJ0120) RX23W Group User's Manual: Hardware Rev.1.20 (R01UH0823EJ0120)		

This document describes corrections to Outline of Specifications regarding the SDHI in the User's Manual: Hardware for the applicable products stated above.

• Page 59 of 1966 for RX231, Page 55 of 1861 for RX23W

The descriptions of the SD host interface (SDHIa) in Table 1.1, Outline of Specifications (4/4), under section 1.1, Outline of Specifications, are corrected as follows.

Before correction

Table 1.1 Outline of Specifications (4/4)

Classification	Module/Function	Description
Communication functions	SD Host Interface (SDHIa)	<ul style="list-style-type: none"> 1 channel Transfer speed : Default speed mode (8MB/s) SD memory card interface (1 bit / 4bits SD bus) MMC, eMMC Backward-compatible are supported. SD Specifications <ul style="list-style-type: none"> Part 1: Compliant with Physical Layer Specification Ver.3.01 (Not support DDR) Part E1: SDIO Specification Ver. 3.00 Error check function: CRC7 (command), CRC16 (data) Interrupt Source: Card access interrupt, SDIO access interrupt, Card detection interrupt, SD buffer access interrupt DMA transfer sources: SD_BUF write, SD_BUF read Card detection, Write protection

After correction

Table 1.1 Outline of Specifications (4/4)

Classification	Module/Function	Description
Communication functions	SD Host Interface (SDHIa)	<ul style="list-style-type: none"> 1 channel Transfer speed : Default speed mode (8 MB/s) Interface for the SD memory or SDIO cards (supporting 1- and 4-bit SD bus) SD Specifications <ul style="list-style-type: none"> Part 1: Compliant with Physical Layer Specification Ver.3.01 (Not support DDR) Part E1: SDIO Specification Ver. 3.00 Error check function: CRC7 (command), CRC16 (data) Interrupt Source: Card access interrupt, SDIO access interrupt, Card detection interrupt, SD buffer access interrupt DMA transfer sources: SD_BUF write, SD_BUF read Card detection, Write protection