

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

Send any inquiries to <http://www.renesas.com/inquiry>.

RENESAS TECHNICAL UPDATE

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Renesas Technology Corp.

Product Category	MPU & MCU		Document No.	TN-450-A010A/E	Rev.	1.00
Title	455A Group Correction of the Misprinted Electrical Characteristics in Datasheet and Customer's Std. Specifications (High-/Low-Speed On-Chip Oscillator Frequencies)		Information Category	Technical Notification		
Applicable Product	M3455AG8FP	Lot No.	Reference Document	455A Group Datasheet 455A Group Customer's Std. Spec.		
	M3455AG8-XXXFP M3455AGCFP M3455AGC-XXXFP	N/A				

Please be advised that the datasheet and customer's std. specifications of the above products have been revised due to corrections made to the on-chip oscillator frequency characteristics.

1. Applicable Datasheet and Customer's Std. Specifications

Datasheet	455A Group Datasheet (REJ03B0224-0101)
Customer's std. spec.	GNOK-M3455AG8FP-00 / GNOK-M3455AG8-XXXFP-00 GNOK-M3455AGCFP-00 / GNOK-M3455AGC-XXXFP-00

2. Details

The table below shows the revised electrical characteristics (high-/low-speed on-chip oscillator frequencies).

Pre-revision (Incorrect)

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
f(HSOCO)	High-speed on-chip oscillator clock Frequency	VDD = 3.0 V	200	500	700	kHz
f(LSOCO)	Low-speed on-chip oscillator clock Frequency	VDD = 3.0 V	20	50	70	kHz

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Revised (Correct)

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
f(HSOCO)	High-speed on-chip oscillator clock Frequency	VDD = 3.0 V	200	500	800	kHz
f(LSOCO)	Low-speed on-chip oscillator clock frequency	VDD = 3.0 V	20	50	80	kHz

* Min. and Typ. are not changed.

3. Revision of Datasheet and Customer's Std. Spec.

The 455A Group Datasheet Rev.1.01 had been reissued with the revisions as Rev.1.02.

The revised values in this notification are now the assumed values for specifications already issued.

These corrections only apply to the English datasheet and customer std. specification. No revisions were made to the Japanese datasheet and customer std. specification.

If revised customer's std. specification is required, please contact our sales division.