

SLG7LI

GreenPAK™ Logic IC Standard Designs

Description

[SLG7LI](#) is a collection of Renesas GreenPAK logic IC standard designs. The collection provides pre-configured implementations of common digital control, timing, gating, signal-conditioning, and interface-support functions.

Available designs include glue logic, logic gates, clock and delay functions, startup logic, multiplexing, platform-control logic, and application-specific digital functions.

Every SLG7LI standard design can be further customized to meet specific customer requirements, including truth tables, timing, polarity, signal count, and package selection.

Full datasheets for individual standard designs are available upon request. Contact greenpak@renesas.com.

Features

- Pre-configured digital logic designs
- Glue logic, gating, timing, and multiplexing options
- Programmable logic and delay functions
- Integration of multiple discrete logic functions
- Compact package choices
- Ready for evaluation and further customization

Applications

- Discrete logic replacement and consolidation
- Platform and board-control logic
- Clock, delay, and pulse-generation functions
- Embedded, computing, and industrial systems

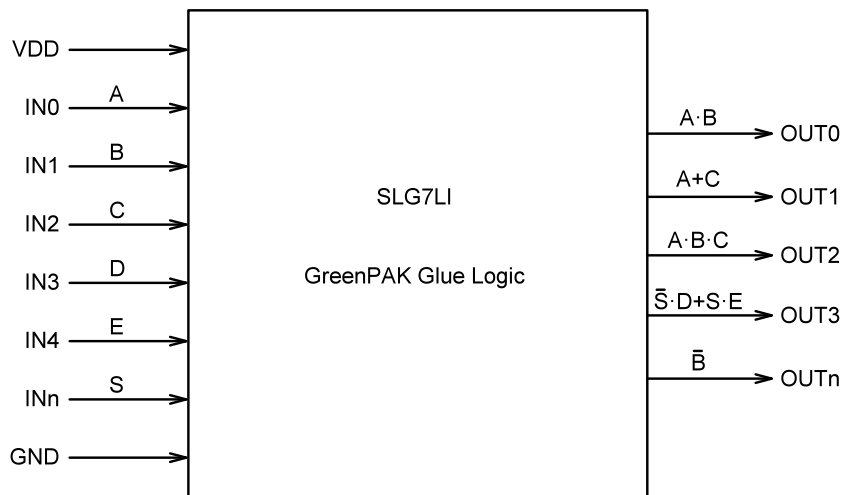


Figure 1. SLG7LI Logic IC – Typical Application Block Diagram

The complete, up-to-date list of available standard designs is published in the Product Options section of the SLG7LI product page. Each design is ready for evaluation and can be further customized to meet specific customer requirements. Individual datasheets are available on request via greenpak@renesas.com.