

Tsi352 RDK1 V1.0 : 2-Port PCI-PCI Bridge Evaluation Board

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Right Angle PCI Connector Page 7

TITLE : TSI352 RDK1 V1.0

|      |                  |          |            |
|------|------------------|----------|------------|
| SIZE | DRAWING NUMBER : | Version: | DESIGNER : |
| C    | 60D6000_SC001    | Rev 0    | IDT        |

|                      |       |   |    |   |
|----------------------|-------|---|----|---|
| LAST MODIFIED DATE : | SHEET | 1 | OF | 8 |
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## E

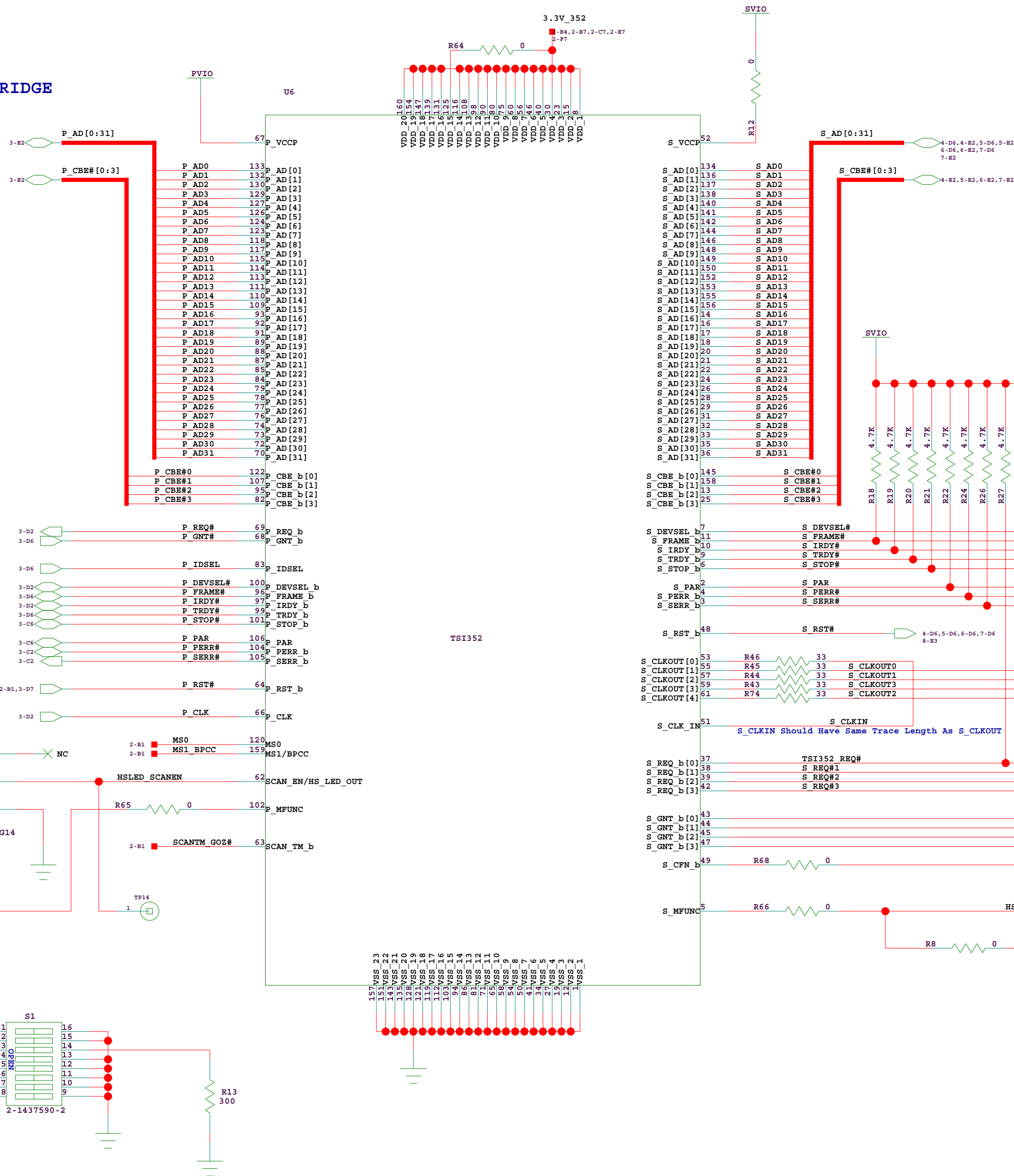
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## TSI352 PCI-to-PCI BRIDGE



## S1 Switch Setting

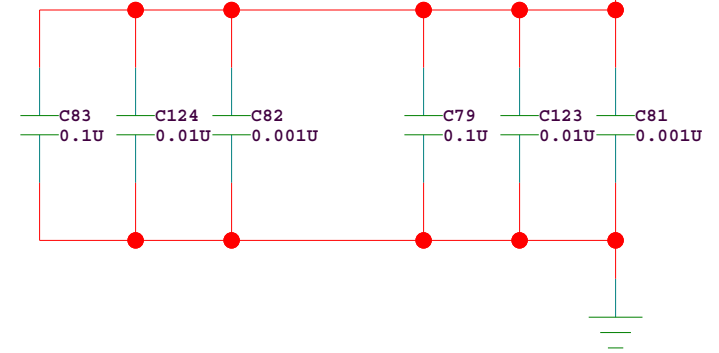
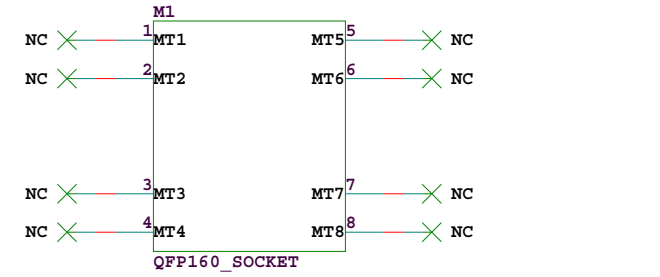
| BIT | Signal              | ON              | OFF            | DEFAULT |
|-----|---------------------|-----------------|----------------|---------|
| 1   | MS0:                | 0: NO-LOCK      | 1: LOCK MODE   | ON      |
| 2   | MS1/BPCC:           | SEE TABLE       | MS[0:1]        | ON      |
| 3   | SCANTM_GOZ#:        | TEST MODE       | Normal         | OFF     |
| 4   | ENUM_PLOCK_PCLKRUN# | TEST MODE       | Normal         | OFF     |
| 5   | HSSW_SLOCK_SCLKRUN# | HS_SWITCH CLOSE | HS_SWITCH OPEN | OFF     |
| 6   | P_RST#:             | TEST MODE       | Normal         | OFF     |
| 7   | S_CFN_ARB#          | INT-ARBITER     | EXT-ARBITER    | ON      |
| 8   | PCI_M66EN           | 33MHz           | 66MHz          | OFF     |

### MS[0:1] Setting Table

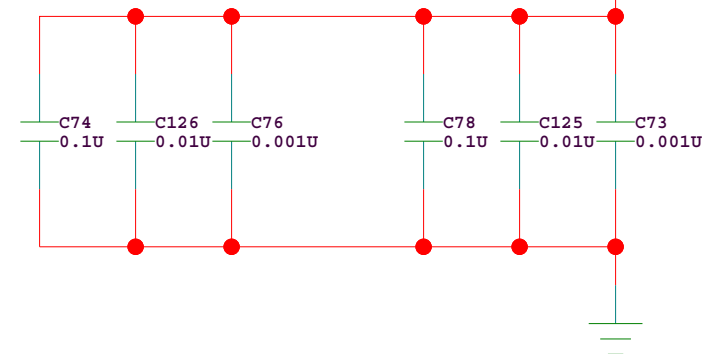
| MS0 MS1 | P_MFUNC                                                  | S_MFUNC    | MODE      |
|---------|----------------------------------------------------------|------------|-----------|
| 0 0     | HS_ENUM#                                                 | HS_SWITCH# | Hot Swap  |
| 0 1     | P_CLKRUN#                                                | S_CLKRUN#  | Clock Run |
| 1 BPCC  | P_LOCK#                                                  | S_LOCK#    | PCI Lock  |
| BPCC    | 1: Enable Power Management. 0: Disable Power Management. |            |           |

Note: Separate 3.3V from Tsi352 for the current measurement

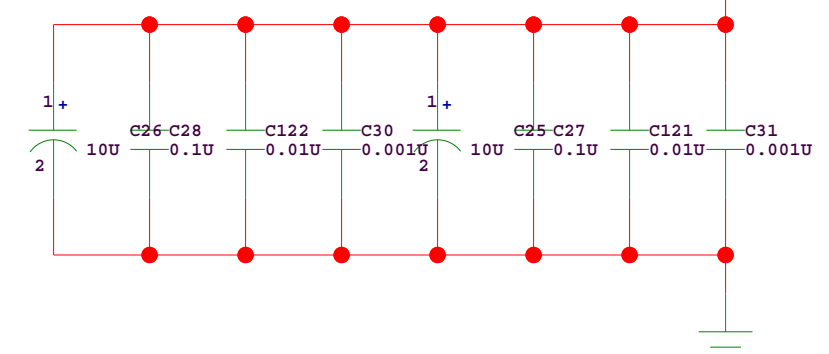
## TSI352 QFP160 SOCKET



Place these 4 sets of caps on bottom layer around Tsi352 corners



Place these 4 sets of caps on top layer around Tsi352 corners



**TITLE :**

TSI352: 2-PORT PCI-PCI BRIDGE

## SIZE

DRAWING NUMBER :

Version:

DESIGNER :

LAST MODIFIED DATE :

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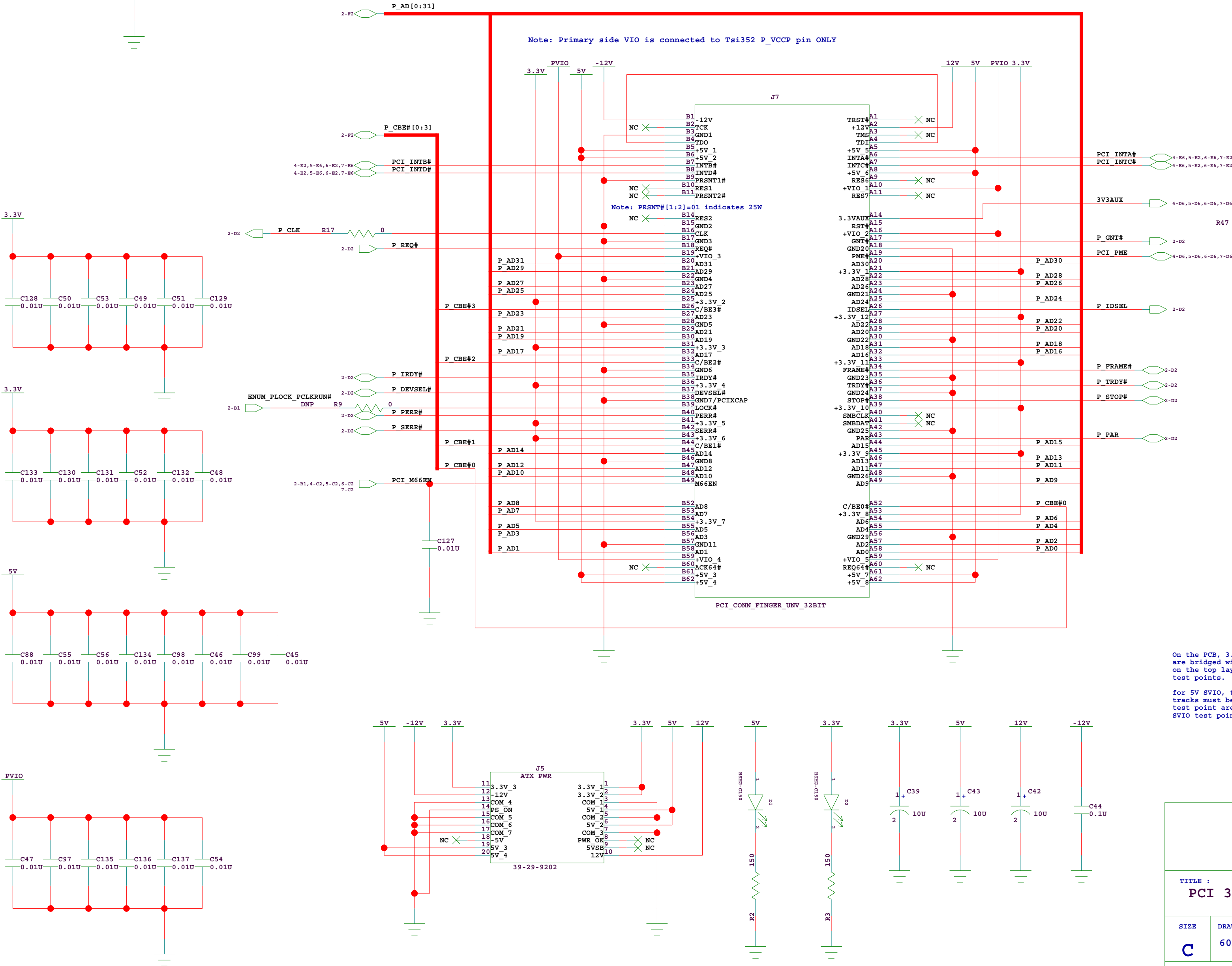
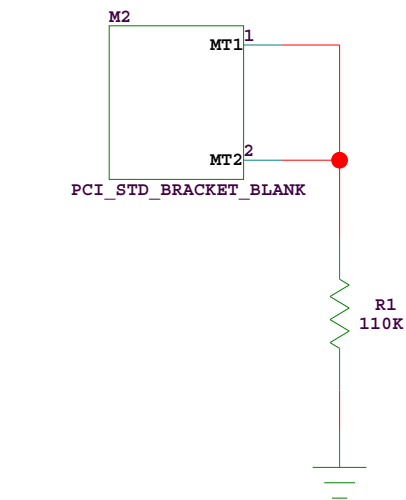
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PCI 32bit Universal Finger Edge Connector  
To Tsi352's primary interface



|                                                      |                                   |                 |                   |
|------------------------------------------------------|-----------------------------------|-----------------|-------------------|
| TITLE :<br>PCI 32BIT UNIVERSAL FINGER EDGE CONNECTOR |                                   |                 |                   |
| SIZE<br>C                                            | DRAWING NUMBER :<br>60D6000_SC001 | Version:        | DESIGNER :<br>IDT |
| LAST MODIFIED DATE :<br>7-23-2007_16:56              |                                   | SHEET<br>3 OF 8 |                   |

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PCI 3.3V 32Bits Slot 1 Thru-hole Connector

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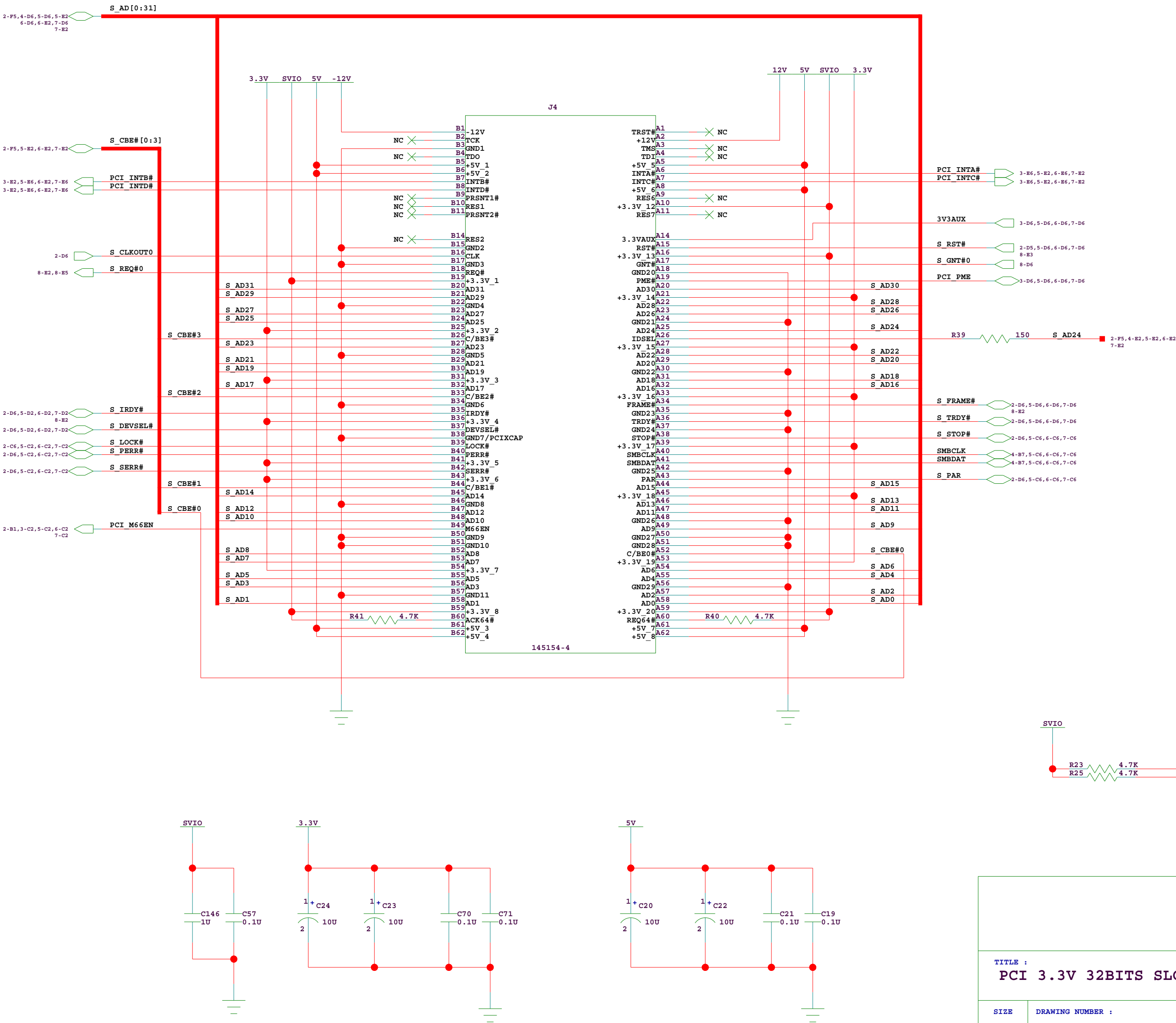
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TITLE :  
PCI 3.3V 32BITS SLOT 1 THRU-HOLE CONNECTOR

| SIZE | DRAWING NUMBER : | Version: | DESIGNER : |
|------|------------------|----------|------------|
| C    | 60D6000_SC001    |          | IDT        |

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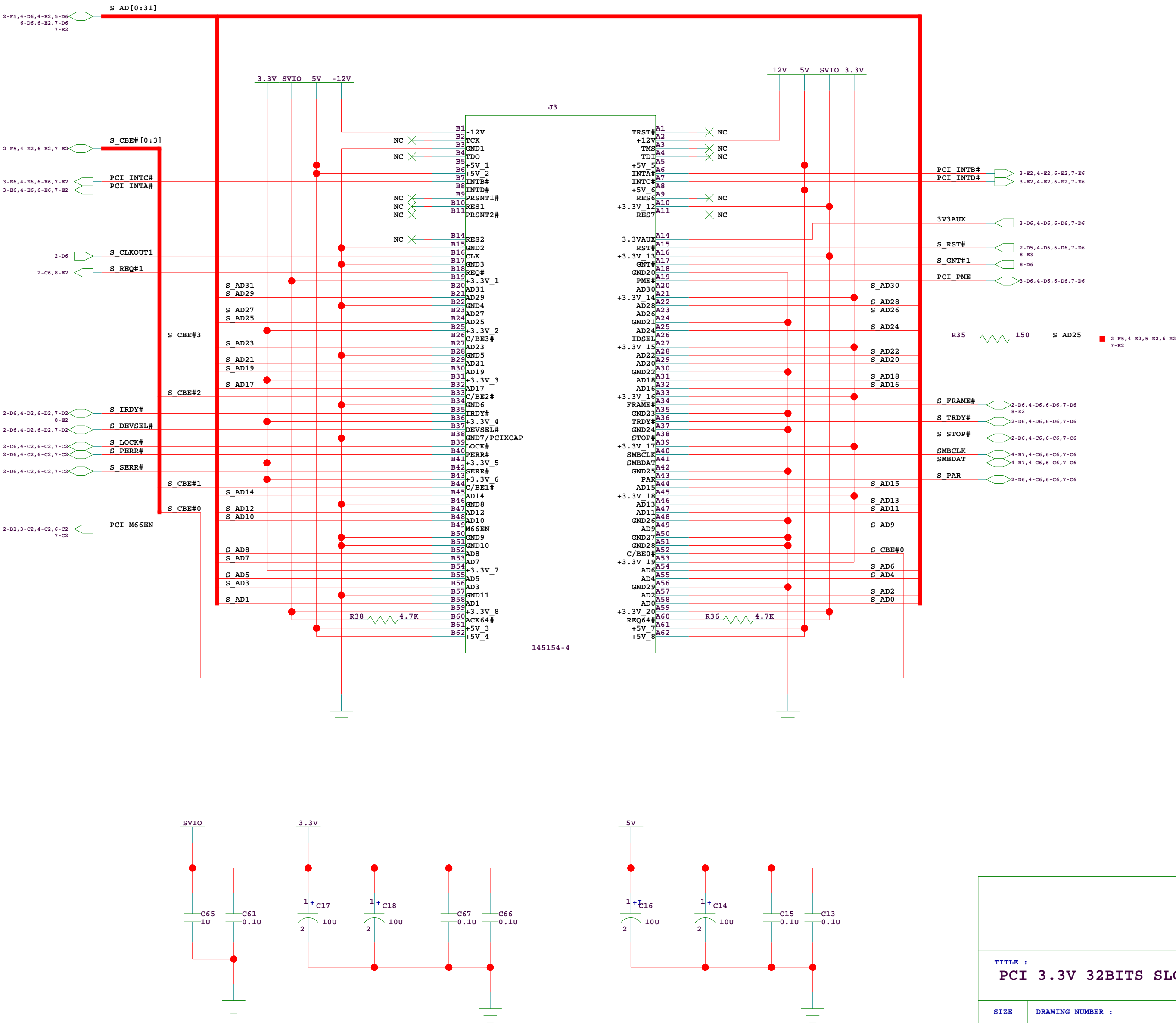
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PCI 3.3V 32Bits slot 2 Thru-hole connector



|                                                       |                                   |              |                   |
|-------------------------------------------------------|-----------------------------------|--------------|-------------------|
| TITLE :<br>PCI 3.3V 32BITS SLOT 2 THRU-HOLE CONNECTOR |                                   |              |                   |
| SIZE<br>C                                             | DRAWING NUMBER :<br>60D6000_SC001 | Version:     | DESIGNER :<br>IDT |
| LAST MODIFIED DATE :<br>7-17-2007_10:11               |                                   | SHEET 5 OF 8 |                   |





|                                                                          |                  |          |                                           |  |  |
|--------------------------------------------------------------------------|------------------|----------|-------------------------------------------|--|--|
| <p>TITLE :</p> <h1 style="text-align: center;">PCI EXTERNAL ARBITER</h1> |                  |          |                                           |  |  |
| SIZE                                                                     | DRAWING NUMBER : | Version: | DESIGNER :                                |  |  |
| C                                                                        | 60E3000_SC001    |          | IDT                                       |  |  |
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