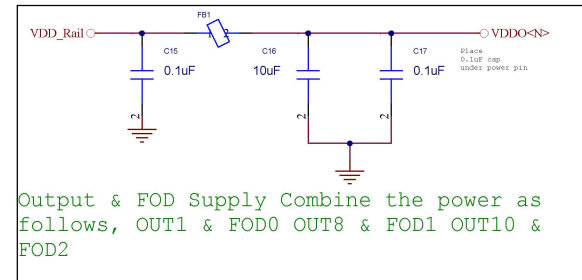
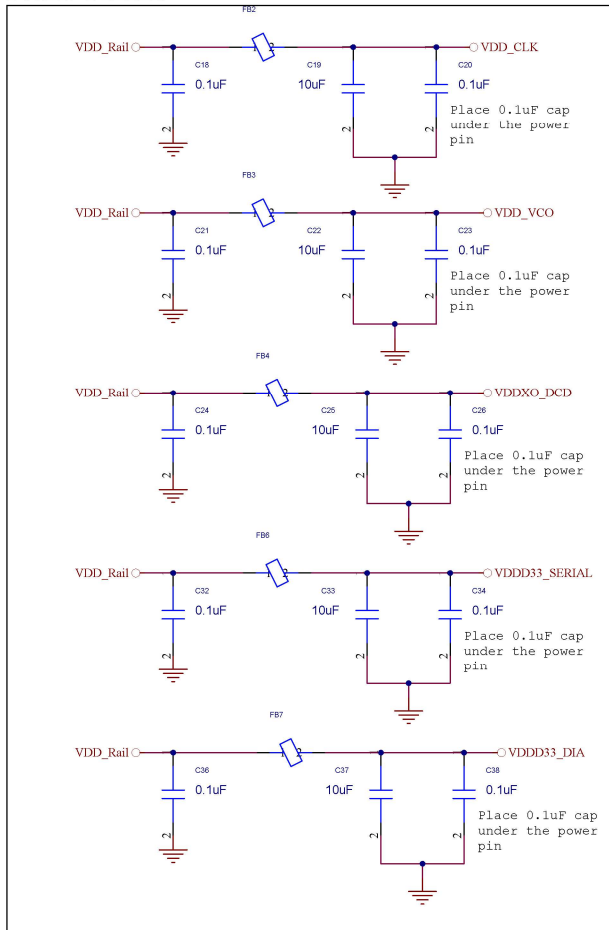


# FC3 RC323xx Customer Reference Schematic

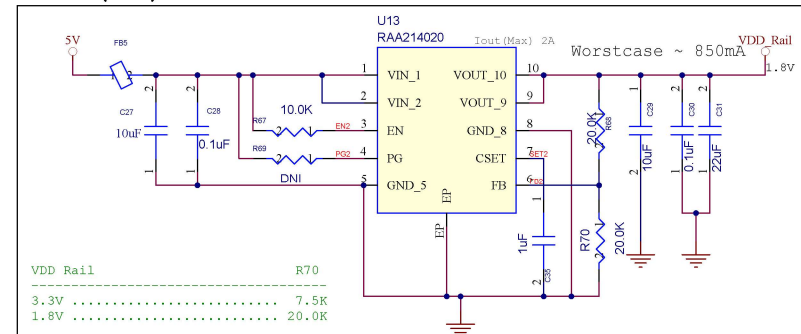
## RC32312A, R22312A Clock Gen & Jitter Attenuation

### POWER FILTER



VDDD33\_DIA & VDDD33\_SERIAL supports 1.8 & 3.3V however, VDDD33\_DIA to 1.8V for lower power consumptions, VDDD33\_SERIAL Selects the I2C comm voltage, 3.3V may required for EEPROM loading

### POWER (LDO)



|                                                                                       |                                |                                                       |                |
|---------------------------------------------------------------------------------------|--------------------------------|-------------------------------------------------------|----------------|
|  |                                | <b>RENESAS</b><br>603 MARCH RD<br>KANATA, ON, K2K 2M5 |                |
| Project: RC323xx Customer Reference Schematic - Altium                                |                                |                                                       |                |
| Size:                                                                                 | Engineer: Kapilan Ramachandran |                                                       |                |
| -                                                                                     | HW Number:                     | Sheet: 2                                              | Of 2           |
|                                                                                       | -                              |                                                       | Revision Rev A |