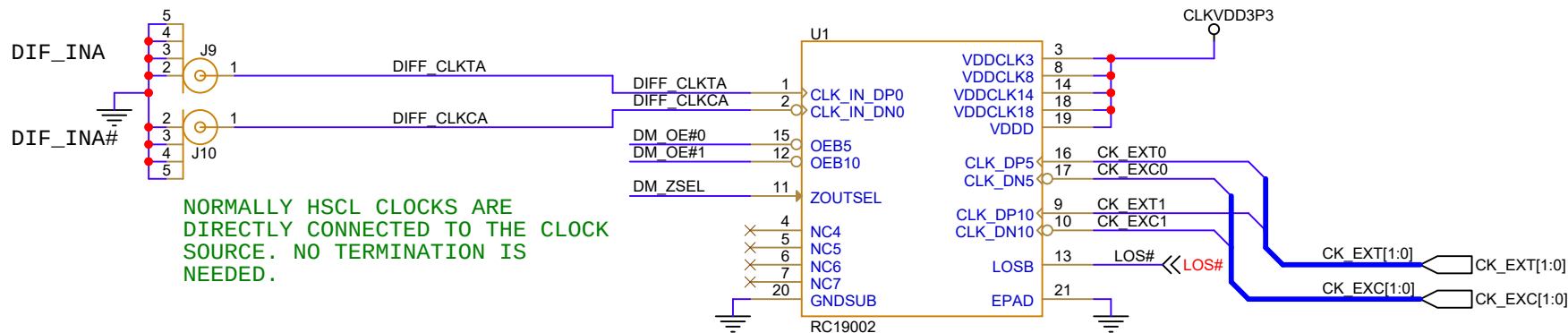
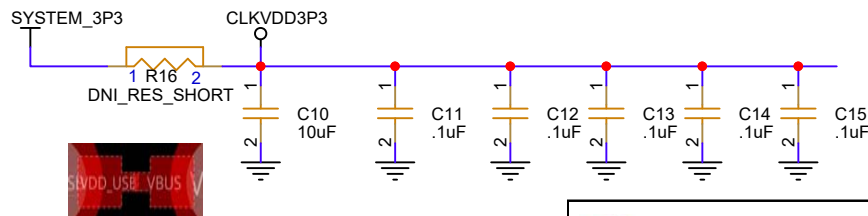
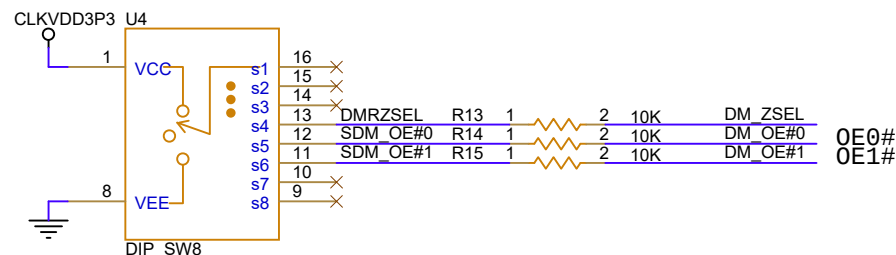




NORMALLY HSCL CLOCKS ARE DIRECTLY CONNECTED TO THE CLOCK SOURCE. NO TERMINATION IS NEEDED.

Default Jumper Settings:
U4: All switches at center position



Example of shared resistor. Placed between pins on a two pin header

RENESAS

Renesas
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Title: RC19002_EVB

Size A	HW Number: NA	Engineer: Louis Poitras	Rev. A.3
	Date: 01/23/2024	Sheet: 1 of 2	

