

IDT PLL CHIP CARRIER

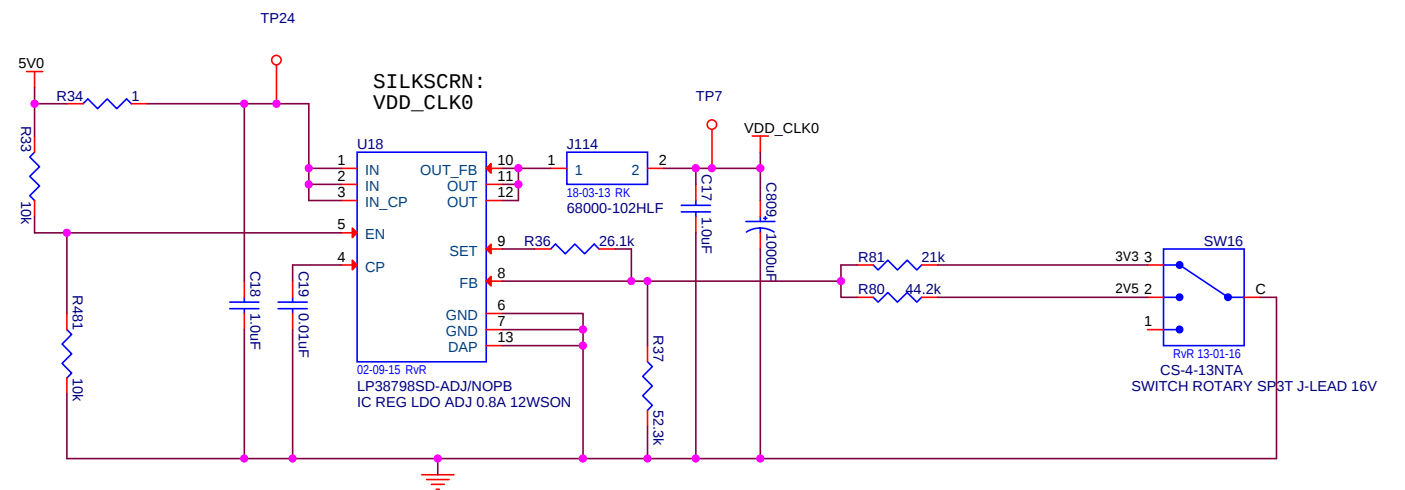
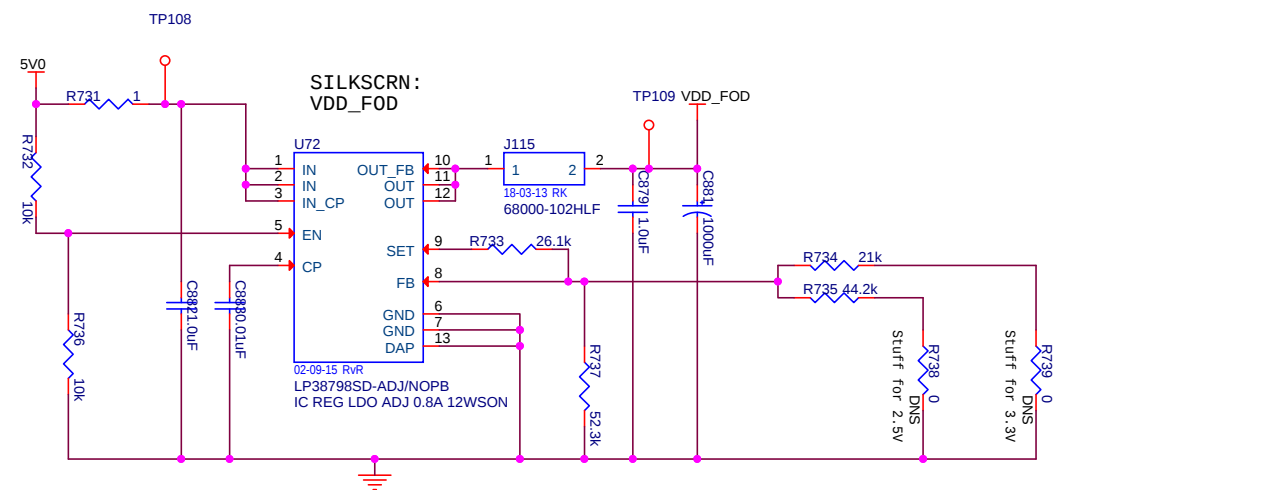
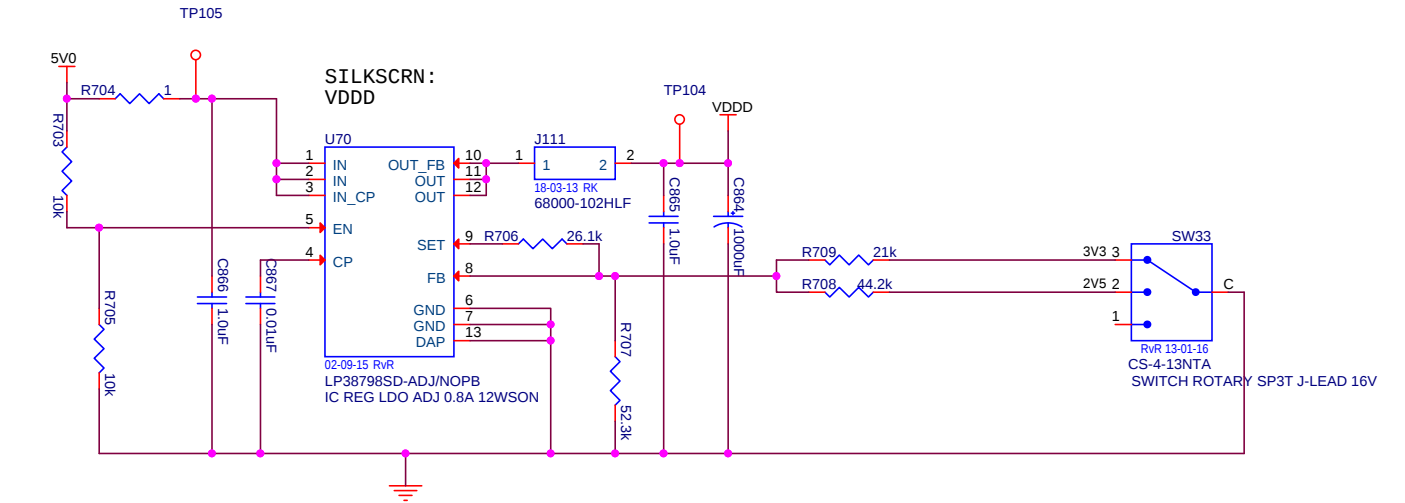
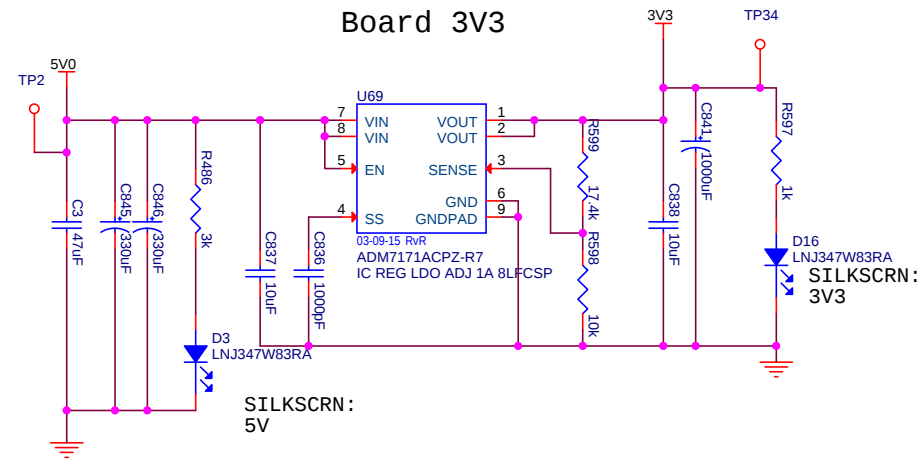
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REVISION HISTORY

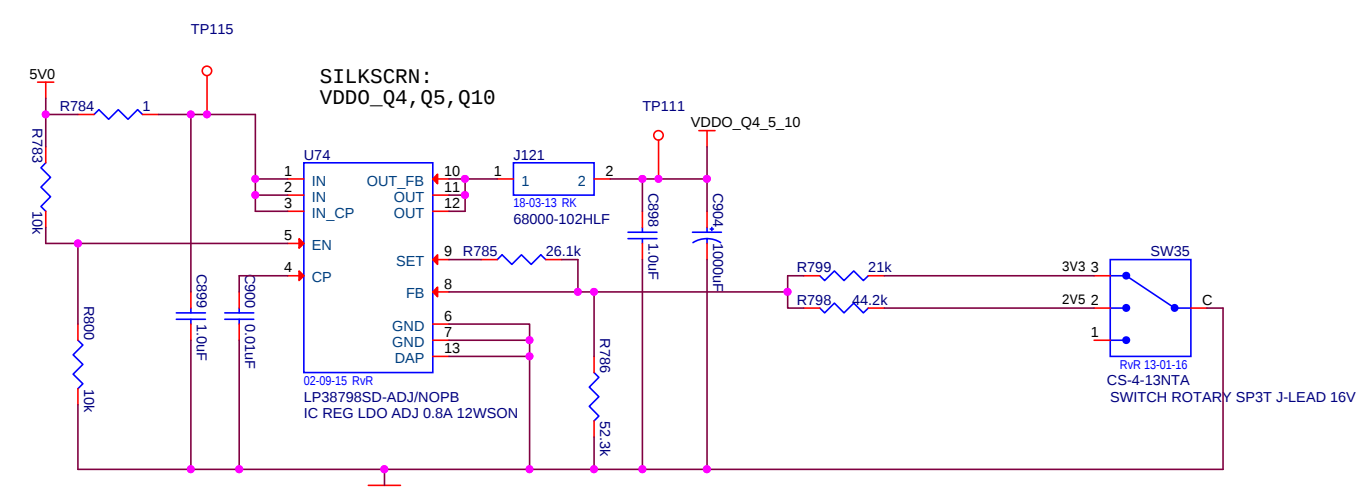
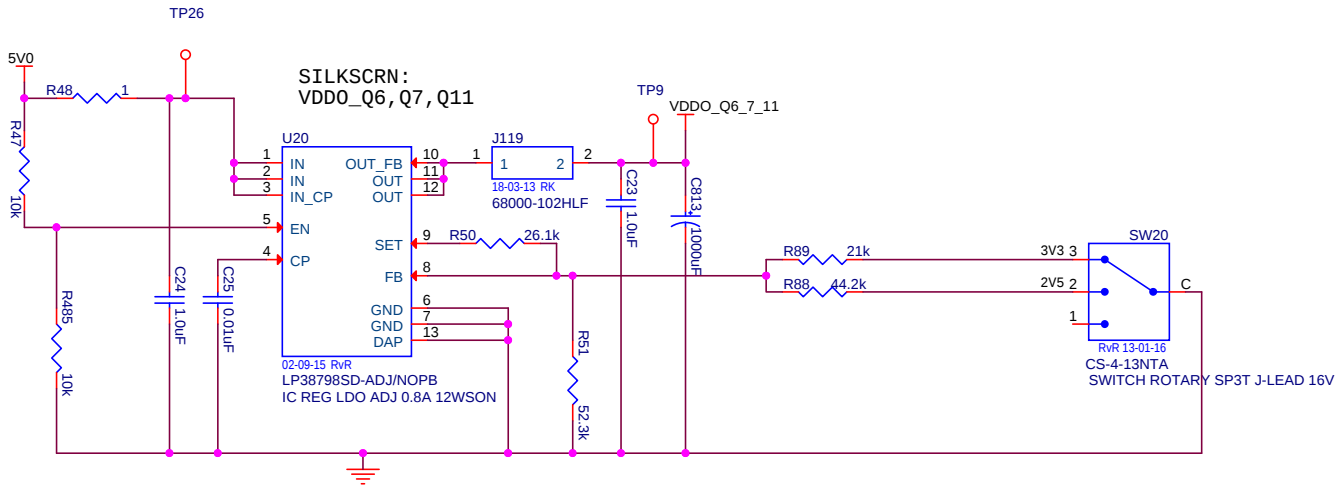
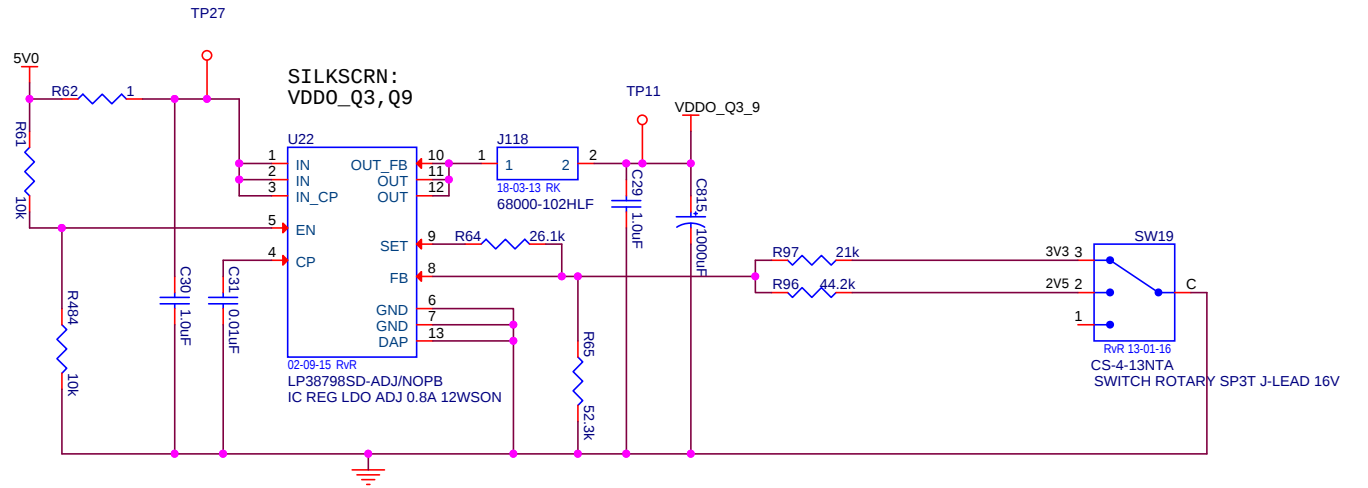
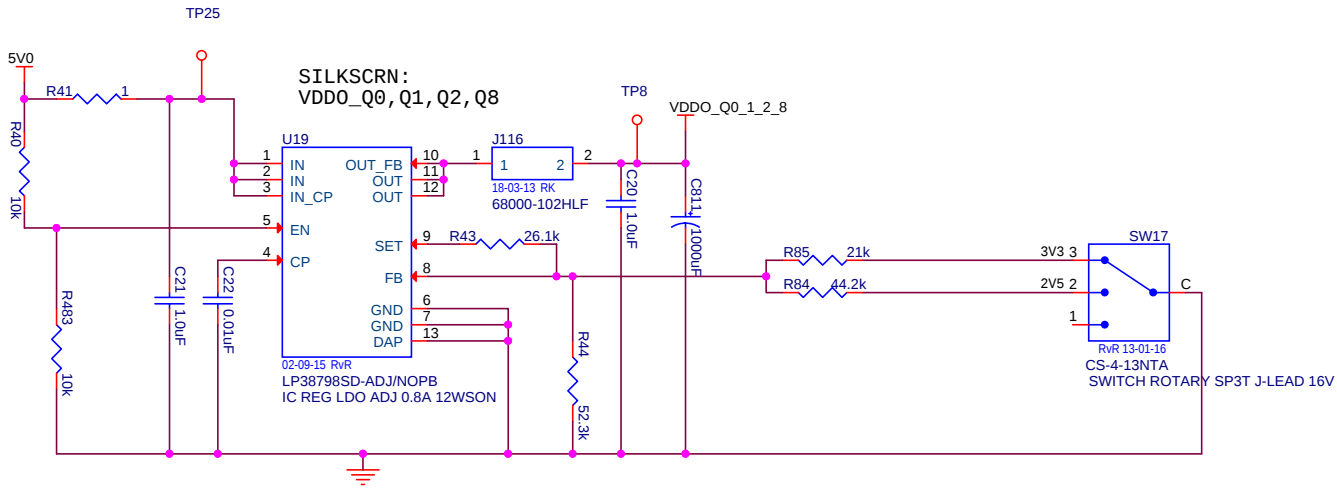
REVISION	DATE	CHANGE DETAILS
0.1	5 December 2017	First Release
0.2	28 February 2018	For details re changes done, refer to: \\navar\z_10397-01_sch\reviews\DN-10397-01_Changes_Feb 28 2018.xlsx

PROJECT NAME	 Fidus Systems	375 Terry Fox Drive, Ottawa, ON K2K 0J8
DESIGN	TITLE	
BM	IDT PLL Chip Carrier	
DRAWN	SUBTITLE	
BM	Table of Contents and Revision History	
CHECK	DRAWING NUMBER	REVISION
CL	SK-10397-01	0.2
	RELEASE DATE	SHEET
	5 December 2017	1 OF 6

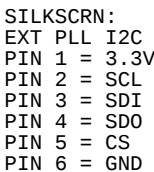


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POS 3 = 3V3
POS 2 = 2V5
POS 1 = 1V8/FPGA control
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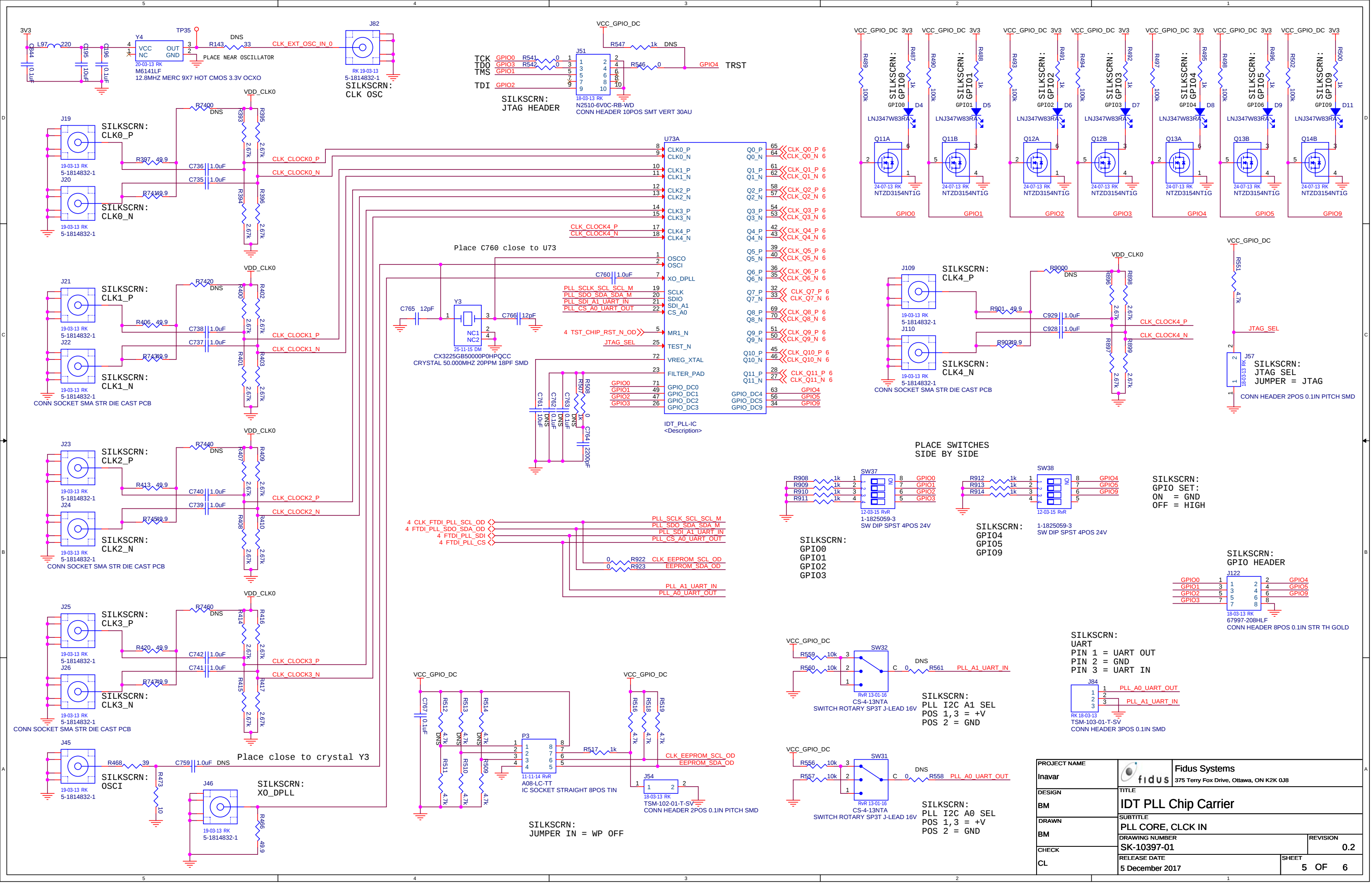
PROJECT NAME	 Fidus Systems 375 Terry Fox Drive, Ottawa, ON K2K 0J8	
DESIGN	TITLE	
BM	IDT PLL Chip Carrier	
DRAWN	SUBTITLE	
BM	PWR SUPPLIES 1	
CHECK	DRAWING NUMBER	REVISION
	SK-10397-01	0.2
CL	RELEASE DATE	SHEET
	5 December 2017	2 OF 6



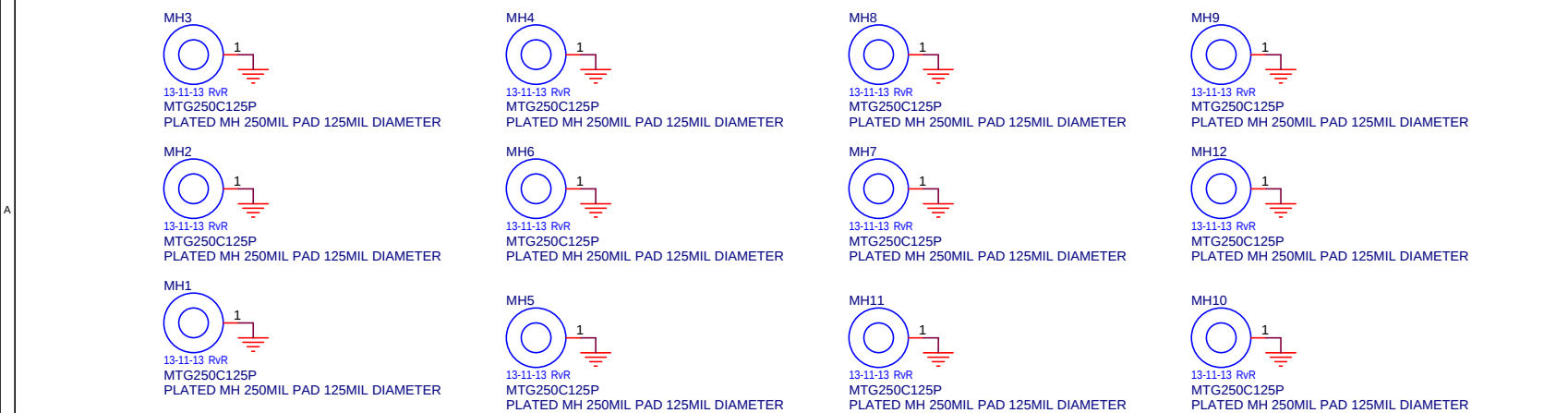
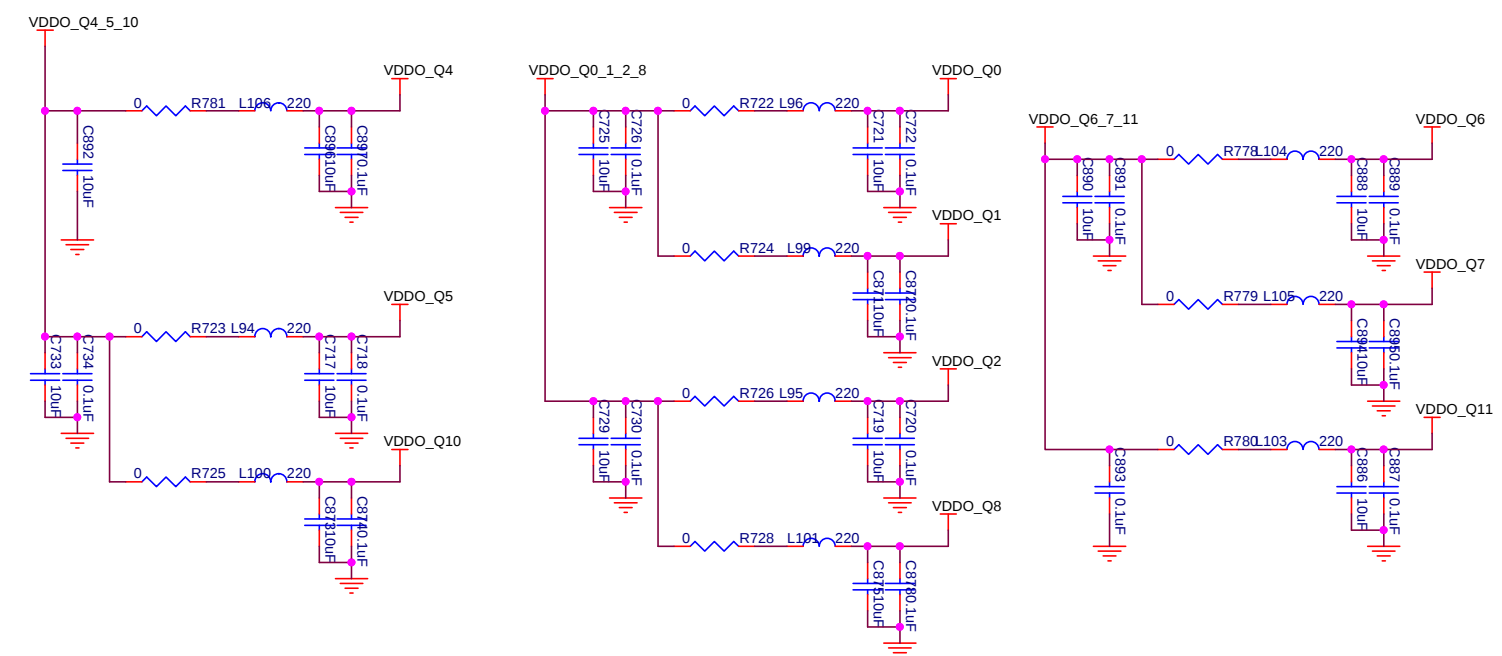
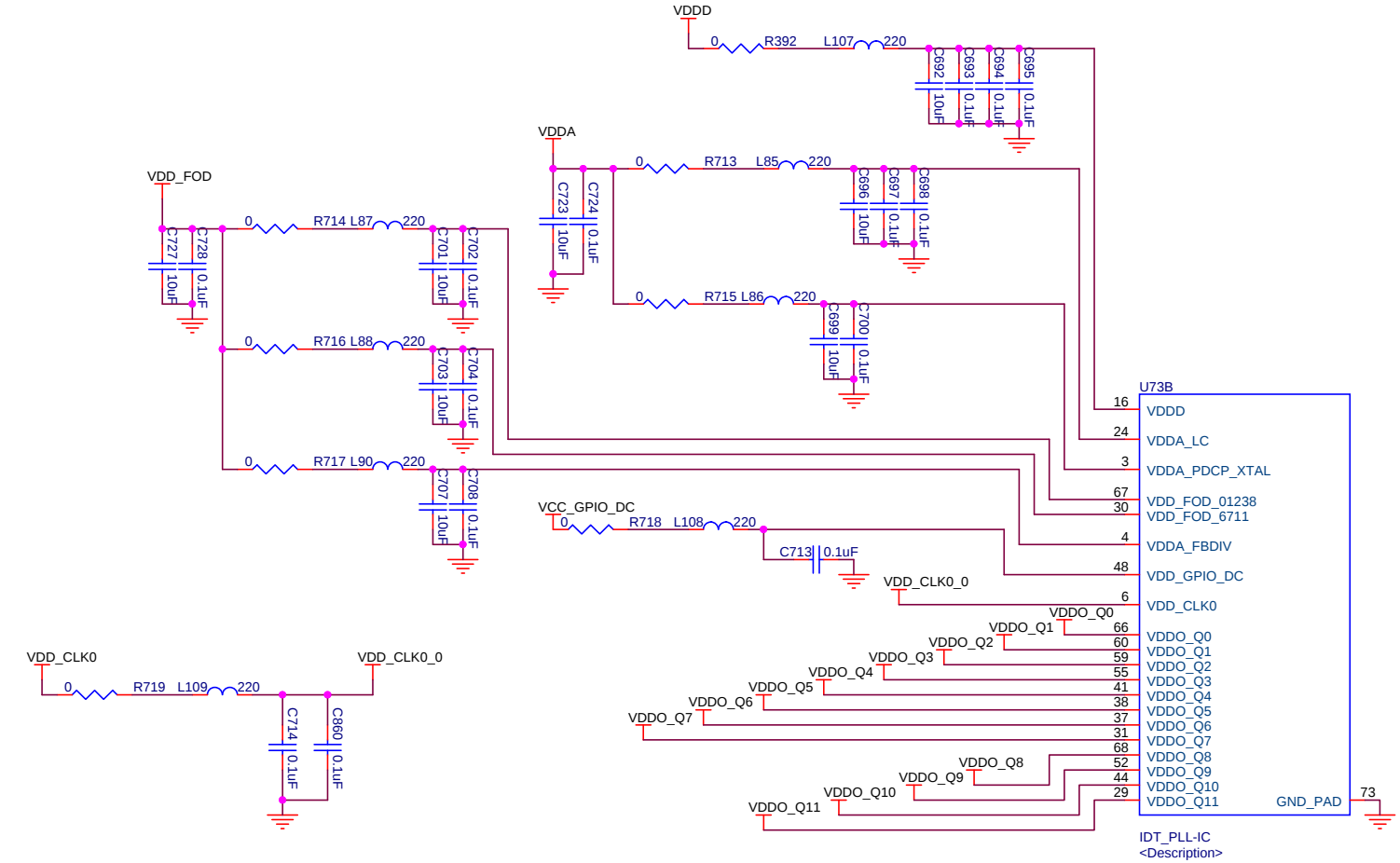
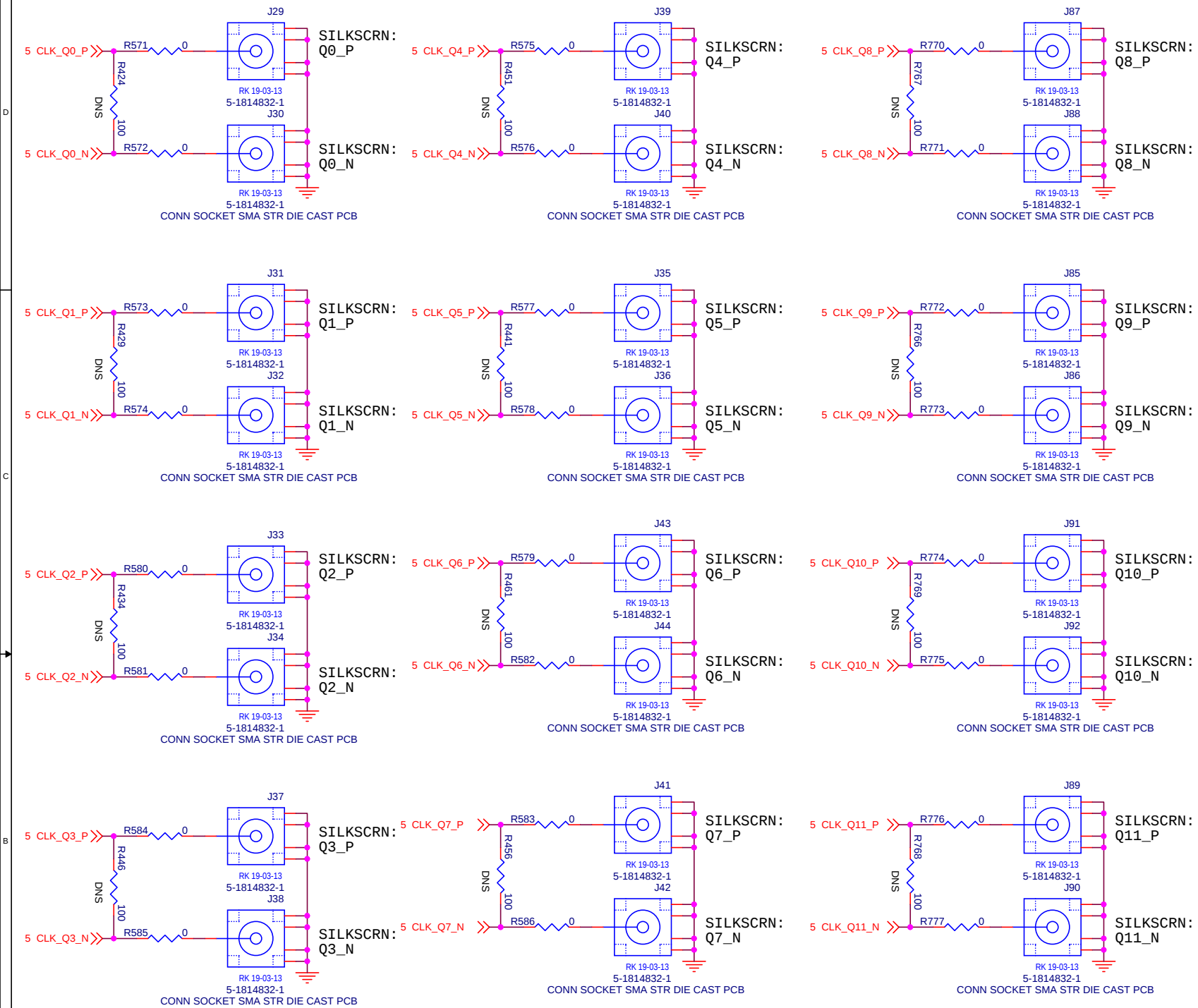
PROJECT NAME		Fidus Systems	
Inavar		375 Terry Fox Drive, Ottawa, ON K2K 0J8	
DESIGN	TITLE		
BM	IDT PLL Chip Carrier		
DRAWN	SUBTITLE		
BM	PWR SUPPLIES 2		
CHECK	DRAWING NUMBER		REVISION
	SK-10397-01		0.2
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PROJECT NAME	 Fidus Systems 375 Terry Fox Drive, Ottawa, ON K2K 0J8	
Inavar		
DESIGN	TITLE	
BM	IDT PLL Chip Carrier	
DRAWN	SUBTITLE	
BM	FTDI USB, RST	
CHECK	DRAWING NUMBER	REVISION
	SK-10397-01	0.2
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PLACE PARALLEL TERMINATIONS CLOSE TO U73



PROJECT NAME	Inavar	Fidus Systems
DESIGN	BM	TITLE
DRAWN	BM	SUBTITLE
CHECK	CL	DRAWING NUMBER
RELEASE DATE	5 December 2017	REVISION
		0.2
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