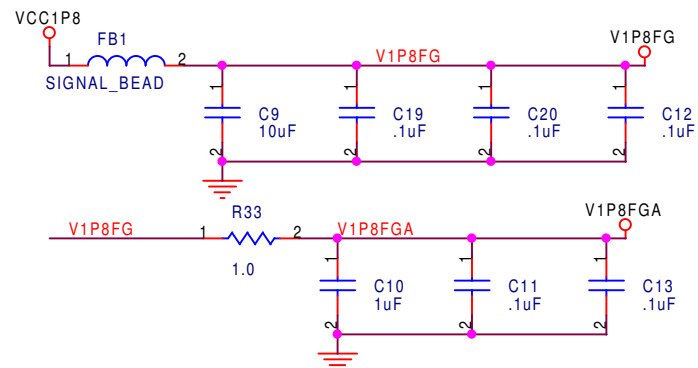




Manufacture	Part Number	Z@100MHz	PkgSz	DC res.	Current (Ma)
muRata	BLM18AG601SN1	600	1608	0.50	200
muRata	BLM18BD601SN1_PB	600	1608	0.65	200
Ceratech	HB-1T1608-601	600	1608	0.50	200
TDK	MMZ1608R301A	300	1608	0.20	500

Usage notes.

1. If used on an add-in card or if spread modulation is or may be used use PLL ByPass mode.
2. Other wise use HiBandWidth mode.

Layout notes.

- Layout notes:
- 1.Route power from bead through bulk capacitor pad then through 0.1uF capacitor pad then to clock chip Vdd pad.
 - 2.Do not share ground vias. One ground pin one ground via.

San Jose, CA

Date: Thursday, February 07, 2013 Sheet 1 of 1