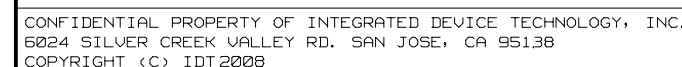


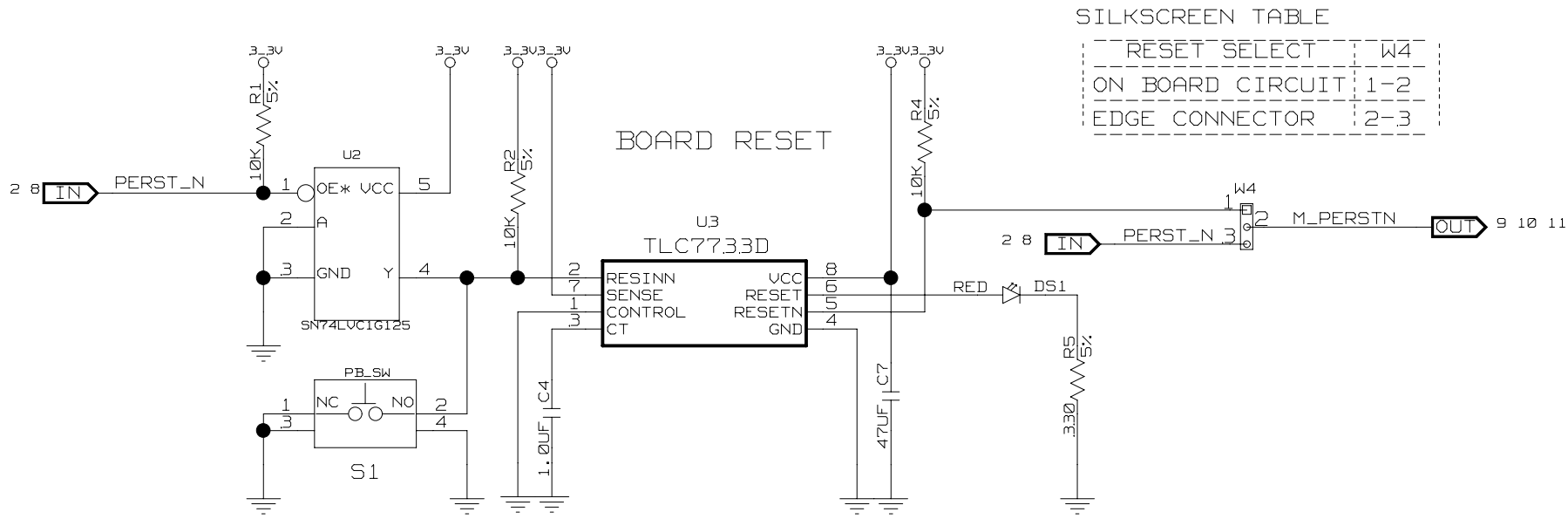
## A



## SHEET DESCRIPTION

- 

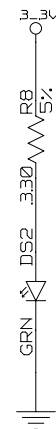
Fri Jan 18 10:42:56 2008



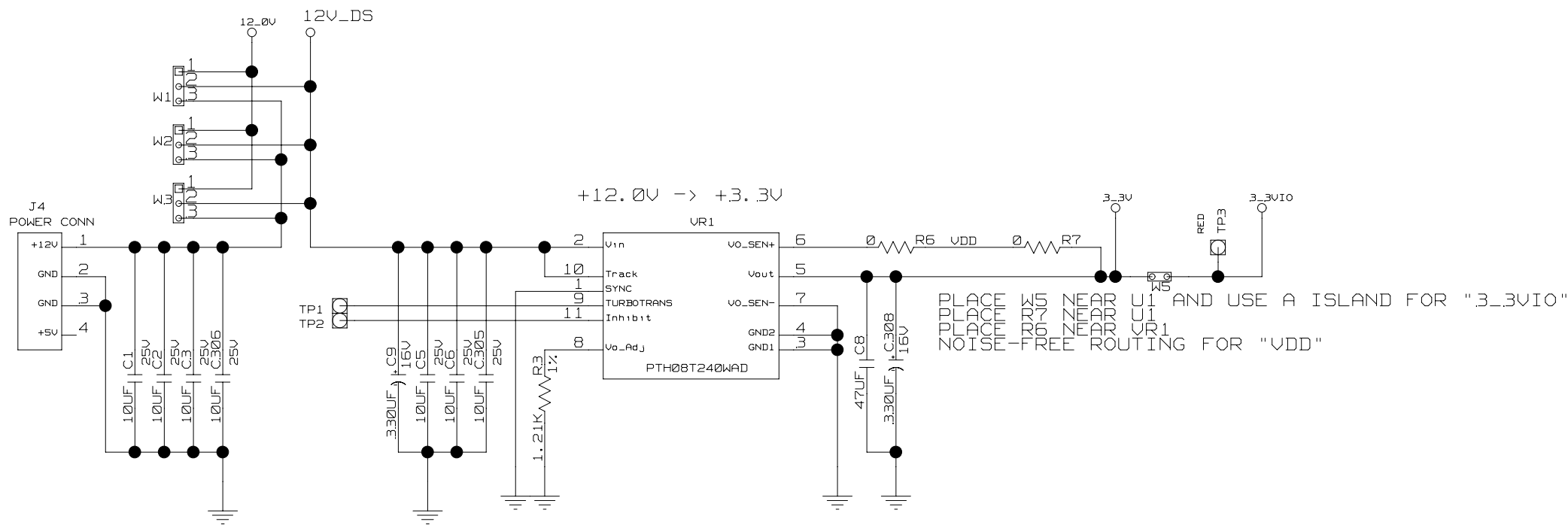
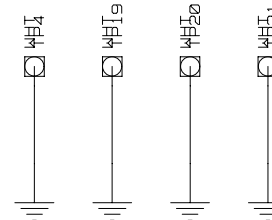
SILKSCREEN TABLE

|                  |     |
|------------------|-----|
| RESET SELECT     | W4  |
| ON BOARD CIRCUIT | 1-2 |
| EDGE CONNECTOR   | 2-3 |

POWER INDICATOR  
PLACE NEAR TOP EDGE  
LABEL 'POWER'



LABEL 'GND'



PLACE LABEL 'VDD' AND USE A ISLAND FOR "3.3VIO"  
NOISE-FREE ROUTING FOR "VDD"

SILKSCREEN TABLE

|                  |     |     |     |
|------------------|-----|-----|-----|
| 12V POWER SELECT | W1  | W2  | W3  |
| EDGE CONNECTOR   | 1-2 | 1-2 | 1-2 |
| EXTERNAL INPUT   | 2-3 | 2-3 | 2-3 |



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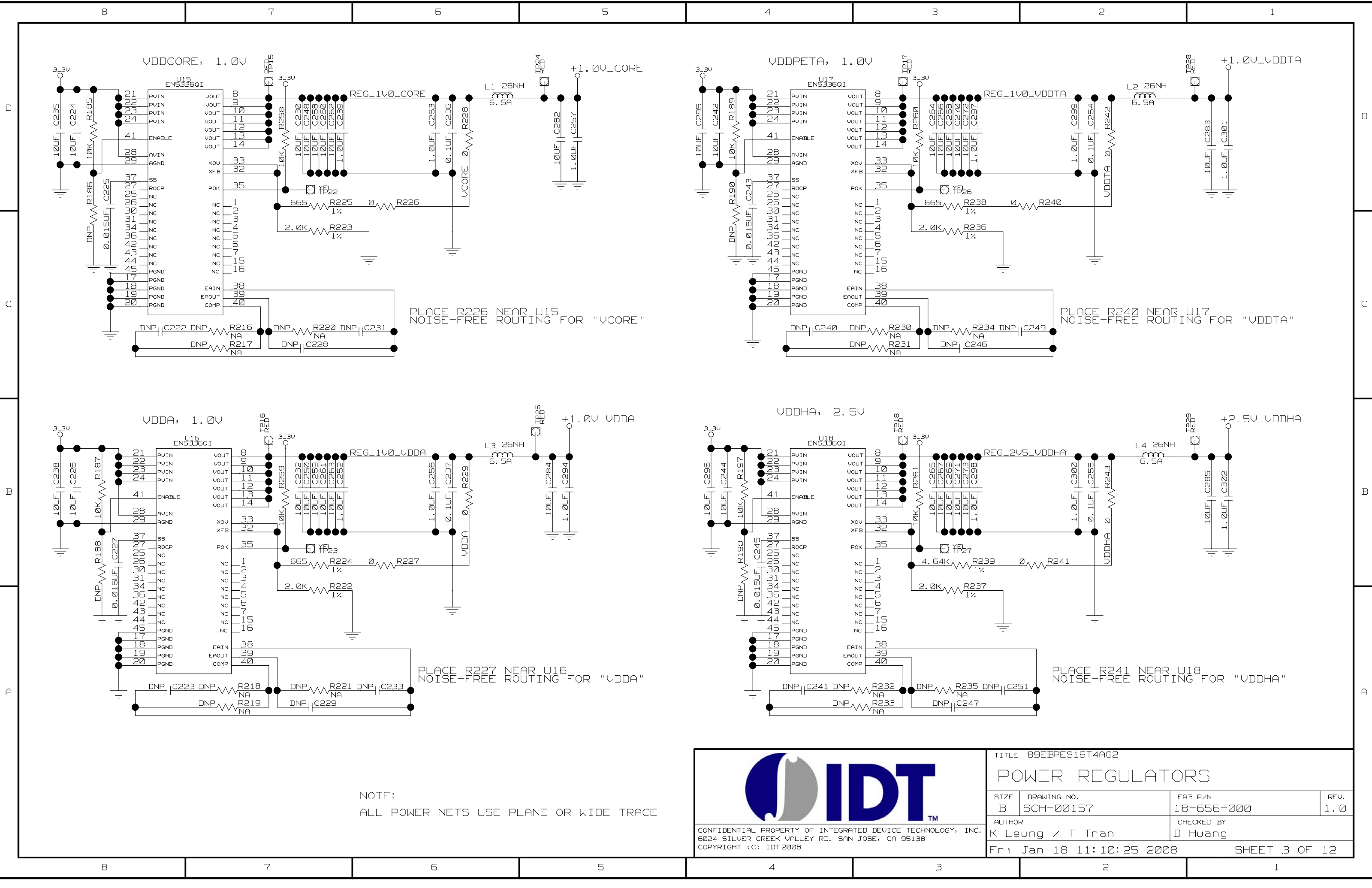
TITLE 89EBPES16T4AG2

RESET, POWER CONNECTOR

|           |                          |                       |             |
|-----------|--------------------------|-----------------------|-------------|
| SIZE<br>B | DRAWING NO.<br>SCH-00157 | FAB P/N<br>18-656-000 | REV.<br>1.0 |
|-----------|--------------------------|-----------------------|-------------|

|                            |                       |
|----------------------------|-----------------------|
| AUTHOR<br>K Leung / T Tran | CHECKED BY<br>D Huang |
|----------------------------|-----------------------|

|                          |               |
|--------------------------|---------------|
| Fri Jan 18 11:10:25 2008 | SHEET 2 OF 12 |
|--------------------------|---------------|



NOTE:  
ALL POWER NETS USE PLANE OR WIDE TRACE

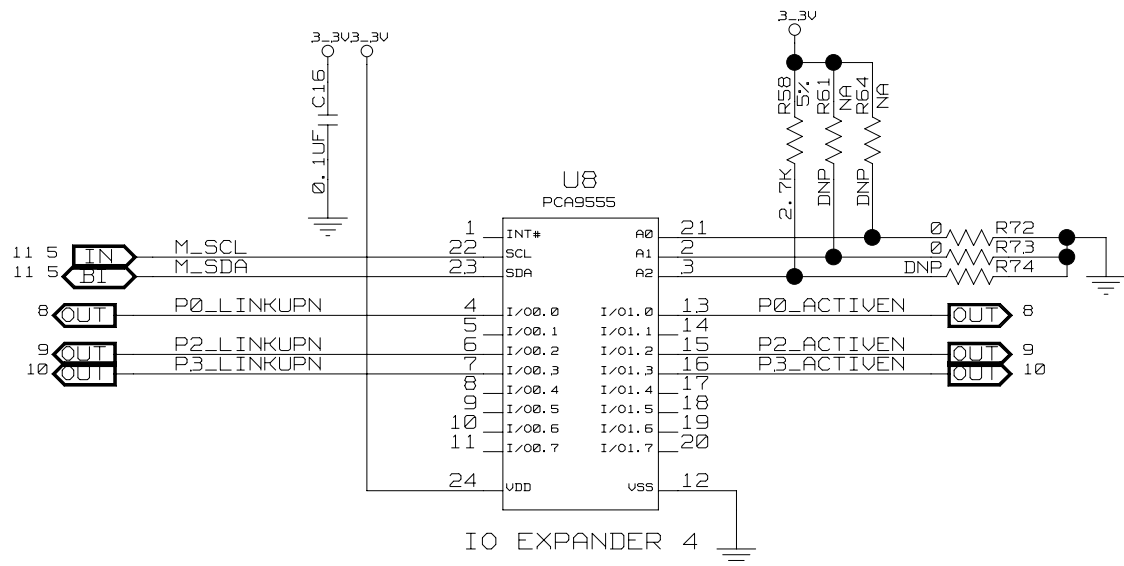
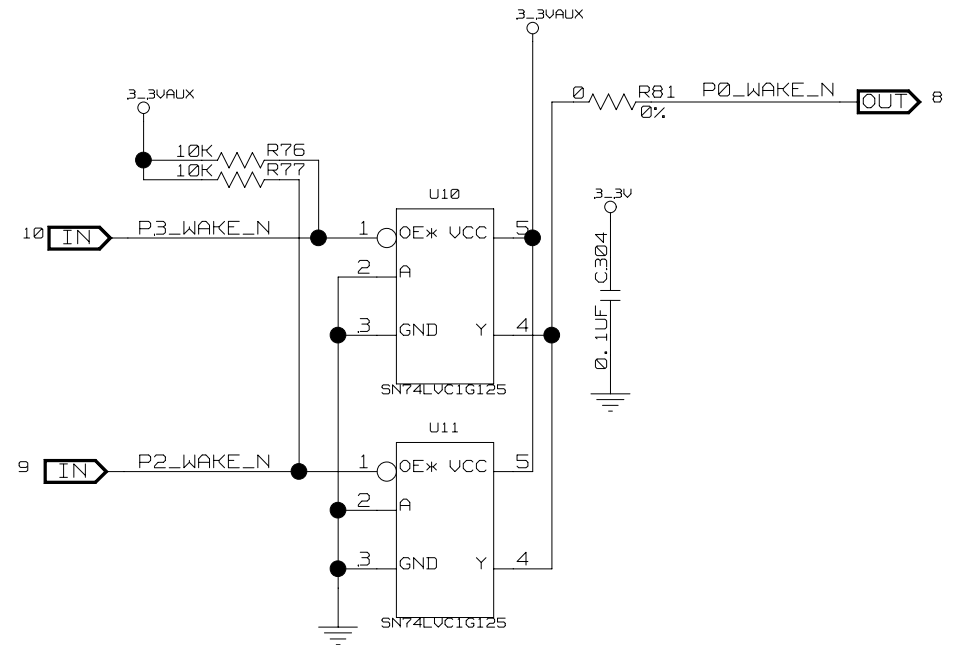
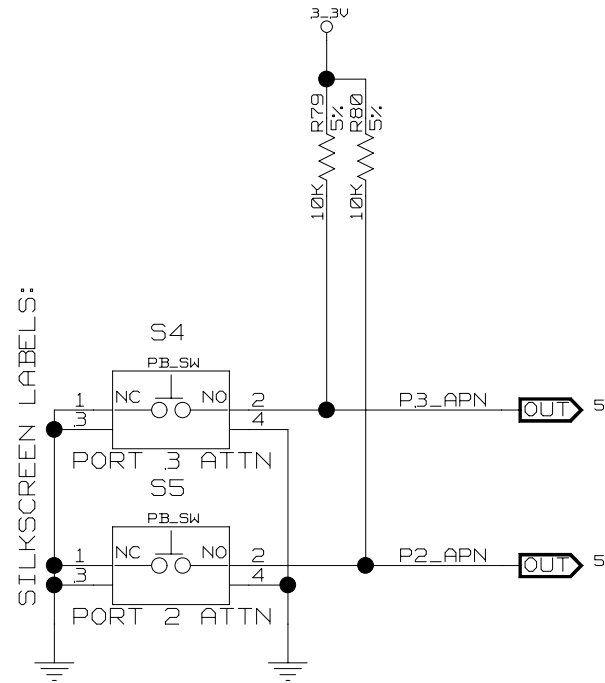
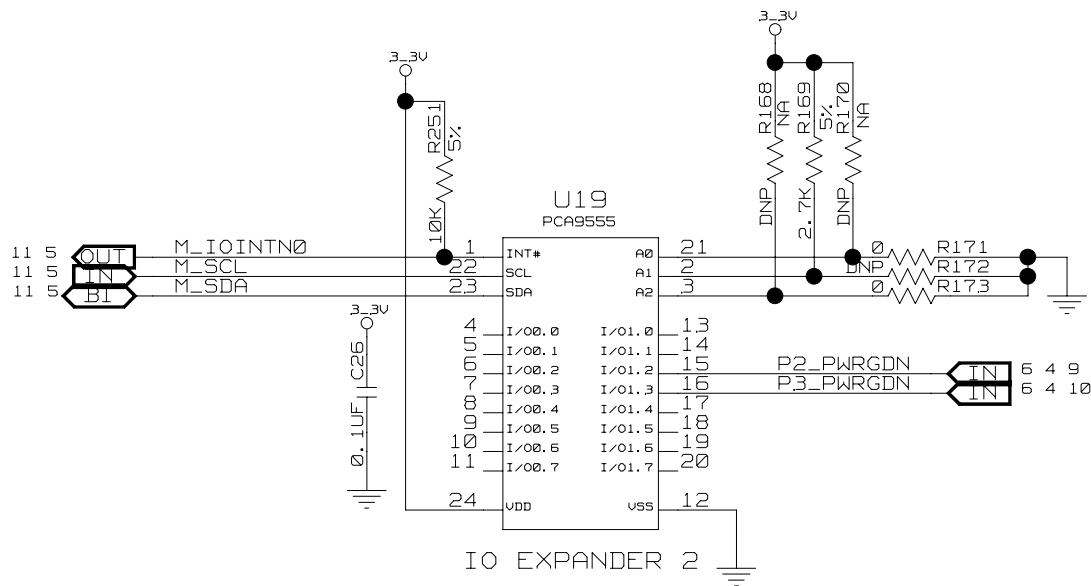
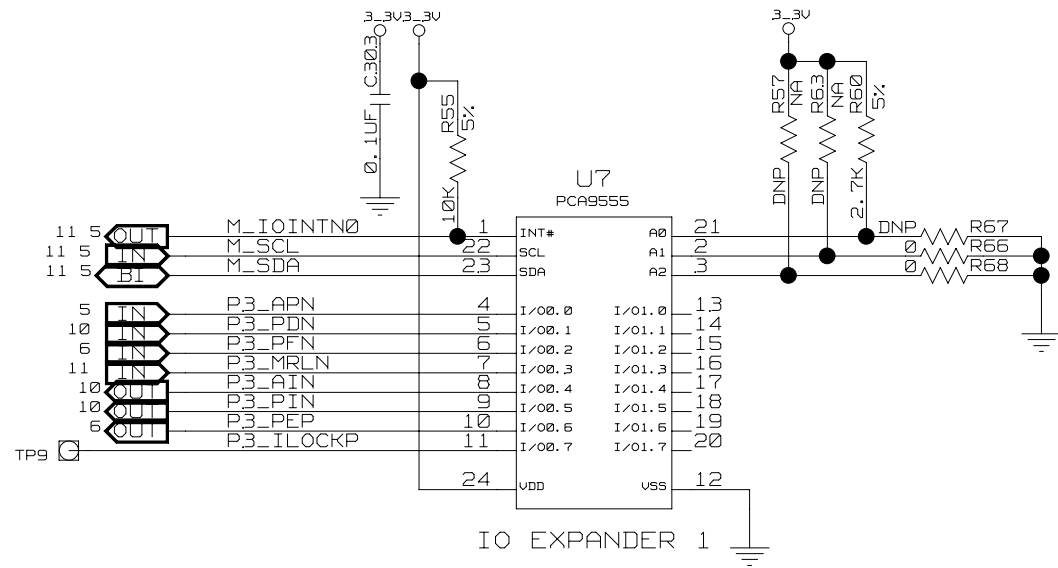
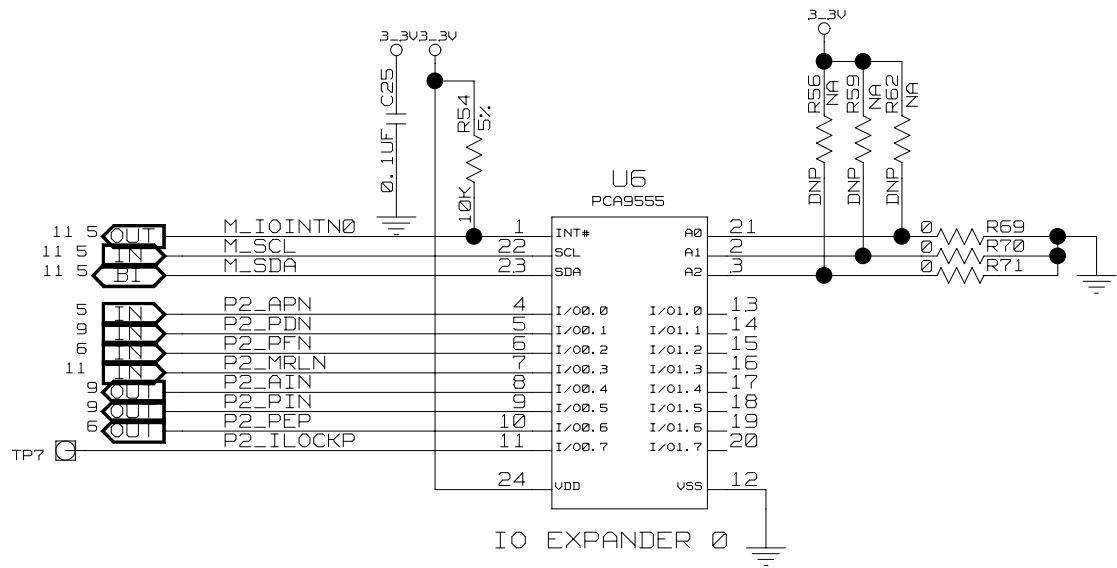


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TITLE 89EBPES16T4AG2  
POWER REGULATORS

|                            |                          |                       |             |
|----------------------------|--------------------------|-----------------------|-------------|
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| AUTHOR<br>K Leung / T Tran |                          | CHECKED BY<br>D Huang |             |
| Fri Jan 18 11:10:25 2008   |                          | SHEET 3 OF 12         |             |



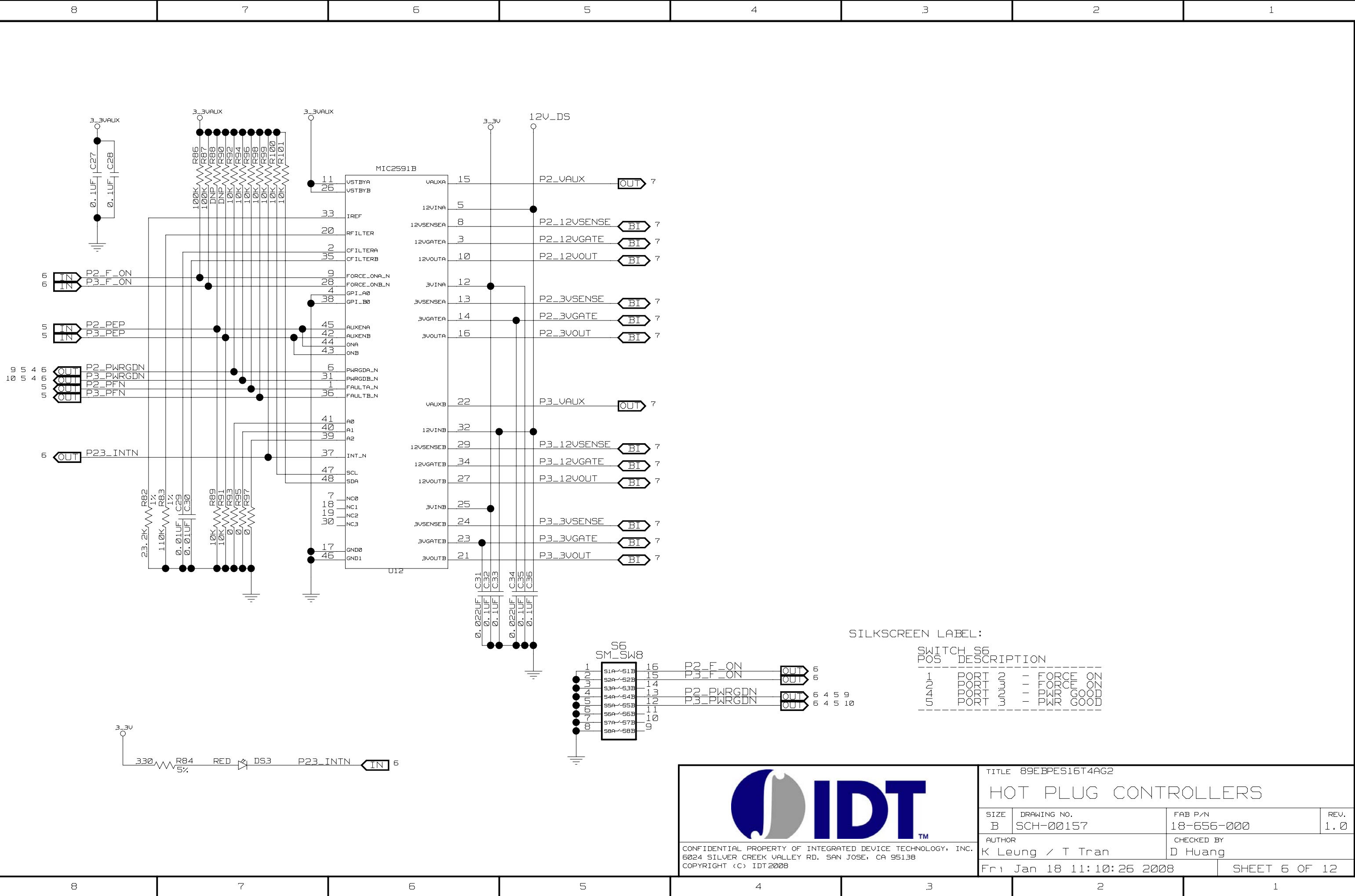




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TITLE 89EBPES16T4AG2  
IO EXP, WAKE, ATTN BUTTONS

|                            |                          |                       |             |
|----------------------------|--------------------------|-----------------------|-------------|
| SIZE<br>B                  | DRAWING NO.<br>SCH-00157 | FAB P/N<br>18-656-000 | REV.<br>1.0 |
| AUTHOR<br>K Leung / T Tran |                          | CHECKED BY<br>D Huang |             |
| Fri Jan 18 11:10:26 2008   |                          | SHEET 5 OF 12         |             |

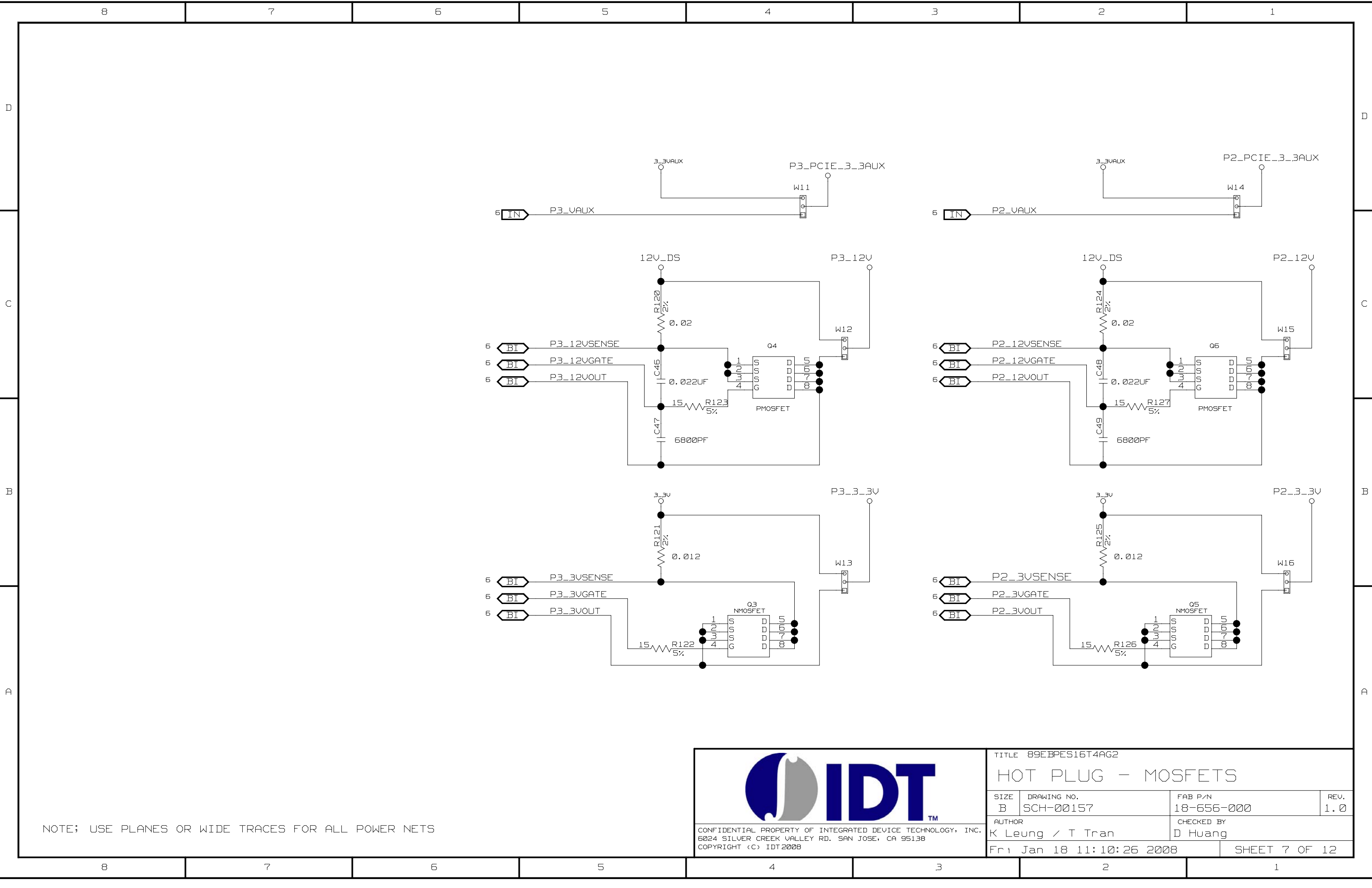




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TITLE 89EBPES16T4AG2  
HOT PLUG CONTROLLERS

|                            |                          |                       |             |
|----------------------------|--------------------------|-----------------------|-------------|
| SIZE<br>B                  | DRAWING NO.<br>SCH-00157 | FAB P/N<br>18-656-000 | REV.<br>1.0 |
| AUTHOR<br>K Leung / T Tran |                          | CHECKED BY<br>D Huang |             |
| Fri Jan 18 11:10:26 2008   |                          | SHEET 6 OF 12         |             |





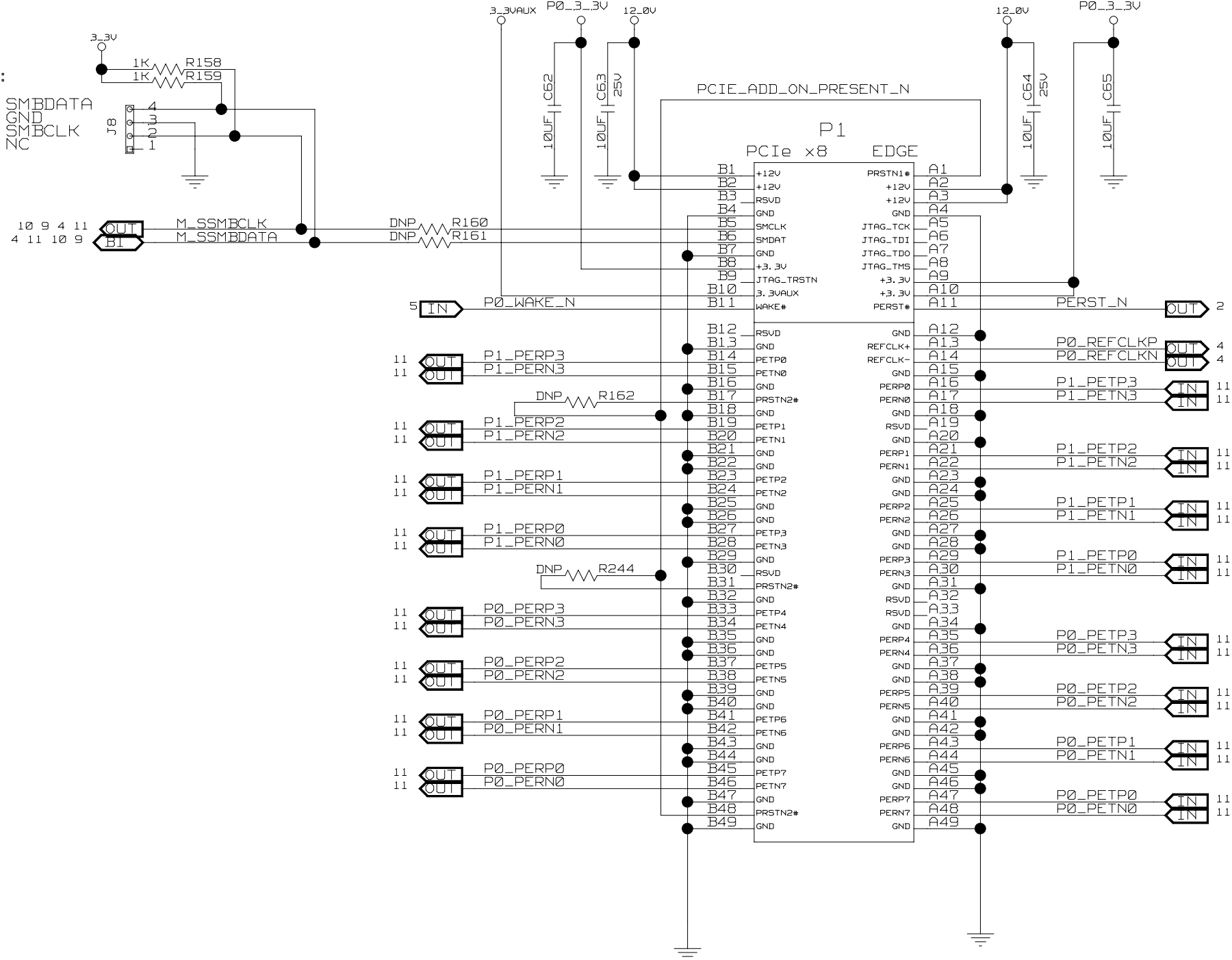
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TITLE 89EBPES16T4AG2  
HOT PLUG - MOSFETS

|                            |                          |                       |               |
|----------------------------|--------------------------|-----------------------|---------------|
| SIZE<br>B                  | DRAWING NO.<br>SCH-00157 | FAB P/N<br>18-656-000 | REV.<br>1.0   |
| AUTHOR<br>K Leung / T Tran |                          | CHECKED BY<br>D Huang |               |
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LABEL PINS:

4 SMBDATA  
1 GND  
2 SMBCLK  
1 NC



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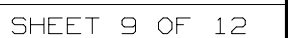
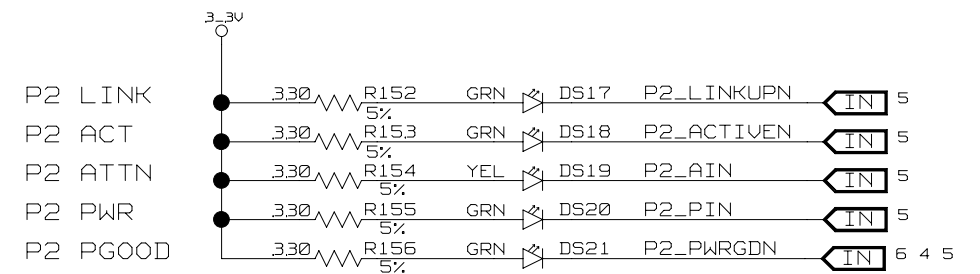
TITLE 89EBPES16T4AG2

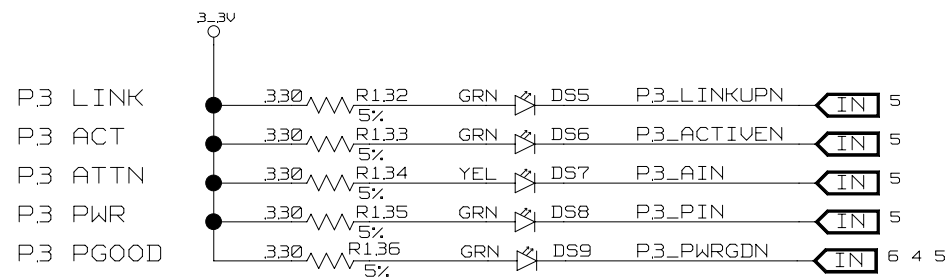
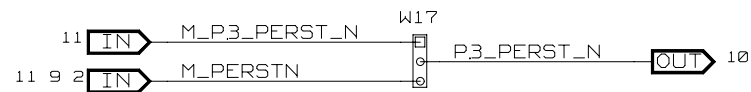
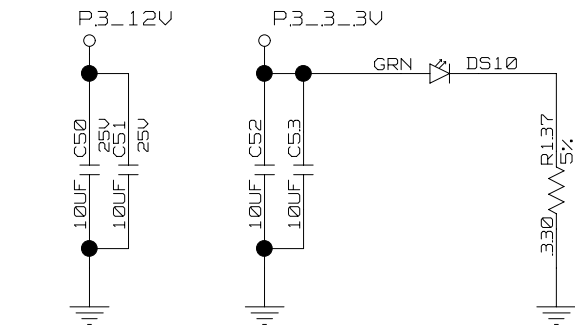
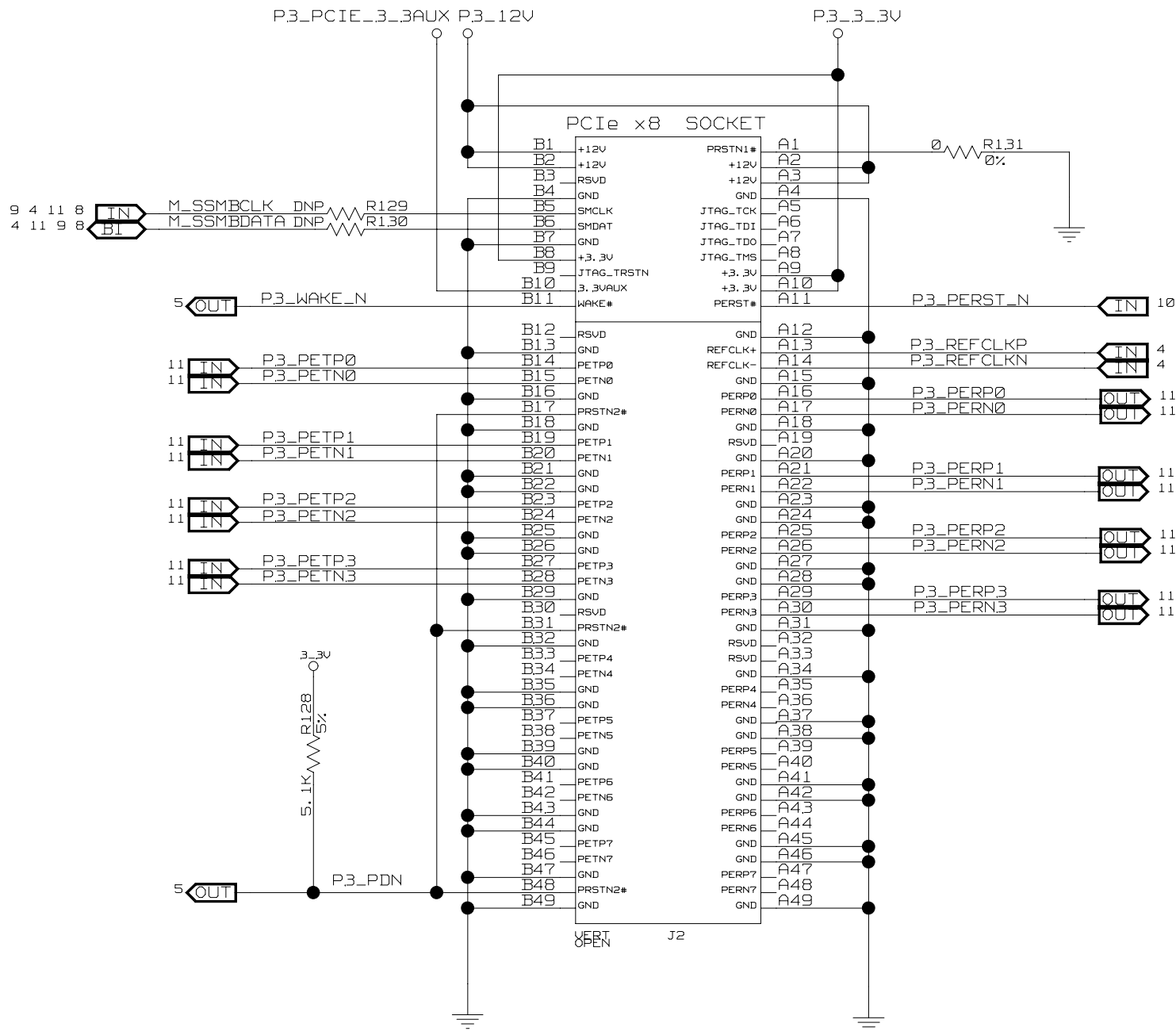
PORT 0 UPSTREAM EDGE CONN.

|      |             |            |      |
|------|-------------|------------|------|
| SIZE | DRAWING NO. | FAB P/N    | REV. |
| B    | SCH-00157   | 18-656-000 | 1.0  |

|                  |            |
|------------------|------------|
| AUTHOR           | CHECKED BY |
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TITLE 89EBPES16T4AG2

## PORT 3 CONNECTOR

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|------|-------------|------------|------|
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