Renesas Starter kit+ for RZ/T1 (RSK+RZT1) e² studio (version 4.0.0.26) Limitations: 15/07/2015

RZ/T1 Working Sample (WS) Restrictions
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This RSK includes a Working Sample device.
This device may contain some issues that are currently undetected.
Issues that are detected with the microcontroller will be identified on the RSK release note or on the RSK+RZT1 product pages on our web site. Renesas cannot accept requests to exchange a board or RZ/T1 device for these issues.

RSK+RZT1 Product Page:
http://www.renesas.com/rskrzt1

Known Issues

Condition	Phenomena	Restriction	Workaround

When R-IN engine is used	When HW-RTOS is activated and the designated RAM for HW-RTOS is read or written by both Cortex-M3 and HW-RTOS at the exact same time, HW-RTOS might malfunction.	1. Use the latest OS library (HW-RTOS driver) dated "**20150715**" (15 July 2015) or later.
		
		2. Prohibit Cortex-M3 from accessing following area
		<Area> <Address>
		DataRAM 0x2002_8000-0x2002_87FF,
		       0x2006_8000-0x2006_87FF
		
		Bitbandalias 0x2250_0000-0x2250_FFFF,
		       0x22D0_0000-0x22D0_FFFF
		
		RSPI0,RSPI1 0x4006_8000-0x4006_87FF (Mirror space)
		       0xA006_8000-0xA006_87FF
		
		Bitbandalias 0x42D0_0000-0x42D0_FFFF
		
		External address Space(CS0-CS5)
		   In 384MB space of 0x6000_0000 - 0x77FF_FFFF, address the downward 5bits of which corresponds with the below
		       0x2_8000-0x2_87FF,0x6_8000-0x6_87FF,
		       0xA_8000-0xA_87FF,0xE_8000-0xE_87FF
		
		3. Do not use service calls for dynamic event-flag, semaphore, and mutex generation
		
		## Please contact our support if you have any questions
			Will be fixed in WS2 (2nd-cut)