

# CubeSuite+ Simulator for RX V2.01.00

## **Release Note**

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## **Chapter 1. Target Devices**

CubeSuite+ Simulator for RX600 series and RX200 series supports both simulation of RX CPU core and simulation of timer.

Below is a list of devices supported by this simulator.

Nickname	lickname Device name	
RX600	RX600 series	
RX200	RX200 series	



# **Chapter 2. User's Manuals**

Please read the following user's manuals together with this document.

Manual Name	Document Number
CubeSuite+ Ver.1.02.00 RX Debug	R20UT1143EJ0100
CubeSuite+ Ver.1.02.00 Message	R20UT0980EJ0100



## **Chapter 3. Range**

This section describes the functions of the RX600 series and RX200 series simulator debugger.

- (1) The simulator/debugger provides simulation functions for the RX600 series microcomputers.
- (2) The simulator/debugger supports the following RX600 series microcomputer functions:
  - All CPU instructions
  - Exception processing
  - Registers
  - All address space
  - Peripheral function (Timer, Memory Protection Unit)
- (3) The simulator/debugger does not support the following RX600 series MCU functions.

No.	Item	Remarks
1	Low power state	Simulation is stopped on the execution of a WAIT instruction.
2	Non-maskable interrupt (NMI)	
3	Reception of an interrupt during execution of any of the following instructions: (RMPA, SCMPU, SMOVF, SMOVB, SMOVU, SSTR, SUNTIL, SWHILE)	The interrupt is accepted when execution of the instruction is completed.
4	Values in memory and registers that become undefined after the execution of instructions	
5	Lower-order 16 bits of the accumulator (ACC)	A simulator debugger returns 0.



## **Chapter 4. Simulation of Peripheral Functions**

This section describes the peripheral functions of the RX600 series and RX200 series simulator debugger.

### 4.1 Timer

(1) Supported Range

The RX600 series and RX200 series simulator debugger supports a total of four compare match timer (CMT) channels, i.e. two CMT units (unit 0 and unit 1), each with two 16-bit timers.

#### (2) Control Registers

Below is a list of registers supported by this simulator.

A control register please access it at register size by all means.

Unit	Supported Control Register	Support
Unit0	CMSTR0	YES
	CMCR0、CMCR1	YES
	CMCNT0、CMCNT1	YES
	CMCOR0、CMCOR1	YES
Unit1	CMSTR1	YES
	CMCR2、CMCR3	YES
	CMCNT2、CMCNT3	YES
	CMCOR2、CMCOR3	YES

Note: YES Supported



### 4.2 Interrupt Controller

#### (1) Supported Range

The RX600 series simulator debugger supports the interrupt controller unit (ICU) that is related to the CMT and SCI. The RX200 series simulator debugger supports the ICU that is related to the CMT. The ICU can convey interrupts to the CPU but cannot activate the DTC or DMAC.

#### (2) Control Registers

Below is a list of registers supported by this simulator.

A control register please access it at register size by all means.

Supported Control Register	Support
IRn (n=028∼029)	YES
IER03	NO
IPRm (m=04~07)	YES
FIR	YES

Note: YES Supported, NO Supported only CMT function



### 4.3 Memory Protection Unit

(1) Supported Range

The RX600 series simulator debugger which RX610 group removes supports the Memory Protection Unit(MPU).

(2) Control RegistersBelow is a list of registers supported by this simulator.

A control register please access it at register size by all means.

Supported Control Register	Support
RSPAGEn (n=0~7)	YES
REPAGEn (n=0~7)	YES
MPEN	YES
MPBAC	YES
MPECLR	YES
MPESTS	YES
MPDEA	YES
MPSA	YES
MPOPS	YES
MPOPI	YES
MHITI	YES
MHITD	YES

Note: YES Supported



## **Chapter 5. Changes**

This chapter describes changes from V2.00.00 to V2.01.00 of RX simulator.

### 5.1 Expansion of a trace memory size

Added 2M, 3M to size appointment of trace memory.

5.2 Correspondence of a method of normal input and output same as an emulator In normal input and output to a debugging console, it supported an input and output method same as an emulator. There is not that changes the low level interface routine that uses with an emulator by using this method, and can use it. A method and the method that supported this time are change possibility by setting of a property panel conventionally.

5.3 Addition of PC display during program execution

RX simulator supported PC display on tool bar during program execution.

About the way to use, refer to online help or User's manual.

### 5.4 Improvement of a trace function

Stop collection of trace data during program practice, and add a function to reopen. Can confirm the trace data which collected without stopping program practice because only collection of trace data stops during program practice.

### 5.5 Removal of cautions

The following problems have been fixed.

- Caution of memory writef When panel windows write memory while the program is running, unintentional instructions may be executed.
- (2) Caution of breakpoint

When data is masked with the following data comparison conditions on a breakpoint event, the data mask may not be executed.

- GT(>))
- LT (<)
- GE (>=)
- LE (<=)
- IN scope (<=Value<=)</li>
  OUT of scope ! (<=Value<=)</li>
- (3) Caution of endian

When the SCMPU, SMOVB, SMOVF, or SMOVU instruction is executed, an incorrect result of execution will be obtained if different endian systems are used in two areas, one including an operand of comparison and the other the other operand of the comparison, or one including the source of a transfer and the other its destination.



# **Chapter 6. Cautions**

This section describes cautions for using the RX600 series and RX200 series simulator debugger.

### 6.1 Caution of time measurement on return out

#### (1) Run-Break timer

When return out is measured by run break timer, only the last instruction is measured. So the total execution time, Number of execution cycle and number of execution instructions are displayed the measurement of the last instruction.

#### (2) Trace

When specifying [Yes] in [Accumulate trace time] for using trace function, last instruction in return out execution is not added.

#### 6.2 Caution of property panel

ROM Installed size to display in a tab [setting for connection] on a property panel becomes the value that added a ROM Installed domain and size displaying to in memory classification of memory mapping.

On this account display the value that was different from the ROM Installed size that chose at the time of project making.

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