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Chapter 1. Target Devices

It is a simulator of RH850 Instructions.

Chapter 2. User's Manuals

Please read the following user's manuals together with this document.

Manual Name	Document Number
CS+ V3.02.00 RH850 Debug	R20UT3456EJ0100
CS+ V3.02.00 Message	R20UT3459EJ0100

Chapter 3. Key Word for Uninstallation

To uninstall this product, use the integrated uninstaller (uninstalls CS+).

Chapter 4. Cautions

4.1 Cautions for CPU function

4.1.1 Caution for CPU operation clock

The CPU clock operates on the frequency set up with the property "main clock" of RH850 simulator.

4.1.2 Functional simulation

- (1) RH850 core operates by architecture of each G3M/G3K/G3MH.
- (2) RH850 simulator operates in pipeline simulation mode, but accuracy is not guaranteed.
(G3K doesn't show pipeline information to a trace panel.)

4.1.3 Caution for operation of a circumference function

RH850 Instructions simulator cannot perform the simulation of a circumference function.

But when choosing the RTOS use time and RH850/E1x-FCC1 (R7F701Z034EBG) device, the OS timer function can be used.

4.1.4 Caution for reset vector

When using a made project in CS+ V3.01.00 before CS+ V3.00.00 with RH850/E1x course, please change the reset address to 0x01000000 and use it.

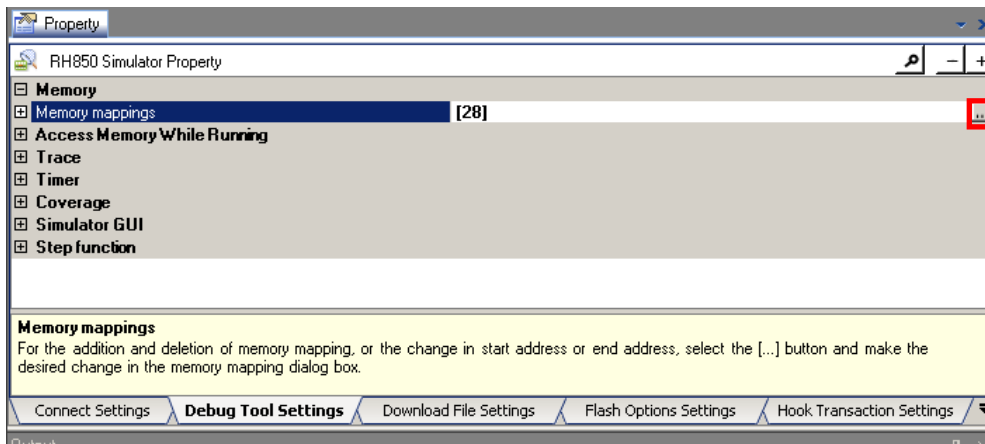
Chapter 5. Changes

5.1 Change from CS+ Simulator for RH850 V3.01.00 to V3.02.00

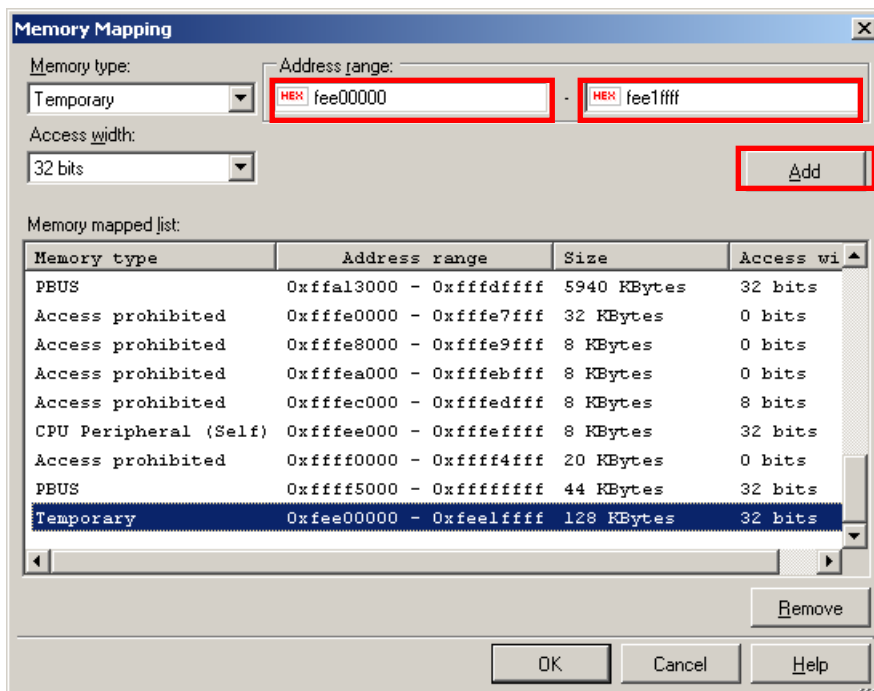
5.1.1 Addition of definition in RAM territory for evaluation.

It's possible to use Access prohibited of CPU as RAM. It's possible to use the memory territory which isn't on the device as a library for evaluation and data area.

Property -> Debug tool settings -> Memory mappings, and a Memory Mapping window is indicated.



When a temporary address range is designated, the Temporary designated as a Memory mapped list is added.



When a connection error of a simulator designated the Access Prohibited territory used by a self programming in Temporary, and occurred, please designate other Access Prohibited territory.

Chapter 6. Restrictions

6.1 Restrictions List

No.	Description	CS+V3.02.00
1	Restrictions for CPU functions	—

○ : Correspondence, -: Not correspondence(finish of correction)

6.2 Restrictions Details

6.2.1 Restriction for CPU operation clock

When "PREPARE list12, imm5, sp/imm" is being executed, and if MDP exception occurs, ep register is changed.

[Workaround] ~~Do not refer to the ep register in the MDP exception handling routine.~~

This issue has been corrected in CubeSuite+ Simulator for RH850 V2.02.00.

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