

Applilet3 for 78K V1.02.00 Release Note

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Chapter 1. Introduction

Applilet3 for 78K is a software tool to generate device driver code for on-chip peripherals. It generates device driver codes using user settings through GUI. Initialize code and API functions are provided.

Chapter 2. Target Devices

Below is a list of devices supported by the Applilet3 for 78K0R/Fx3 V3.01.00.01						
Nickname		Device name				
78K0R/FB3	μPD78F1804, μPD78F1809	PD78F1804, μPD78F1805, μPD78F1806, μPD78F1807				
	μPD78F1808, μPD78F1809	9, μPD78F1810, μPD78F1811				
78K0R/FC3	μPD78F1812, μPD78F1813	3, μPD78F1814, μPD78F1815, μPD78F1816, μPD78F1817				
	μPD78F1826, μPD78F182	7, μPD78F1828, μPD78F1829, μPD78F1830				
	μPD78F1818, μPD78F181	μPD78F1818, μPD78F1819, μPD78F1820, μPD78F1821, μPD78F1822				
78K0R/FE3	μPD78F1831, μPD78F1832, μPD78F1833, μPD78F1834, μPD78F1835					
	μPD78F1823, μPD78F1824, μPD78F1825					
78K0R/FF3	μPD78F1836, μPD78F183	μPD78F1836, μPD78F1837, μPD78F1838, μPD78F1839, μPD78F1840				
78K0R/FG3	μPD78F1841, μPD78F1842	2, μPD78F1843, μPD78F1844, μPD78F1845				
The Applilet3 is bas	sed on the following document	S.				
Manual Name Document Number						
		U19145JJ1V0UD00				
78K0R/F	Fx3 User's Manual	U19145EJ1V0UD00				

Below is a list of devices supported by the Applilet3 for 78K0R/lx3 V3.01.00.01						
Nickname		Device name				
78K0R/IB3	μPD78F1201, μPD78F1203	PD78F1201, µPD78F1203				
78K0R/IC3		D78F1211(38pin), μPD78F1213(38pin), D78F1211(44pin), μPD78F1213(44pin)				
78K0R/ID3	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	uPD78F1213(48pin), μPD78F1214(48pin), μPD78F1215(48pin) uPD78F1223, μPD78F1224,μPD78F1225				
78K0R/IE3	μPD78F1233, μPD78F1234,μPD78F1235					
The Applilet3 is ba	sed on the following documents	S.				
M	lanual Name	Document Number				
78K0R/	/Ix3 User's Manual	U19678JJ1V1UD00				
7 51010	INO COOL S MIGHIGA	U19678EJ1V1UD00				

Nickname		Device name					
78K0R/KE3	μPD78F1142/A, μPD78I	F1143/A, µPD78F1144/A, µPD78F1145/A, µPD78F1146/A					
78K0R/KF3	μPD78F1152/A, μPD78I	F1153/A, µPD78F1154/A, µPD78F1155/A, µPD78F1156/A					
	μPD78F1162/A, μPD78I	F1163/A, µPD78F1164/A, µPD78F1165/A, µPD78F1166/A,					
78K0R/KG3	μPD78F1167/A, μPD78I	F1168/A					
78K0R/KH3	μPD78F1174/A, μPD78I	F1175/A, µPD78F1176/A, µPD78F1177/A, µPD78F1178/A					
78K0R/KJ3	μPD78F1184A, μPD78F	F1185A, μPD78F1186A, μPD78F1187A, μPD78F1188A					
he Applilet3 is ba	sed on the following docum	ents					
N	lanual Name	Document Number					
701/05/	(50.11 1.14	U17854JJ8V0UD00					
/8KUR/	KE3 User's Manual	U17854EJ8V0UD00					
		U17893JJ7V0UD00					
78K0R/	KF3 User's Manual	U17893EJ7V0UD00					
	(0011 1 11	U17894JJ8V0UD00					
78K0R/KG3 User's Manual U17894EJ8V0UD00							
701657 (410.11	U18432JJ4V0UD00					
78K0R/l	KH3 User's Manual	U18432EJ4V0UD00					
		U18417JJ3V0UD00					
78K0R/	KJ3 User's Manual	U18417EJ3V0UD00					

Below is a list of devices supported by the Applilet3 for 78K0R/Kx3-A V3.01.00.01					
Nickname		Device name			
78K0R/KE3-A	μPD78F1016, μPD78F1017	PD78F1016, μPD78F1017, μPD78F1018			
The Applilet3 is base	The Applilet3 is based on the following documents				
Ma	nual Name	Document Number			
78K0P/Kv′	B-A User's Manual	U19653JJ1V0UD			
7 ONUN/NX3-A USEI S Manual		U19653EJ1V0UD			

Below is a list of de	vices supported by the Applile	et3 for 78K0R/Kx3-L V3.01.00.01				
Nickname		Device name				
701/00/1/00 1	μPD78F1000(44pin), μPD	78F1001(44pin), µPD78F1002(44pin),µPD78F1003(44pin),				
78K0R/KC3-L	μPD78F1001(48pin), μPD	78F1002(48pin), µPD78F1003(48pin)				
78K0R/KD3-L	μPD78F1004, μPD78F100	05, μPD78F1006				
78K0R/KE3-L	μPD78F1007, μPD78F100	08, μPD78F1009				
78K0R/KF3-L	μPD78F1010, μPD78F101	1, μPD78F1012				
78K0R/KG3-L	μPD78F1013, μPD78F1014					
The Applilet3 is bas	ed on the following documen	ts				
Ma	anual Name	Document Number				
		U19291JJ3V0UD00				
78K0R/K	k3-L User's Manual	U19291EJ2V0UD00				
701/05 ///	CO I II	U19459JJ1V0UD00				
/8KUR/KI	F3–L User's Manual	U19459EJ1V0UD00				
701/00/1/	22 I Haawa Manual	U19460JJ1V0UD00				
/8KUR/KU	G3-L User's Manual	U19460EJ1V0UD00				

Below is a list of devices supported by the Applilet3 for 78K0R/Lx3 V3.01.00.01						
Nickname		Device name				
78K0R/LF3	μPD78F1500, μPD78F1501	² D78F1500, μPD78F1501, μPD78F1502				
78K0R/LG3	μPD78F1503, μPD78F1504	PD78F1503, µPD78F1504, µPD78F1505				
78K0R/LH3	μPD78F1506, μPD78F1507	μPD78F1506, μPD78F1507, μPD78F1508				
The Applilet3 is based on the following documents						
Ma	nual Name	Document Number				
78K0R/Lx3 User's Manual		U19155JJ3V0UD				
70KUK/LAS USEI S Mailuai		U19155EJ3V0UD				

Below is a list of devices supported by the Applilet3 for 78K0/lx2 V3.01.00.01						
Nickname		Device name				
78K0/IY2		ιPD78F0740, μPD78F0741, μPD78F0742, ιPD78F0750, μPD78F0751, μPD78F0752				
78K0/IA2	μPD78F0743, μPD78F0744 μPD78F0753, μPD78F0754	IPD78F0743, μPD78F0744, IPD78F0753, μPD78F0754				
78K0/IB2		μPD78F0745, μPD78F0746, μPD78F0755, μPD78F0756 32pin are not supporting.				
The Applilet3 is bas	ed on the following documents	S.				
Ma	anual Name	Document Number				
78K0/lx2 User's Manual		U19353JJ3V0UD00 U19353EJ3V0UD00				

Below is a list of devices supported by the Applilet3 for 78K0/Kx2-L V3.01.00.02						
Nickname		Device name				
	μPD78F0550, μPD78F0551	l, μPD78F0552,				
78K0/KY2-L	μPD78F0555, μPD78F0556	δ, μPD78F0557				
70140/1440	μPD78F0560, μPD78F0561	l, μPD78F0562,				
78K0/KA2-L	μPD78F0565, μPD78F0566	S, μPD78F0567				
70140/1470	μPD78F0571, μPD78F0572	μPD78F0571, μPD78F0572, μPD78F0573,				
78K0/KB2-L	μPD78F0576, μPD78F0577, μPD78F0578					
	μPD78F0581(44pin), μPD78F0582(44pin), μPD78F0583(44pin),					
701/0/1/00 1	μPD78F0581(48pin), μPD78F0582(48pin), μPD78F0583(48pin),					
78K0/KC2-L	μPD78F0586(44pin), μPD78F0587(44pin), μPD78F0588(44pin),					
	μPD78F0586(48pin), μPD78F0587(48pin), μPD78F0588(48pin)					
The Applilet3 is base	ed on the following documents	3.				
Manual Name Document Number						
78K0/Kx2-L User's Manual		U19111JJ2V1UD				
/ 6NU/NX2	-L USEIS IVIAITUAI	U19111EJ2V1UD				

Chapter 3. Operating Environment

Host machine

- IBM PC/AT compatibles (Windows® 8, Windows® 7, Windows Vista®)
- Processor: 1 GHz or higher (must support hyper-threading, multi-core CPUs)
- Memory capacity: 2 GB or more recommended. Minimum requirement is 1 GB or more (64-bit Windows requires 2 G or more)
 - Hard disk capacity: 200 MB or more spare capacity
 - Display: 1024 x 768 or higher resolution, 65,536 or more colors
- All other necessary software environments in addition to WindowsOS
- .NET Framework version4.5
- Microsoft Visual C++ 2010 SP1 runtime library

Development Environments

Product Name	Version
IAR Embedded Workbench for Renesas RL78	V4.80 or later

Chapter 4. Changes

This chapter describes change of Appliet3 for 78K V1.02.00

	Description	Corresponds of Applilet3							
No		78K0R/Fx3	78K0R/lx3	78KOR/Kx3	78K0R/Kx3-A	78K0R/Kx3-L	78K0R/Lx3	78K0/lx2	78K0/Kx2-L
		V3.01.00.01	V3.01.00.01	V3.01.00.01	V3.01.00.01	V3.01.00.01	V3.01.00.01	V3.01.00.01	V3.01.00.02
1	Additional function generation file mode	-	-	-	_	-	-	-	-
2	Changes of hdwinit() function	-	-	-	-	-	-	-	-
3	Changes the setting external interrupts	/	/	/	/	-	/	/	/

^{• :} Correspondence, -: Not correspondence(finish of correction), /: Outside of function

4.1 Details of Changes

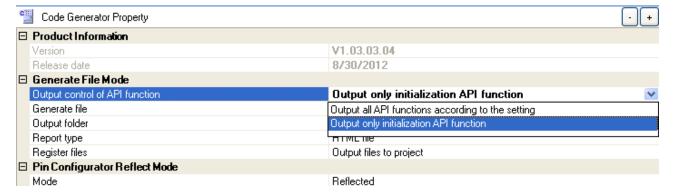
4.1.1 Additional function generation file mode

"Output control of API function" has been added to the Code Generator Property for 78K0R and 78K0.

"Output all API functions according to the setting": Outputs necessary API functions according to the GUI settings (conventional output method).

"Output only initialization API function": Outputs only initialization functions (Create functions) regardless of the GUI settings. Users can configure the settings such as interrupt functions according to their needs.

This issue has been corrected in Applilet3 for 78K V1.02.00



4.1.2 Changes of hdwinit() function

```
We have changed the initial code for the hdwinit() and main() functions.
void hdwinit(void)
{
  DI();
  R_Systeminit();
  EI();
The above code has been changed to the code given below. Accordingly, interrupts are not enabled
within the hdwinit function.
void hdwinit(void)
  DI();
  R_Systeminit();
Interrupts are now enabled within the main() function.
* Function Name: main
* Description: This function implements main function.
void main(void)
  R_MAIN_UserInit();
  /* Start user code. Do not edit comment generated here */
  while (1U)
  /* End user code. Do not edit comment generated here */
* Function Name: R_MAIN_UserInit
* Description: This function adds user code before implementing main function.
void R MAIN UserInit(void)
  /* Start user code. Do not edit comment generated here */
  /* End user code. Do not edit comment generated here */
```

When an old project is used in code generation, the definitions of variables within the main function may lead to errors.

```
[Old project]
void main(void)
  /* Start user code. Do not edit comment generated here */
  char c;
  while (1U)
[When an old project is loaded into CubeSuite+V1.03.00 and used for code generation]
void main(void)
  R_MAIN_UserInit();
  /* Start user code. Do not edit comment generated here */
  char c;
              <- error!!
  while (1U)
    ...
In that case, use { }.
void main(void)
{
  R_MAIN_UserInit();
  /* Start user code. Do not edit comment generated here */
              <- add "{"
    char c;
              <- not error!
    while (1U)
    {
              <- add "}"
```

This issue has been corrected in Applilet3 for 78K V1.01.00.

4.1.3 Changes the setting external interrupts

In the 78K0R/Kx3-L (with 44 or 48 pins), when the external interrupt INTP3 or INTP7 is selected, the code for initialization which changes PIM8 into a digital signal is not present.

This issue has been corrected in Applilet3 for 78K V1.01.00.

Chapter 5. Cautions

This section describes cautions for using Applilet3 for 78K

5.1 Cautions List

	Description		Corre	spon	ds of	code	gene	eratio	n
No.		78K0R/Fx3	78K0R/lx3	78K0R/Kx3	78K0R/Kx3-A	78K0R/Kx3-L	78K0R/Lx3	78K0/lx2	78K0/Kx2-L
		V3.01.00.01	V3.01.00.02						
1	Cautions of the LIN-bus function of UART0, UART2, UART3 or UART6.	0	0	0	0	0	0	0	0
2	Cautions of extension code, wakeup function and multimaster of serial interface IICA or IIC0	/	0	0	0	0	0	0	0
3	Cautions of CAN controllers	0	/	/	/	/	/	/	/
4	Restrictions of internal low-speed or internal high-speed oscillator trimming	0	/	0	/	/	/	/	/
5	Cautions of file merge	0	0	0	0	0	0	0	0

O: Correspondence, -: Not correspondence, /:Outside of function.

5.2 Cautions Details

5.2.1 Cautions of the LIN-bus function of UART0, UART2, UART3 or UART6

The Applilet3 for 78K is not supporting the LIN-bus functions of serial interface UART0 , UART2, UART3 or UART6.

[Workaround] There is no workaround.

5.2.2 Cautions of extension code, multimaster, wakeup function of serial interface IICA or IIC0

The Applilet3 for 78K is not supporting the extension code, multimaster, wakeup function of serial interface IIC.

[Workaround] There is no workaround.

5.2.3 Cautions of CAN controllers

The Applilet3 for 78K is not supporting the CAN Controllers. [Workaround] There is no workaround.

5.2.4 Restrictions of internal low-speed or internal high-speed oscillator trimming

Applilet3 for 78K is not equivalent to a setup of internal low-speed or internal high-speed oscillator trimming register

5.2.5 Cautions of file merge

If you select Merge File in Generate File Mode in the property of Applilet3 and the source codes are written between each comment below, the file will be merged.

/* Start user code. Do not edit comment generated here */

/* End user code. Do not edit comment generated here */

However, if the number of braces ("{" and "}") in the edited source codes (including the comments) are not the same, the edited source codes may disappear when you run the Applilet3.

[Workaround] There is no workaround.

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