

## ISL71091SEH33

Neutron Testing of the ISL71091SEH33 Radiation Hardened Ultra Low Noise, Precision Voltage Reference

### Introduction

This report summarizes the results of 1MeV equivalent neutron testing of the [ISL71091SEH33](#) precision voltage reference. The test was conducted to determine the sensitivity of the part to Displacement Damage (DD) caused by neutron or proton environments. Neutron fluences ranged from  $5 \times 10^{11} \text{ n/cm}^2$  to  $1 \times 10^{14} \text{ n/cm}^2$ . This project was carried out in collaboration with Honeywell Aerospace in Clearwater, FL, and their support is gratefully acknowledged. This report is also applicable to the other variants (ISL71091SEH20 (2.048V), ISL71091SEH40 (4.096V) and ISL71091SEH10 (10.0V)).

### Product Description

The ISL71091SEH33 is an ultra-low noise, high DC accuracy precision voltage reference with a wide input voltage range from 4.6V to 30V. It uses the Renesas advanced proprietary bipolar technology, PR40, to achieve  $5.2 \mu\text{V}_{\text{P-P}}$  0.1Hz to 10Hz noise with an initial voltage accuracy of 0.05% across the operating temperature range of  $-55^\circ\text{C}$  to  $+125^\circ\text{C}$ . This device is fabricated on Silicon on Insulator (SOI) and is immune to single event latch-up.

The ISL71091SEH33 offers a 3.3V output voltage option with  $6 \text{ ppm}/^\circ\text{C}$  temperature coefficient and also provides excellent line and load regulation. The device is offered in an 8 Ld flatpack package.

Specifications for Rad Hard QML devices are controlled by the Defense Logistics Agency (DLA) in Columbus, OH. The SMD is the controlling document and must be cited when ordering.

### Related Literature

For a full list of related documents, visit our website:

- [ISL71091SEH33](#), [ISL71091SEH20](#), [ISL71091SEH40](#), [ISL71091SEH10](#) device pages
- MIL-STD-883 test method 1017

### Content

<b>1. Test Description</b>	<b>2</b>
1.1 Irradiation Facility	2
1.2 Test Fixturing	2
1.3 Radiation Dosimetry	2
1.4 Characterization Equipment and Procedures	2
1.5 Experimental Matrix	2
<b>2. Results</b>	<b>2</b>
2.1 Attributes Data	3
2.2 Variables Data	3
<b>3. Discussion and Conclusion</b>	<b>6</b>
<b>4. Revision History</b>	<b>6</b>
<b>A. Appendix</b>	<b>7</b>

## 1. Test Description

### 1.1 Irradiation Facility

Neutron fluence irradiations were performed on the test samples on June 25, 2018, at the WSMR Fast Burst Reactor (FBR) per Mil-STD-883G, Method 1017.2, with each part unpowered during irradiation and all leads shorted. The target irradiation levels were  $5 \times 10^{11} \text{n/cm}^2$ ,  $2 \times 10^{12} \text{n/cm}^2$ ,  $1 \times 10^{13} \text{n/cm}^2$ , and  $1 \times 10^{14} \text{n/cm}^2$ . As neutron irradiation activates many of the heavier elements found in a packaged integrated circuit, the parts exposed at the higher neutron levels required (as expected) some cooldown time before being shipped back to Renesas (Palm Bay, FL) for electrical testing.

### 1.2 Test Fixturing

No formal irradiation test fixturing is involved, as these DD tests are considered bag tests, which means the parts are irradiated with all leads shorted together.

### 1.3 Radiation Dosimetry

Table 1 shows the TLD and Sulfur pellet dosimetry from WSMR indicating the total accumulated gamma dose and actual neutron fluence exposure levels for each set of samples. This dosimetry process is traceable to NIST (IAW ASTM E722).

Table 1. ISL71091SEH33 Neutron Fluence Dosimetry Data

TLD		Sulfur Pellet						
TLD #	cGy(Si) <sup>[1]</sup>	Pellet #	Distance (inches)	Exposure ID	Flu > 3MeV (n/cm <sup>2</sup> )	% Unc <sup>[2]</sup>	Total Fluence (n/cm <sup>2</sup> )	1Mev Si (n/cm <sup>2</sup> )
296	1.168E+02	7001	26.6	Free Field	7.611E+10	7.1%	6.154E+11	5.294E+11
276	3.608E+02	6412	13.45	Free Field	2.810E+11	7.1%	2.217E+12	1.966E+12
261	2.295E+03	6486	24	Free Field	1.474E+12	7.1%	1.182E+13	1.023E+13
250	1.575E+04	6465	8	Free Field	1.136E+13	7.1%	8.916E+13	7.965E+13

1. 1cGy(Si) = 1rad(Si)

2. The Uncertainty (% Unc) column is applicable only to the Fluence > 3MeV.

### 1.4 Characterization Equipment and Procedures

Electrical testing was performed before and after irradiation using the production Automated Test Equipment (ATE). All electrical testing was performed at room temperature.

### 1.5 Experimental Matrix

Testing proceeded in general accordance with the guidelines of MIL-STD-883 TM 1017. The experimental matrix consisted of five samples irradiated at  $5 \times 10^{11} \text{n/cm}^2$ , five irradiated at  $2 \times 10^{12} \text{n/cm}^2$ , five irradiated at  $1 \times 10^{13} \text{n/cm}^2$ , and five irradiated at  $1 \times 10^{14} \text{n/cm}^2$ . Three control units were used.

ISL71091SEH33 samples were drawn from lots WXM8XDB and WXM8AFBA. Samples were packaged in the 8 Ld ceramic flatpack package (package code KCJ). Samples were processed through burn-in before irradiation and screened to the SMD limits at room, low, and high temperatures before the start of neutron testing.

## 2. Results

Neutron testing of the ISL71091SEH33 is complete and the results are reported in the balance of this report. It should be understood when interpreting the data that each neutron irradiation was performed on a different set of samples; this is not total dose testing, where the damage is cumulative.

## 2.1 Attributes Data

Table 2 summarizes the neutron exposure test results. The maximum planned fluence of  $1 \times 10^{14} \text{ n/cm}^2$  was not achieved; the actual maximum fluence reached  $7.97 \times 10^{13} \text{ n/cm}^2$ .

Table 2. Attributes Data

Fluence, (n/cm <sup>2</sup> )		Sample Size	Pass <sup>[1]</sup>	Fail
Planned	Actual			
$5 \times 10^{11}$	$5.30 \times 10^{11}$	5	5	0
$2 \times 10^{12}$	$1.97 \times 10^{12}$	5	5	0
$1 \times 10^{13}$	$1.02 \times 10^{13}$	5	5	5
$1 \times 10^{14}$	$7.97 \times 10^{13}$	5	0	5

1. A Pass indicates a sample that passes all SMD limits.

## 2.2 Variables Data

The plots in Figure 1 through Figure 6 show data plots for key parameters before and after irradiation to each level. The plots show the mean of each parameter as a function of neutron irradiation. The plots include error bars at each down-point, representing the minimum and maximum measured values of the samples, although in some plots the error bars might not be visible due to their values compared to the scale of the graph.

All samples passed the post-irradiation SMD limits after all exposures up to and including  $1 \times 10^{13} \text{ n/cm}^2$ , but failed at  $1 \times 10^{14} \text{ n/cm}^2$ , with one part failing output voltage accuracy, as shown in Figure 1, and all of the parts failing line regulation as shown in Figure 3.

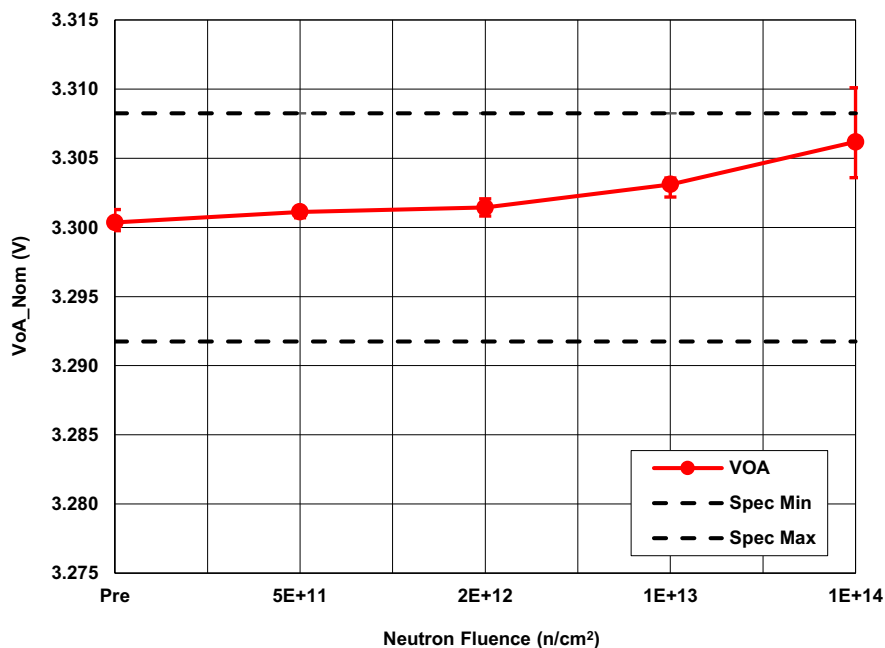


Figure 1. ISL71091SEH33 output voltage ( $V_{OA}$ ), following irradiation to each neutron fluence level. The error bars, if visible, represent the minimum and maximum measured values. The post-irradiation SMD limits are 3.29175V minimum and 3.30825V maximum ( $\pm 0.25\%$ ).

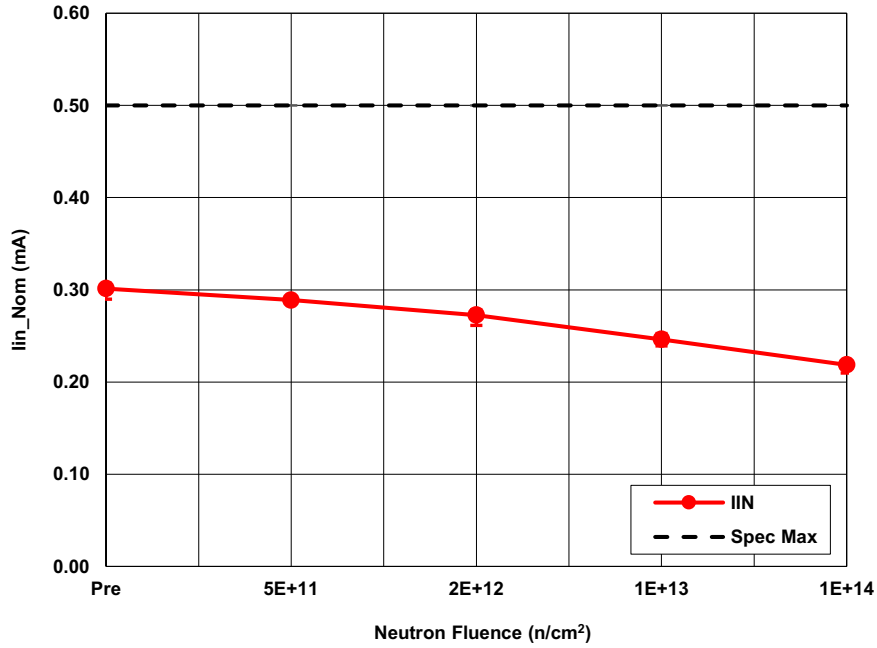


Figure 2. ISL71091SEH33 supply current (I<sub>IN</sub>), following irradiation to each neutron fluence level. The error bars, if visible, represent the minimum and maximum measured values. The post-irradiation SMD limit is 0.5mA maximum.

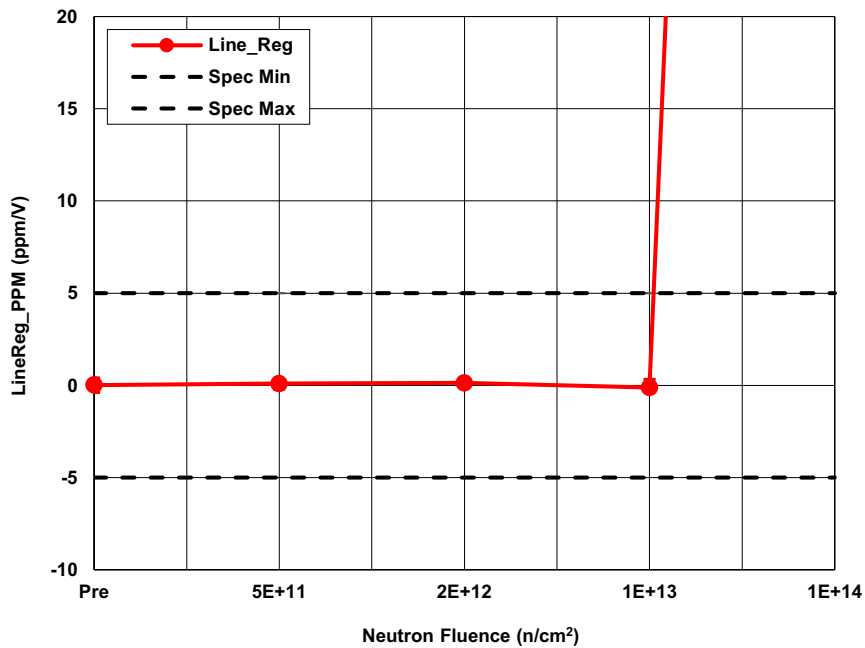


Figure 3. ISL71091SEH33 line regulation ( $\Delta V_{OUT}/\Delta V_{IN}$ ), following irradiation to each neutron fluence level. The error bars, if visible, represent the minimum and maximum measured values. The post-irradiation SMD limits are -5ppm/V minimum and +5ppm/V maximum. Note: The 1E+14n/cm<sup>2</sup> data point is not shown to show the performance of the part below that level (the average value was 228ppm/V).

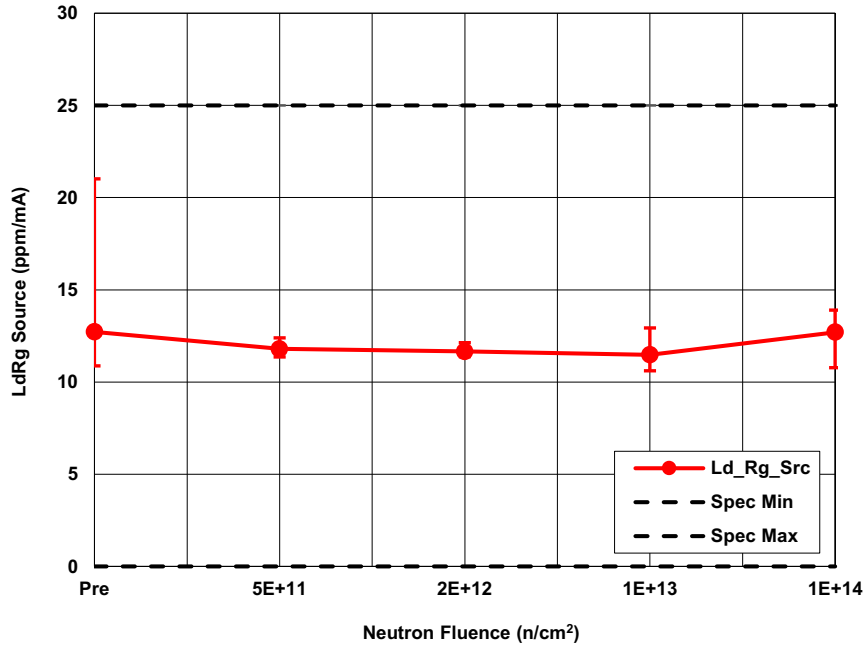


Figure 4. ISL71091SEH33 load regulation sourcing 10mA ( $\Delta V_{OUT}/\Delta I_{OUT}$ ), following irradiation to each neutron fluence level. The error bars, if visible, represent the minimum and maximum measured values. The post-irradiation SMD limit is -25ppm/V minimum and +25ppm/V maximum.

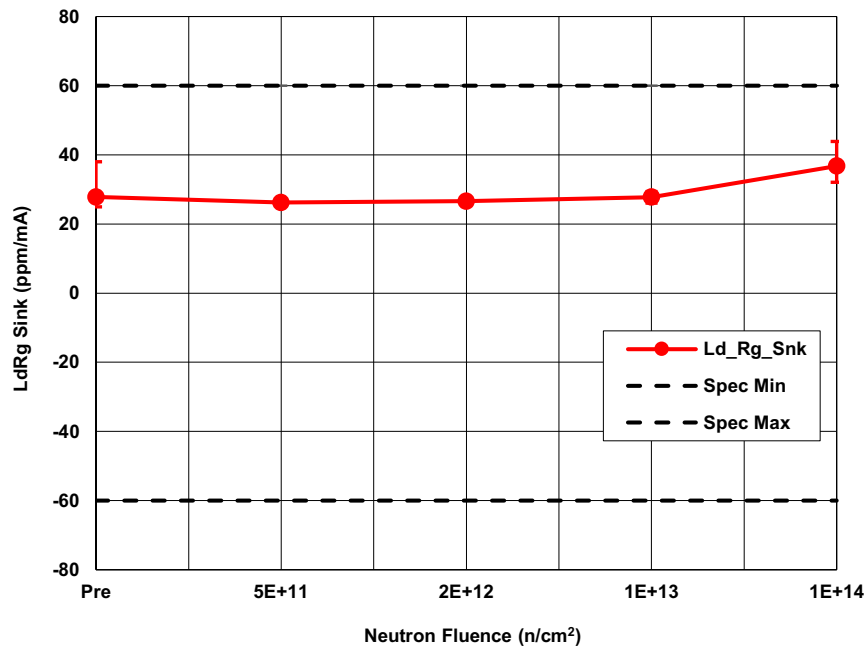


Figure 5. ISL71091SEH33 load regulation sinking 5mA ( $\Delta V_{OUT}/\Delta I_{OUT}$ ), following irradiation to each neutron fluence level. The error bars, if visible, represent the minimum and maximum measured values. The post-irradiation SMD limit -60ppm/V minimum and +60ppm/V maximum.

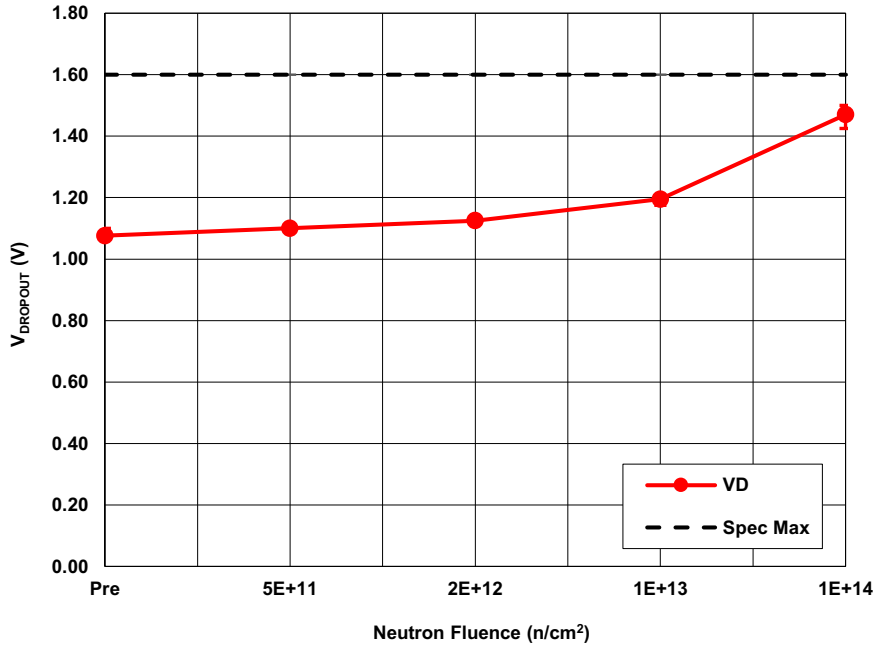


Figure 6. ISL71091SEH33 dropout voltage (V<sub>D</sub>), following irradiation to each neutron fluence level. The error bars, if visible, represent the minimum and maximum measured values. The post-irradiation SMD limit is 1.6V maximum.

### 3. Discussion and Conclusion

The results of 1MeV equivalent neutron testing of the ISL71091SEH33 precision voltage reference were reported. Parts were tested at 5x10<sup>11</sup>n/cm<sup>2</sup>, 2x10<sup>12</sup>n/cm<sup>2</sup>, 1x10<sup>13</sup>n/cm<sup>2</sup>, and 1x10<sup>14</sup>n/cm<sup>2</sup>. The results of key parameters before and after irradiation to each level are plotted in Figure 1 through Figure 6. The plots show the mean of each parameter as a function of neutron irradiation, with error bars that represent the minimum and maximum measured values. All samples passed the SMD limits after all exposures up to and including 1x10<sup>13</sup>n/cm<sup>2</sup>, but failed at 1x10<sup>14</sup>n/cm<sup>2</sup>.

### 4. Revision History

Rev.	Date	Description
1.01	Apr 30, 2026	Applied latest template. Updated Variables Data section.
1.00	Oct 21, 2019	Initial release

## A. Appendix

### A.1 Reported Parameters

Figure	Parameter	Symbol	Limit, Low	Limit, High	Units	Notes
1	V <sub>OUT</sub> Accuracy	V <sub>OA</sub>	-0.25	0.25	%	3.29175V to 3.30825V
2	Supply Current	I <sub>IN</sub>	-	0.5	mA	-
3	Line Regulation	$\Delta V_{OUT}/\Delta V_{IN}$	-5	5	ppm/V	-
4	Load Regulation, Sourcing	$\Delta V_{OUT}/\Delta I_{OUT}$	-25	25	ppm/mA	10mA
5	Load Regulation, Sinking	$\Delta V_{OUT}/\Delta I_{OUT}$	-60	60	ppm/mA	5mA
6	Dropout Voltage	V <sub>D</sub>	-	1.6	V	-

## IMPORTANT NOTICE AND DISCLAIMER

RENESAS ELECTRONICS CORPORATION AND ITS SUBSIDIARIES (“RENESAS”) PROVIDES TECHNICAL SPECIFICATIONS AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES “AS IS” AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS OR IMPLIED, INCLUDING, WITHOUT LIMITATION, ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, OR NON-INFRINGEMENT OF THIRD-PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for developers who are designing with Renesas products. You are solely responsible for (1) selecting the appropriate products for your application, (2) designing, validating, and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. Renesas grants you permission to use these resources only to develop an application that uses Renesas products. Other reproduction or use of these resources is strictly prohibited. No license is granted to any other Renesas intellectual property or to any third-party intellectual property. Renesas disclaims responsibility for, and you will fully indemnify Renesas and its representatives against, any claims, damages, costs, losses, or liabilities arising from your use of these resources. Renesas' products are provided only subject to Renesas' Terms and Conditions of Sale or other applicable terms agreed to in writing. No use of any Renesas resources expands or otherwise alters any applicable warranties or warranty disclaimers for these products.

(Disclaimer Rev.1.01)

### Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu,  
Koto-ku, Tokyo 135-0061, Japan  
[www.renesas.com](http://www.renesas.com)

### Trademarks

Renesas and the Renesas logo are trademarks of Renesas Electronics Corporation. All trademarks and registered trademarks are the property of their respective owners.

### Contact Information

For further information on a product, technology, the most up-to-date version of a document, or your nearest sales office, please visit [www.renesas.com/contact-us/](http://www.renesas.com/contact-us/).