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## Introduction

This document describes the Linux features specific to the SH7203 CPU and the RSK+ development board. The SH7203 RSK+ BSP is distributed as a compressed tar archive. During this document, you will create a tool chain, and build a working environment for the SH7203 RSK+. This document assumes the reader has a basic knowledge of Linux.

## Requirements

- To work with the SH7203 RSK+ BSP, you will need a Windows Host environment, and a GNU/Linux Development Environment.
- The Renesas High Performance Embedded Workshop (HEW) runs in a Windows environment, and needs to be installed to facilitate the downloading of images to the RSK+ board using a Renesas E10A.
- To build and develop on the  $\mu$ Clinux environment, you will need a Linux development machine. This can be a second physical machine, or a virtual machine sharing the Windows environment.

On a clean Ubuntu distribution, packages must be added to support this build process.

You will need to enter the following commands, or ensure the following packages are installed if you use a different distribution.

```
sudo apt-get install gcc-4.0
sudo apt-get install build-essential
sudo apt-get install ncurses-dev
sudo apt-get install mtd-tools
```

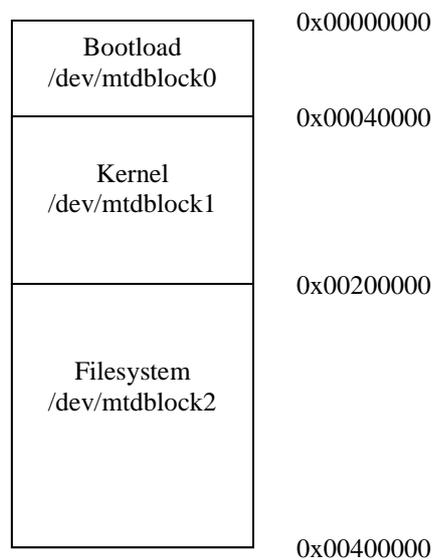
The BSP was compiled with gcc4.0, hence the specific version, gcc >4.0 should work but hasn't been tested.

The Linux development environment will require at least 1.0 GB of free space.

This document is based on work completed using Ubuntu 7.04 default install with the above packages as the development environment, and an SH7203 RSK+ development board (issue 3). Other Linux distributions may be used, but please be aware of slight differences that may occur in command usage and package management.

## SH7203 Flash Memory Partition Map

The following partition mappings have been used in building the SH7203 RSK+ BSP. These partitions are mapped to the MTD driver.



The above diagram shows the physical addresses of the partitions, and the logical naming of the partitions in the /dev subsystem.

## SH7203 RSK+ BSP Project Structure

To get started, extract the compressed archive to your home directory or desired location in the Linux environment.

```
cd ~
tar xvf /media/cdrom/sh7203-uclinux-1.0.tar.gz
```

The SH7203 BSP consists of the following locations that you need to be aware of when developing:

<p><b>Build Scripts</b></p> <p>\configs \target \stage \sh7203-images</p>	<p>Master copies of configuration files, project make-files for cross-compilation and built material.</p> <ul style="list-style-type: none"> <li>- Configuration file location</li> <li>- Built toolchain location</li> <li>- Build location for packages</li> <li>- Final location for sh7203-linux.bin and sh7203-filessystem.bin</li> </ul>
<p><b>Tool chain</b></p> <p>binutils-2.17 elf2flt  gcc-3.4.6 uClibc-0.9.27-sh2</p>	<p>The following packages are built to create a tool chain for the SH7203 by the build scripts.</p> <ul style="list-style-type: none"> <li>- Assembler and Utilities required for the GNU C Compiler</li> <li>- FLT libraries and Linker scripts to run applications in a flat memory model.</li> <li>- The GNU Compiler</li> <li>- C Libraries</li> </ul>
<p><b>Source Packages</b></p> <p>bootstrap  linux-2.6.17-sh7203 init busybox-1.5.1 protofs fbv-1.0b WebFE</p>	<ul style="list-style-type: none"> <li>- A basic functionality bootstrap which displays the splash screen and loads Linux.</li> <li>- The <math>\mu</math>Clinux source tree for the SH7203 board and drivers.</li> <li>- Provides initial initialisation and bootscripts for userspace.</li> <li>- Main utilities including the Shell and Web Server.</li> <li>- Contains the filesystem structure, utilities and images.</li> <li>- An image viewer to display images on the QVGA display.</li> <li>- A demonstration Web based front end that can control the QVGA</li> </ul>
<p><b>Extras</b></p> <p>fmtree bmtree  zips</p>	<ul style="list-style-type: none"> <li>- Flash Memory Tool Scripts for use with HEW E10A Support</li> <li>- Sample application for converting a BMP file into the special RLE encoded C array</li> <li>- A container folder that holds the compressed packages.</li> </ul>

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## Build Scripts

The build directory should be the base directory for compilation of the SH7203 BSP and tool chain. The Makefile in the build directory will build the tool chain, followed by the BSP the first time it is run.

Execute the following in the projects root to begin:

```
cd build
make
sudo make protofs-install
```

The first build takes around 45 minutes. The build scripts will extract the required packages from compressed files stored in the zips directory to create the entire SH7203 Tool chain, and BSP.

The Build directory has been created such that the top level makefile can control the entire build process. The following actions are supported.

<b>make</b>	Will build any updated packages.
<b>make clean</b>	Cleans the BSP but will not remove the tool chain.
<b>make distclean</b>	Cleans both BSP and tool chain, requiring a complete rebuild.

You can also build specific targets in the BSP as long as there is a prebuilt tool chain; for example:

```
make linux-7203
make fbv-1.0b
make busybox
make WebFE
```

Rebuilding any package which is installed to the filesystem will require a re-make of the filesystem image using:

```
sudo make protofs-install
```

*\*Root access is required to create the device nodes in the /dev directory.*

Whilst in the build directory, you can call the package makefile directly to access more targets. Use the make -f option for this.

```
make -f Makefile.linux-sh7203 build
make -f Makefile.linux-sh7203 configure
```

*If you wish to add new packages to be built for the SH7203 RSK+  $\mu$ Clinux distribution, it is recommended that you create a directory to contain the source code and build, and create a makefile in the build directory that uses the environment provided.*

*You can copy the existing makefiles for examples of creating your own packages.*

## Toolchain

The SH7203 RSK+ toolchain consists of the following packages.

```
binutils-2.17
elf2flt-sh2
gcc-3.4.6
uClibc-0.9.27-sh2
```

Currently the toolchain is configured by default to support the C language, and is targeted to the SH2 processor with Big Endian (MSB).

Elf2flt libraries are installed into the toolchain to provide flat executable support for userspace programs. This is used by passing the "-Wl,-elf2flt" flags at compilation and linking.

The toolchain when built is put into the target directory under build. This destination is defined by the make variable \$(PREFIX).

## **Bootstrap**

The bootstrap directory contains the source for a basic bootloader for the SH7203 RSK+. This bootloader initialises the CPU and board registers, the bus state controller, SDRAM, flash and serial UART for operation.

The serial UART is currently configured to run at 19200 baud with 8 Bits, no Parity, 1 Stop Bit and no Flow Control. (19200,n,8,1)

The bootloader will perform some early checks to test the current bus width mode the board has been set to. In order to support the QVGA screen, the SH7203 RSK+ board must be set in 16 bit bus width by setting SW4 bit 10 to the off position. If this is incorrectly set, a warning will be displayed on the serial output indicating a change needs to be made.

The bootloader is also responsible for initialising the QVGA display, loading a splash image, and updating the QVGA. Due to space constraints, the boot splash image is stored as a custom type RLE image with a simplistic extraction algorithm.

An example utility called 'bmprle' has been provided in for converting BMP files into the RLE encoding format, as a C Array. The image is compiled into image.c along with the extraction algorithms. These can be changed and updated to support new images or other forms of image compression if the developer wishes.

The final task of the bootloader is to pass control to the decompression routines of the Linux compressor which takes responsibility of extracting Linux to the correct location in SDRAM.

To build the bootstrap individually and create an sh7203-linux.bin file in the build/sh7203-images directory use the following command in the build directory:

```
make bootstrap
```

## **Linux Kernel**

The SH7203 Linux Kernel has a preconfigured default configuration to ensure a working binary can be built. To restore this configuration and then configure the Kernel, type the following in the build directory;

```
make -f Makefile.linux-2.6.17-sh7203 default_config  
make -f Makefile.linux-2.6.17-sh7203 configure  
make -f Makefile.linux-2.6.17-sh7203
```

When the new kernel has been built, re-link it into the sh7203-linux.bin by rebuilding the bootstrap as above.

## **Init and Booting scripts**

When the SH7203 Board boots; Linux will execute /sbin/init. This program is contained in the "init" directory to be built and installed by the build scripts.

Also contained in "init" is an rc script file which is executed by init. This script is responsible for calling BusyBox to install on a fresh file system at first boot and setting up ethernet and boot parameters.

Customise this file to change boot parameters or configure the boot process.

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## **BusyBox**

BusyBox 1.5.1 has been built to provide the SH7203 with the core functionality of a Linux Console based Operating System. In the SH7203 BSP BusyBox has been split into separate components to improve performance of the running Linux console.

*No Shared Library support.*

Currently the BusyBox binary executables are not able to share common code. This leads to large run time of applications when performing copy operations to instantiate a new process. One of BusyBox's features is to build all of the executables into a single file. However this adds to the quantity of data to copy on a process creation. To overcome this and improve performance, the BusyBox utilities have been split into several executables containing related utilities.

Currently there are four BusyBox categories:

- Shellinit (MSH Shell)
- Coreutils (ls, cp, mv, ...)
- netutils (ifconfig, ping, ...)
- miscutils (insmod, mount, ...)

*Fork and Vfork on  $\mu$ Clinux:*

When adding utilities or changing source on the SH7203, is it important to remember that the fork() system call is not supported, and where possible the vfork() call should be used instead to allow process execution.

When building BusyBox, several options have been built into the makefile to accommodate the split files of the SH BusyBox. The following commands can be issued in the build directory:

To perform a complete build of all of the BusyBox utilities

```
make -f Makefile.busybox-1.5.1
```

or to configure and make individual binaries,

```
make -f Makefile.busybox-1.5.1 coreutils.config  
make -f Makefile.busybox-1.5.1 coreutils.busybox
```

where coreutils is one of:

```
shellinit coreutils netutils miscutils
```

*Installing on the board:*

As part of the boot process provided by /etc/rc, all executable binaries in the /busybox directory will be called with the '--install -s' options at bootup. This facilitates the first time install of the symlinks and keeps them up to date if new binaries are added with more functionality.

When the BusyBox binaries are rebuilt they can be copied directly onto the flash filesystem into /busybox. The boot scripts will update the symlinks on the next power cycle of the board. The boot scripts will not however remove symlinks if applets are removed from the utilities or moved.

## **Web Front End (WebFE)**

The demonstration web front end of the SH7203 RSK+ board is served by a skeletal web server called httpd. The web server is an optional part of a BusyBox build and is configured into the BusyBox object – '/busybox/netutils'. The server is invoked via the symbolic link '/sbin/httpd' at system startup from the configuration file '/etc/rc'. The BusyBox httpd server takes only one parameter - the pathname of a directory to use as the root of its website. The default is '/www'.

Structure of the demonstration website under /www:

Index.html redirects the web browser to the cgi-bin/renesas.cgi script. This script performs the input parsing, control, and outputs a formatted HTML page.

images/ stores the images for rendering the web interface

cgi-bin/ - hosts programs to be made available for execution by a web browser. Programs may be executables compiled for the SH2 architecture or shell scripts to be executed by /bin/sh. In this demonstration, the cgi-bin directory hosts a single shell script 'renesas.cgi' optimised for execution by the lightweight default shell, /bin/sh.

## USB Host and Function Controller

The SH7203 CPU has an internal USB Host and Peripheral Controller Module.

The USB module can only be operated in one mode at a time. To facilitate this, the drivers for the two USB modes have been built as separate loadable modules, and loading them is performed at boot time by the /etc/rc script. This script will default to load the USB Host Module Driver, unless SW3 is held during boot. If SW3 is held when the rc script executes, the board will be set in USB Function Controller mode.

### USB Host:

The USB Host Module Driver can be found in the Linux configuration under Device Drivers -> USB Support -> M66596 Controller (Host Only)

For the board to support loading and mounting of Mass Storage Devices this option also needs to be selected in this menu. When a USB memory stick is inserted to the board it will be available in the '/dev' structure as '/dev/sda1'

### USB Function Controller:

The USB Function Module Driver can be found in the Linux configuration under Drivers -> USB -> Gadget

The SH7203 RSK+ USB Function Controller driver is intended to present the board to a USB host as a Mass Storage Device. This is achieved in combination with the File Storage Gadget (g\_file\_storage.ko) and the USB function controller driver. The file that is presented to the USB host can be any filesystem the host accepts, but it is recommended to make a FAT file system to enable compatibility with both Windows and Linux.

To create a new image file of a different size, follow the procedure below:

- 1) `dd if=/dev/zero of=massfile.img bs=1k count=750`

Count can be adjusted to create a file of any size, in this case a 750Kb file will be generated. Make sure the file will fit on the target device.

- 2) `fdisk massfile.img`

Enter the following sequence of commands to the menu.

x : enter the expert menu.

c : set the cylinders

l : This is only a theoretical number as we don't have a physical drive - but one is the easiest number to manage

r : return to main menu

n : New partition

p : Create a Primary Partition

1 : Partition 1

1 : First cylinder is 1

t : Set the partition type

e : W95 Fat 16 (LBA)

w : Write and quit

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- 3) You can now use this file as a parameter to the 'g\_file\_storage' gadget. When first accessed Windows should detect that the drive is clean and asks you if you would like to format the drive.
- 4) Please see /etc/rc for an example of how the mass storage device is loaded and how the modules are chosen at boot time to decide which mode the board will operate in.

## Known Observations

The Web Front End is not as responsive as the button response time. There is a great deal more processing being performed, through scripting and the /bin/sh shell. This could be improved in the future by taking a single binary approach, or solving the shared library issues.

The Frame Buffer Viewer application (FBV) uses 'mmap' to obtain a pointer to the frame buffer. This call has known limitations on the  $\mu$ Clinux platform and so the FBV passes flags to the kernel that are unexpected. The kernel responds to this by outputting a "BUG" warning from nommu.c. This is harmless and can be ignored or hidden by selecting to hide BUG warnings in the kernel configuration.

The USB Pen Drive is not always loaded completely. It may be necessary upon insertion, to remove and re-insert the pen drive if it does not mount successfully. The drive is successfully mounted when the 'Web Front End' shows the "Unmount" option.