Go Configure Development Board R1.0

Introduction

This quick start guide is intended to familiarize new users with the Go Configure Development Board R1.0. The document provides step-by-step instructions for working with the board.

Contents

Figures

1. Install the Software

Download and install the latest version of the Go Configure™ Software Hub from the Go Configure Software Hub. Version 6.50 or newer is supported.

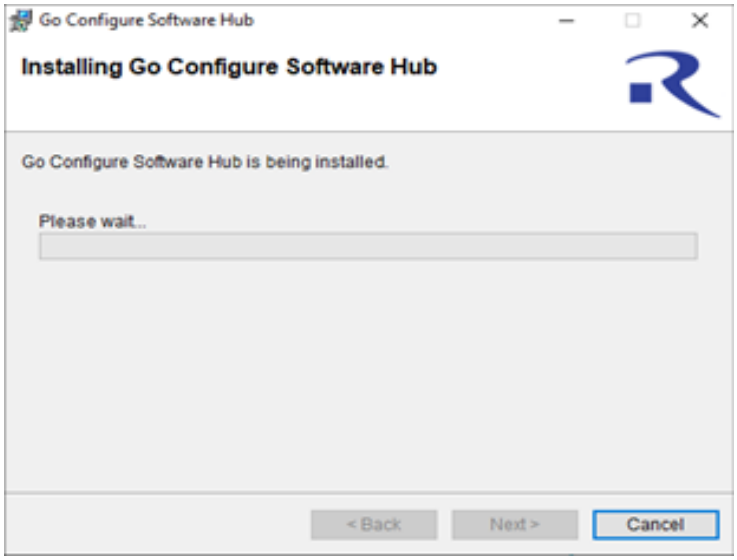

Figure 1. Software Installation

2. Prepare the Working Setup

For work with ForgeFPGA™ products, take a corresponding Socket Card, use tweezers or another tool to place the target IC into a plastic socket. If the Go Configure Development Board (GCDB) is used with the GreenPAK™ Extension Card, in the same way, take the corresponding Socket Adapter and place the target IC into it, considering the location of pin #1 (dot) on the target IC and plastic. The Socket Adapter itself should be connected to a 20-pin or 32 pin socket connector on the GreenPAK Socket Adapter and then attached to the GCDB.

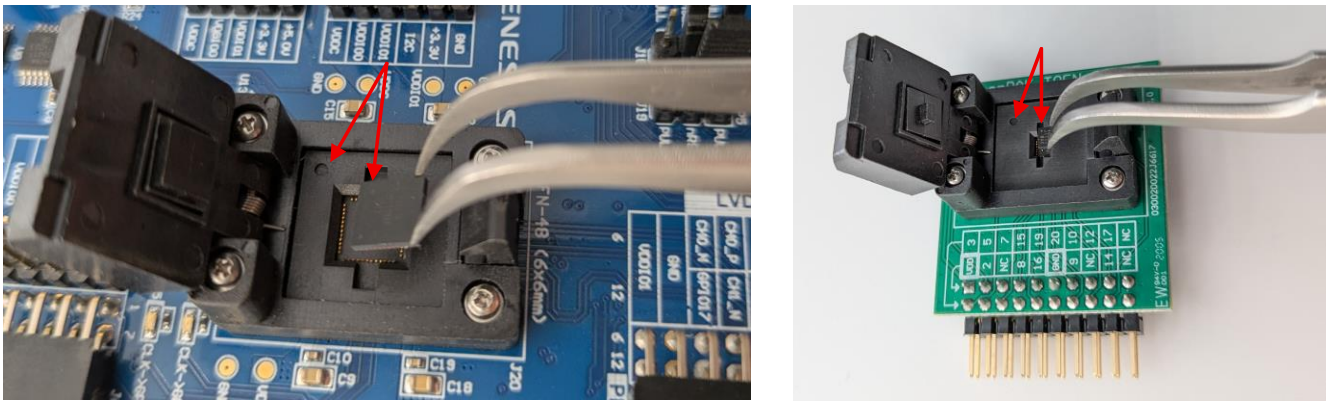

Figure 2. Prepare the Working Setup

3. Assemble the Working Setup

Attach the target board (Socket Card #1 shown as an example) to the GCDB. Connect the GCDB to a host PC via USB cable attached to the USB DATA port. Optionally, connect an additional USB power supply to the USB POWER port or a proper DC source to the DC IN port.

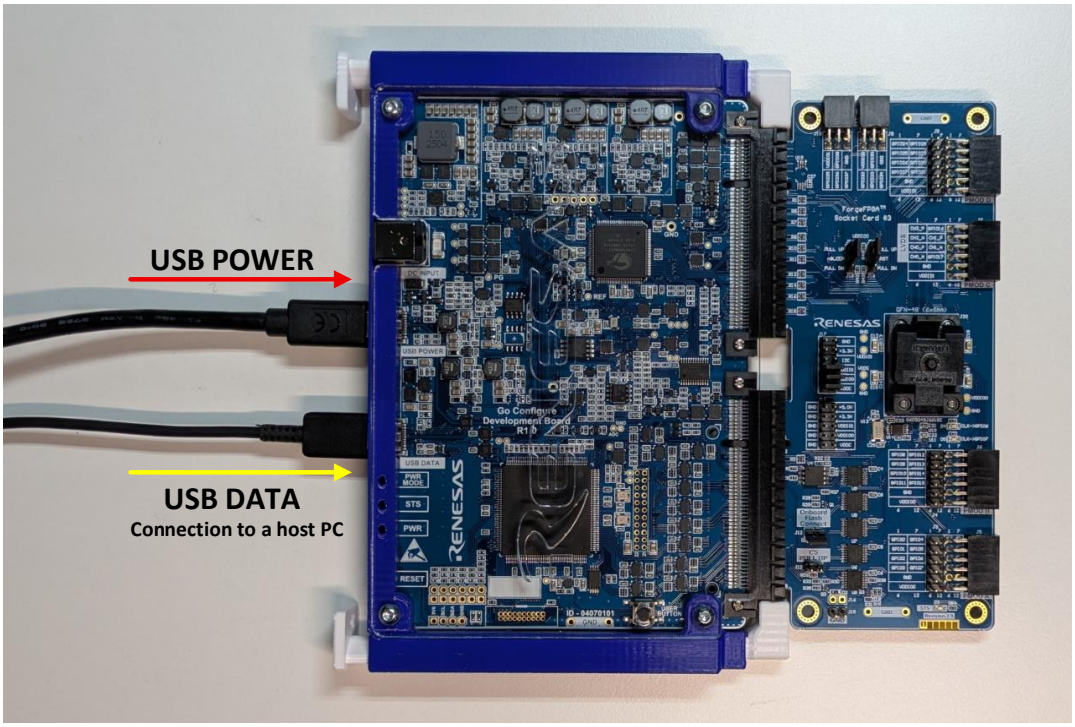

Figure 3. Setup with ForgeFPGA Socket Card

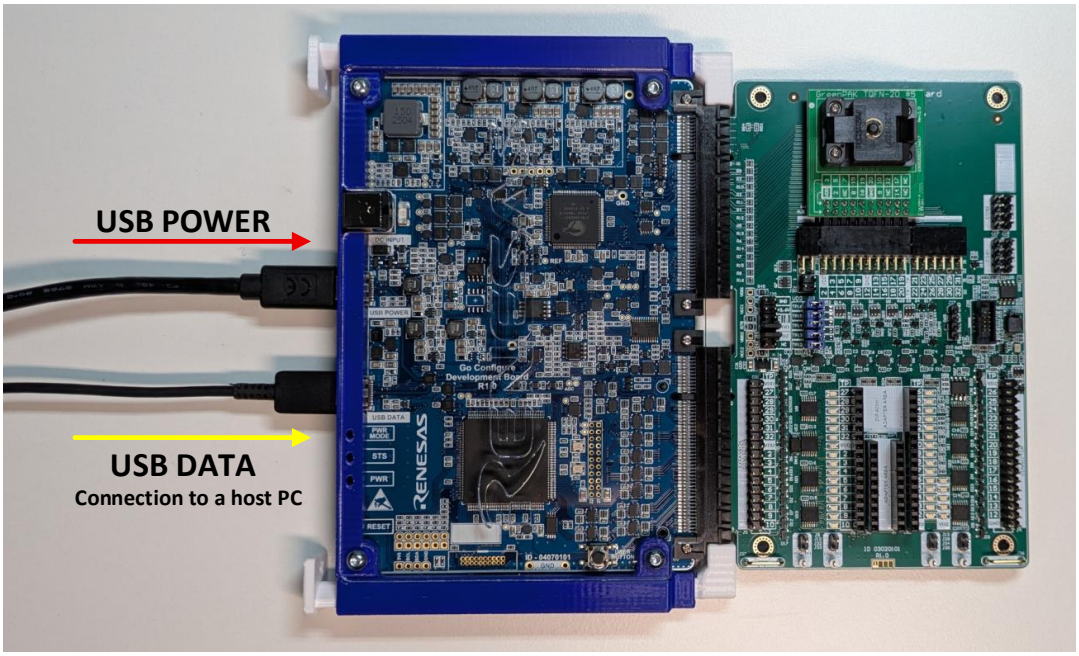

Figure 4. Setup with GreenPAK Extension Card

4. Create the Project

Open the Go Configure Software Hub and create a new project or open an existing project based on the target IC. The SLG47910V is shown as an example. Define operation conditions (V_{DD} and temperature ranges). Create the desired design.

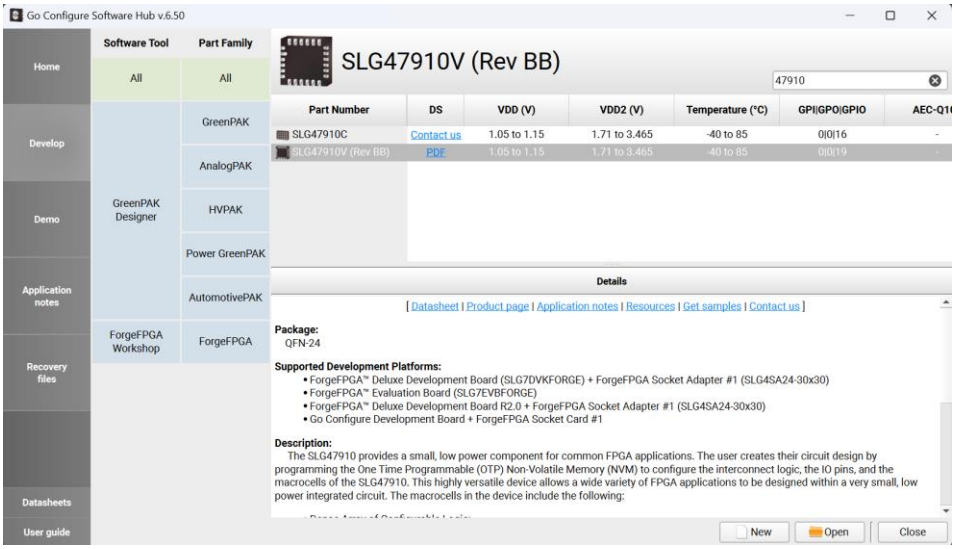

Figure 5. Creating the Project

5. Select the Tool

Click the “Debug” icon on the main toolbar menu and select the “Go Configure Development Board” as an active tool.

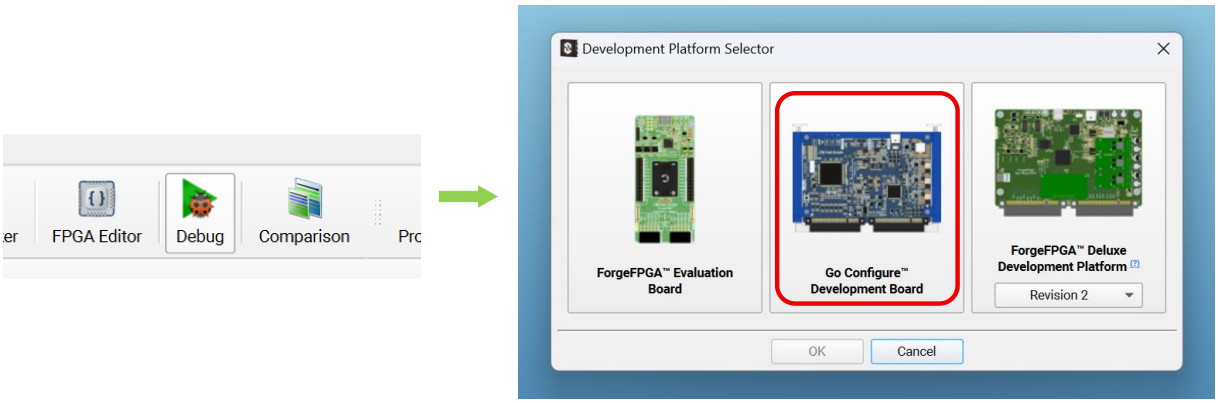

Figure 6. Tool Selection

6. Work with the ForgeFPGA

In the “Debugging Controls” section, the user can find the standard options for working with the ForgeFPGA target devices. Among them are emulation, programming, clock configuration, programming FLASH IC, and others.

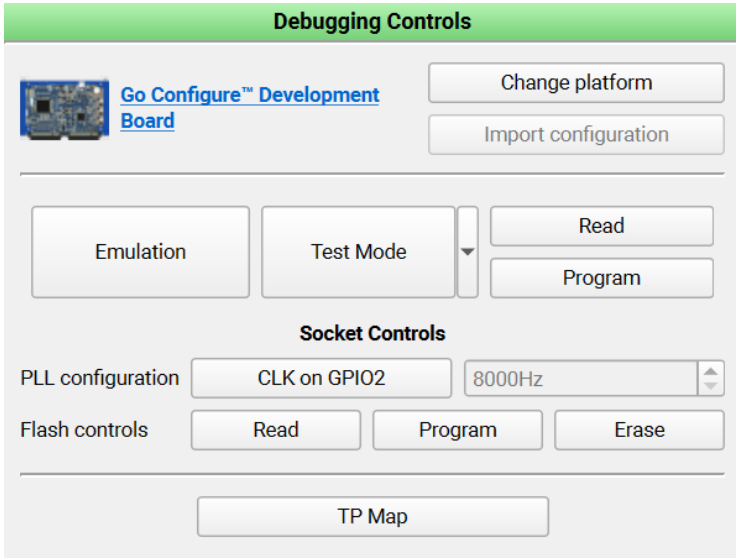

Figure 7. Work with the ForgeFPGA

7. Work with the GreenPAK

For the GreenPAK products, all the standard options are available in the “Debugging Controls” section. Among them are emulation, programming (OTP and MTP devices), reading IC, and others. The set of available options will vary depending on the target IC family and the specific part number.

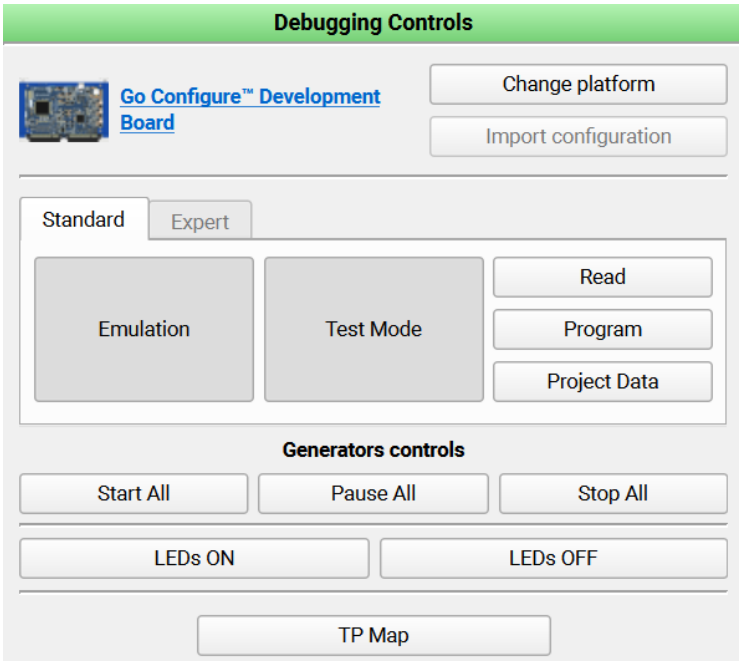

Figure 8. Work with the GreenPAK

8. Revision History

Revision	Date	Description
1.00	Nov 7, 2025	Initial release
1.01	Dec 12, 2025	Fixed typos Updated the document format Section 6. Emulate or Program the Chip split into two new sections - 6. Work with the ForgeFPGA and 7. Work with the GreenPAK