

RA8D2 Group

Evaluation Kit for RA8D2 Microcontroller Group
EK-RA8D2 v1
Quick Start Guide

Renesas RA Family
RA8 Series

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1. Precaution against Electrostatic Discharge (ESD)

A strong electrical field, when exposed to a CMOS device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop the generation of static electricity as much as possible, and quickly dissipate it when it occurs. Environmental control must be adequate. When it is dry, a humidifier should be used. This is recommended to avoid using insulators that can easily build up static electricity. Semiconductor devices must be stored and transported in an anti-static container, static shielding bag or conductive material. All test and measurement tools including work benches and floors must be grounded. The operator must also be grounded using a wrist strap. Semiconductor devices must not be touched with bare hands. Similar precautions must be taken for printed circuit boards with mounted semiconductor devices.

2. Processing at power-on

The state of the product is undefined at the time when power is supplied. The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the time when power is supplied. In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the time when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the time when power is supplied until the power reaches the level at which resetting is specified.

3. Input of signal during power-off state

Do not input signals or an I/O pull-up power supply while the device is powered off. The current injection that results from input of such a signal or I/O pull-up power supply may cause malfunction and the abnormal current that passes in the device at this time may cause degradation of internal elements. Follow the guideline for input signal during power-off state as described in your product documentation.

4. Handling of unused pins

Handle unused pins in accordance with the directions given under handling of unused pins in the manual. The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of the LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible.

5. Clock signals

After applying a reset, only release the reset line after the operating clock signal becomes stable. When switching the clock signal during program execution, wait until the target clock signal is stabilized. When the clock signal is generated with an external resonator or from an external oscillator during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Additionally, when switching to a clock signal produced with an external resonator or by an external oscillator while program execution is in progress, wait until the target clock signal is stable.

6. Voltage application waveform at input pin

Waveform distortion due to input noise or a reflected wave may cause malfunction. If the input of the CMOS device stays in the area between V_{IL} (Max.) and V_{IH} (Min.) due to noise, for example, the device may malfunction. Take care to prevent chattering noise from entering the device when the input level is fixed, and also in the transition period when the input level passes through the area between V_{IL} (Max.) and V_{IH} (Min.).

7. Prohibition of access to reserved addresses

Access to reserved addresses is prohibited. The reserved addresses are provided for possible future expansion of functions. Do not access these addresses as the correct operation of the LSI is not guaranteed.

8. Differences between products

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This Evaluation Kit is only intended for use in a laboratory environment under ambient temperature and humidity conditions. A safe separation distance should be used between this and any sensitive equipment. Its use outside the laboratory, classroom, study area, or similar such area invalidates conformity with the protection requirements of the Electromagnetic Compatibility Directive and could lead to prosecution.

The product generates, uses, and can radiate radio frequency energy and may cause harmful interference to radio communications. There is no guarantee that interference will not occur in a particular installation. If this equipment causes harmful interference to radio or television reception, which can be determined by turning the equipment off or on, you are encouraged to try to correct the interference by one or more of the following measures:

- Ensure attached cables do not lie across the equipment.
- Reorient the receiving antenna.
- Increase the distance between the equipment and the receiver.
- Connect the equipment into an outlet on a circuit different from that which the receiver is connected.
- Power down the equipment when not in use.
- Consult the dealer or an experienced radio/TV technician for help.

Note: It is recommended that wherever possible shielded interface cables are used.

The product is potentially susceptible to certain EMC phenomena. To mitigate against them it is recommended that the following measures be undertaken:

- The user is advised that mobile phones should not be used within 10 m of the product when in use.
- The user is advised to take ESD precautions when handling the equipment.

The Evaluation Kit does not represent an ideal reference design for an end product and does not fulfill the regulatory standards for an end product.

Renesas RA Family

EK-RA8D2 v1
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1. Introduction

This Quick Start Guide (QSG) provides:

- An overview of the Quick Start example project (QSEP) that is pre-programmed onto the EK-RA8D2.
- Instructions for running the Quick Start example project.
- Instructions for importing, modifying, and building the Quick Start example project using Flexible Software Package (FSP) and e²studio Integrated Development Environment (IDE).

1.1 Assumptions and Advisory Notes

1. **Tool experience:** It is assumed that the user has prior experience working with IDEs such as e²studio and terminal emulation programs such as Tera Term.
2. **Subject knowledge:** It is assumed that the user has basic knowledge about microcontrollers, embedded systems, and FSP to modify the example project described in this document.
3. **Default jumper settings:** Prior to running the Quick Start example project or programming the EK-RA8D2 board, default jumper settings must be used. Refer to the EK-RA8D2 user's manual for the default jumper configuration.
4. **Screenshots:** The screenshots provided throughout this document are for reference. The actual screen content may differ depending on the version of software and development tools used.

2. Kit Contents

The following components are included in the kit:

1. EK-RA8D2 v1 board
2. Parallel Graphics Expansion Board 1
3. Camera FFC cable
4. Camera Expansion Board
5. Display mounting hardware (spacers and fixing screws)

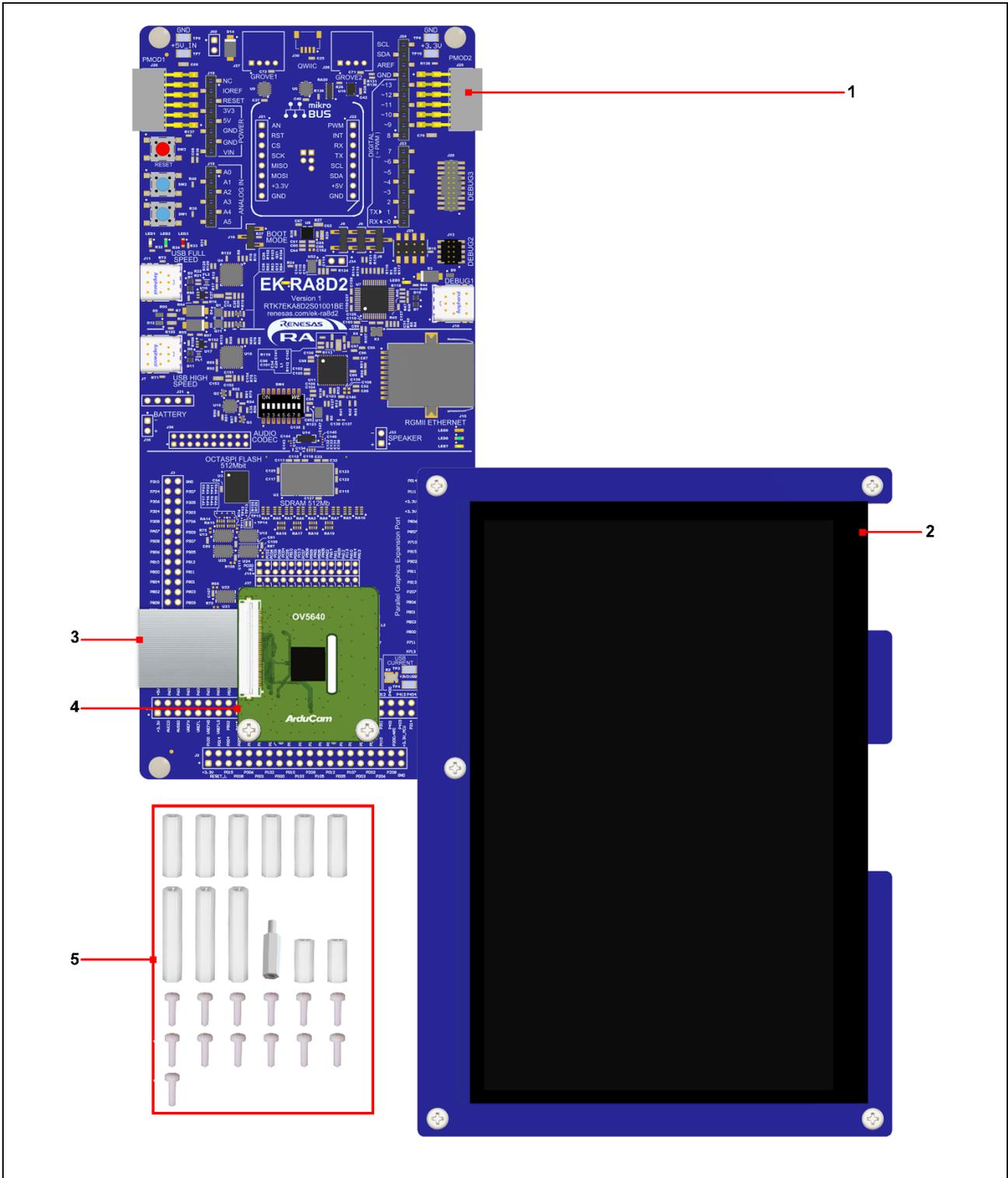


Figure 1. EK-RA8D2 Kit Contents

3. Kit Assembly

Connect the Parallel Graphics Expansion Board 1 and Camera Expansion Board to the EK-RA8D2 using the screws and spacers provided as shown below:

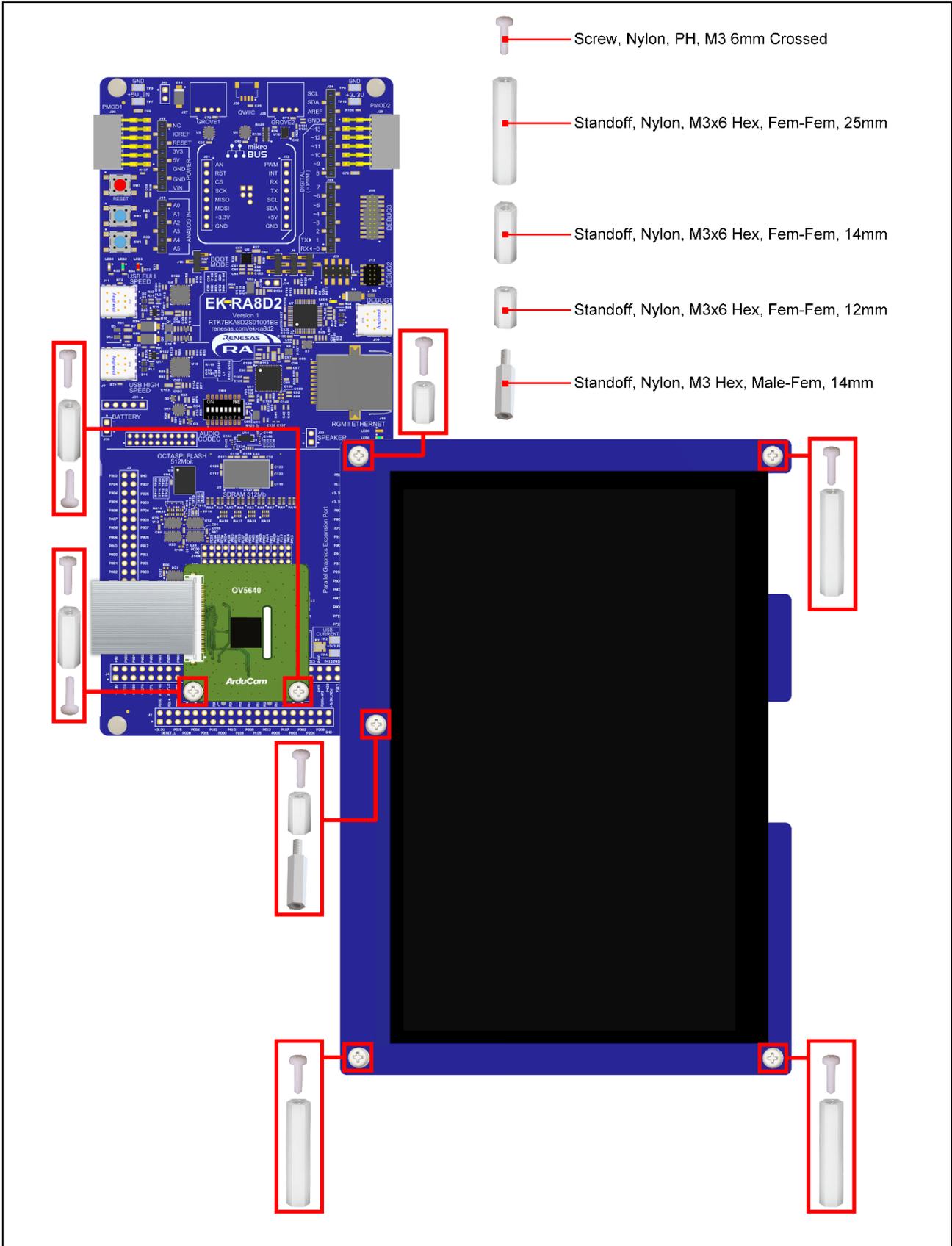


Figure 2. Kit Assembly

4. Overview of the Quick Start Example Project

The Quick Start example project allows the user to change the frequency and intensity of the on-board user LED1 (blue) using the user switches (SW1 and SW2). The supported frequencies are 1 Hz, 5 Hz, and 10 Hz and the supported intensities are 10%, 50%, and 90%.

When the EK-RA8D2 board running the Quick Start example project is connected to a host PC via USB debug J10, the welcome menu is displayed on a terminal console. The QSEP can also be navigated using the Parallel Graphics Expansion Board 1.

4.1 Quick Start Example Project Flow

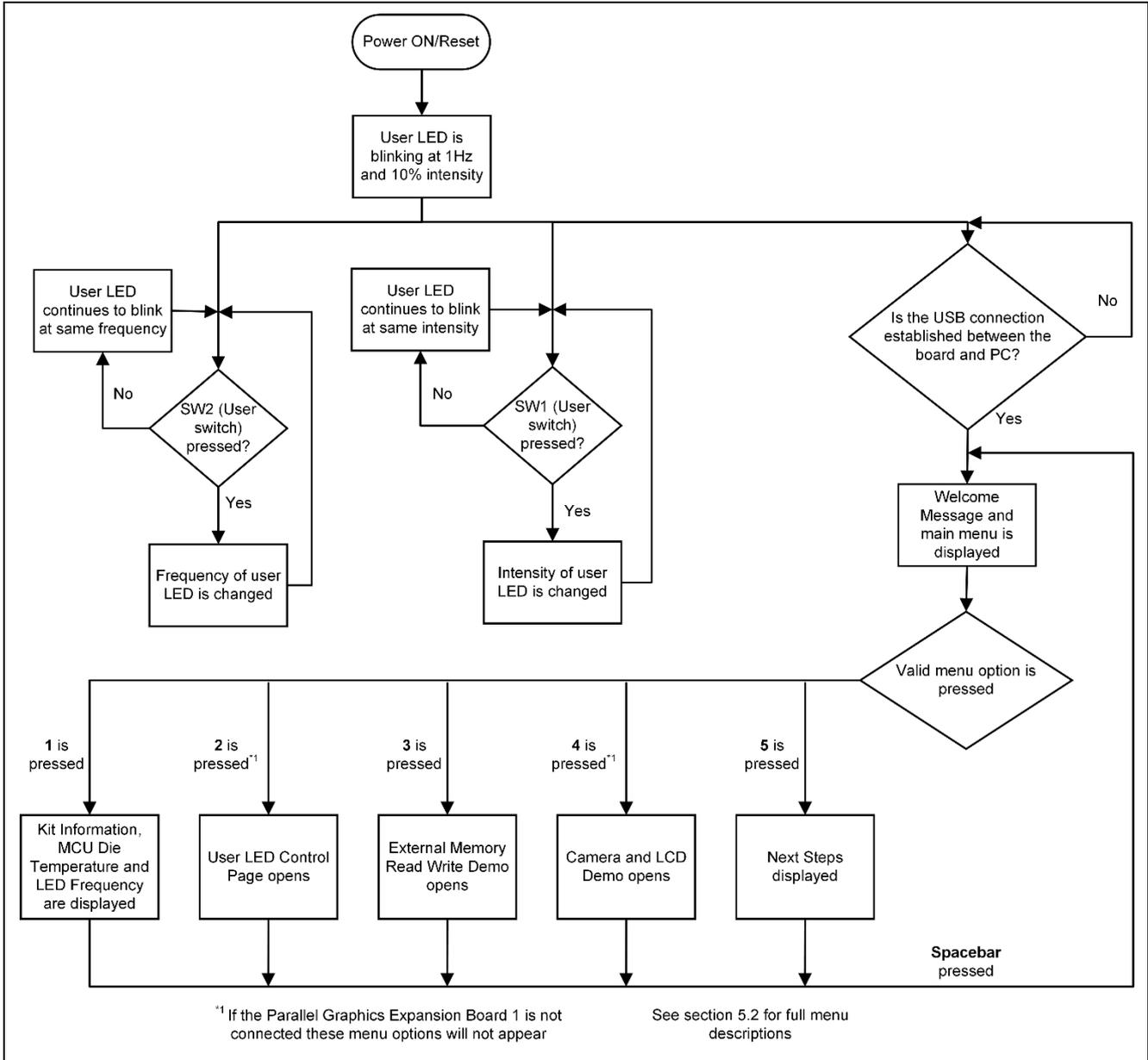


Figure 3. Quick Start Example Project Flow

5. Quick Start Example Project

This section lists the requirements and instructions to power up the EK-RA8D2 board and run the Quick Start example project.

Hardware Requirements

- EK-RA8D2 board
- Parallel Graphics Expansion Board 1
- Camera Expansion Board
- Camera Flat Flexible Cable (FFC)
- USB-A / USB-C to USB-C cable
- A PC with at least one USB port

Software Requirements

- Windows® 11 operating system (or later)
- USB Serial Drivers (included in Windows Drivers)
- Tera Term (or similar) terminal console application

5.1 Connecting and Powering Up the EK-RA8D2 Board

1. Connect the Parallel Graphics Expansion Board 1 to J1
2. Connect the Camera Expansion Board to J35 using the FFC cable
3. Connect the type-C end of the USB-C cable to USB Debug port (J10) of the EK-RA8D2 board.
4. Connect the other end of this cable to the USB port of the host PC*. Power LED (LED4) on the EK-RA8D2 board lights up white, indicating that the EK-RA8D2 board is powered on.

* This board requires significant current to run the display, use a root host PC port, or a powered hub.

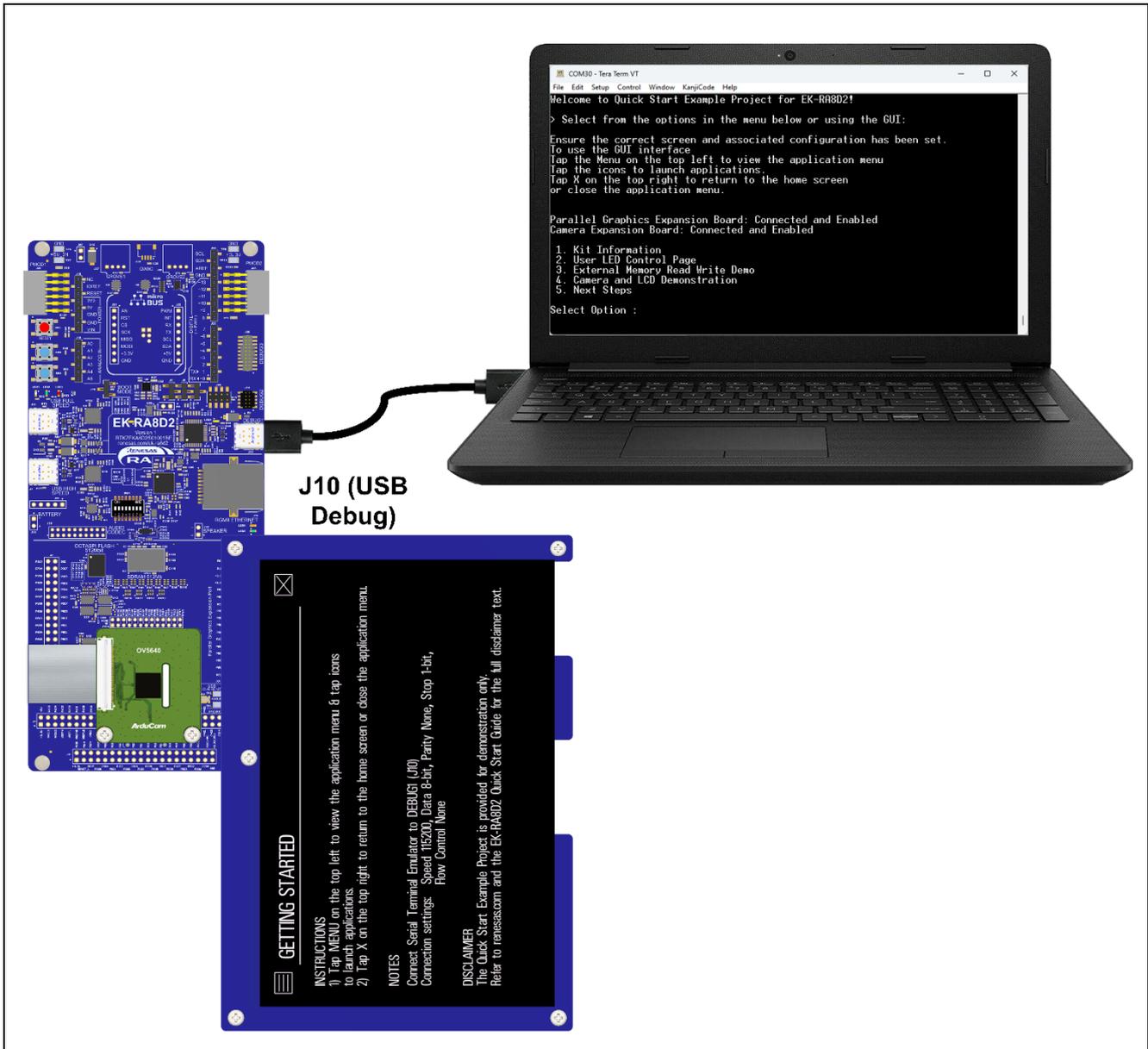


Figure 4. Connecting the EK-RA8D2 Board to the Host PC via USB Debug

5.2 Running the Quick Start Example Project

To run the Quick Start example project, use the following instructions:

1. On power up or RESET, the three user LEDs will take on the following states:
 - LED1 blue - Blinking at 1 Hz frequency and at 10% intensity
 - LED2 green - Steady, full intensity
 - LED3 red - Off
2. Press the user switch (SW1) on the EK-RA8D2 board to change the intensity of the user LED1 (blue). With every press of the user switch (SW1), the intensity will switch from 10% to 50% to 90% and cycle back.
3. Press the user switch (SW2) on the EK-RA8D2 board to change the blinking frequency of the user LED1 (blue). With every press of the first user switch (SW2), the frequency will switch from 1 Hz to 5 Hz to 10 Hz and cycle back.
4. Upon connecting the EK-RA8D2 to power, the following screen will appear on the Parallel display.

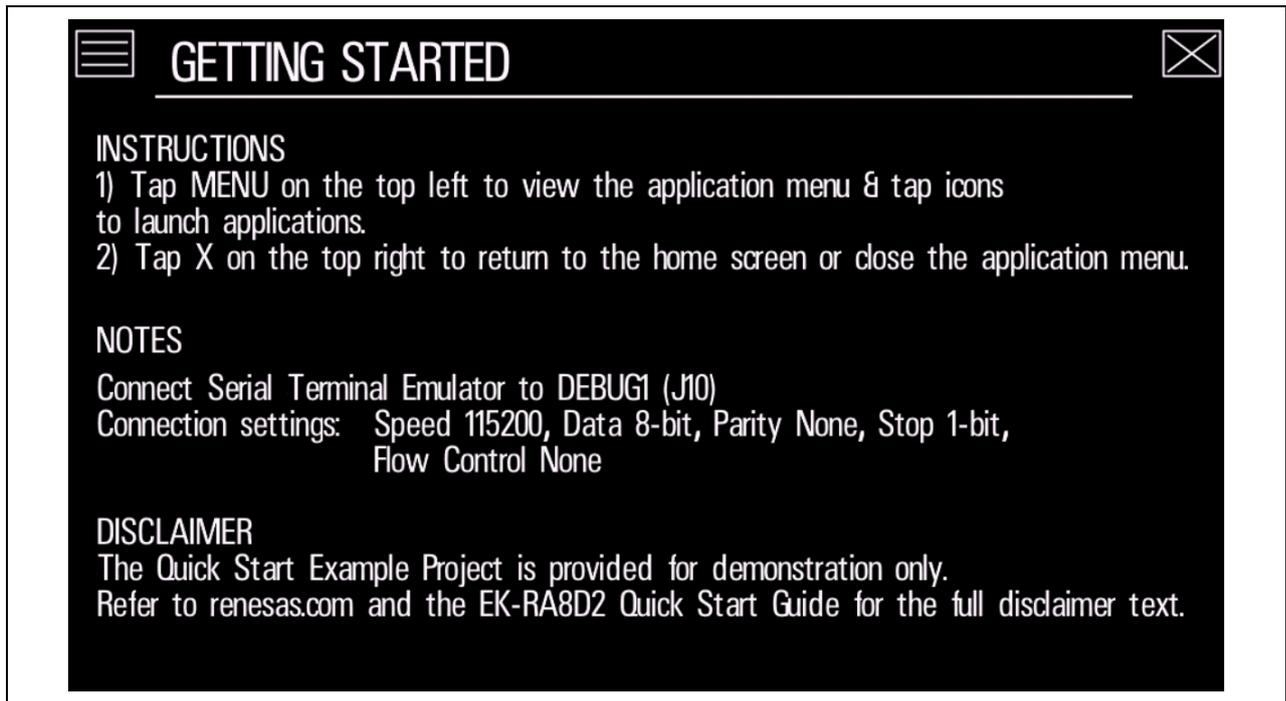


Figure 5. Parallel Display: Getting Started

5. Connect a Serial Terminal as described on the screen.
On the host PC, open Windows Device Manager. Expand **Ports (COM & LPT)**, locate **USB Serial Device (COMxx)** and note down the COM port number for reference in the next step.

Note: When the board is connected to the PC for the first time, J-Link drivers will install automatically. If prompted to restart the computer, please do so.

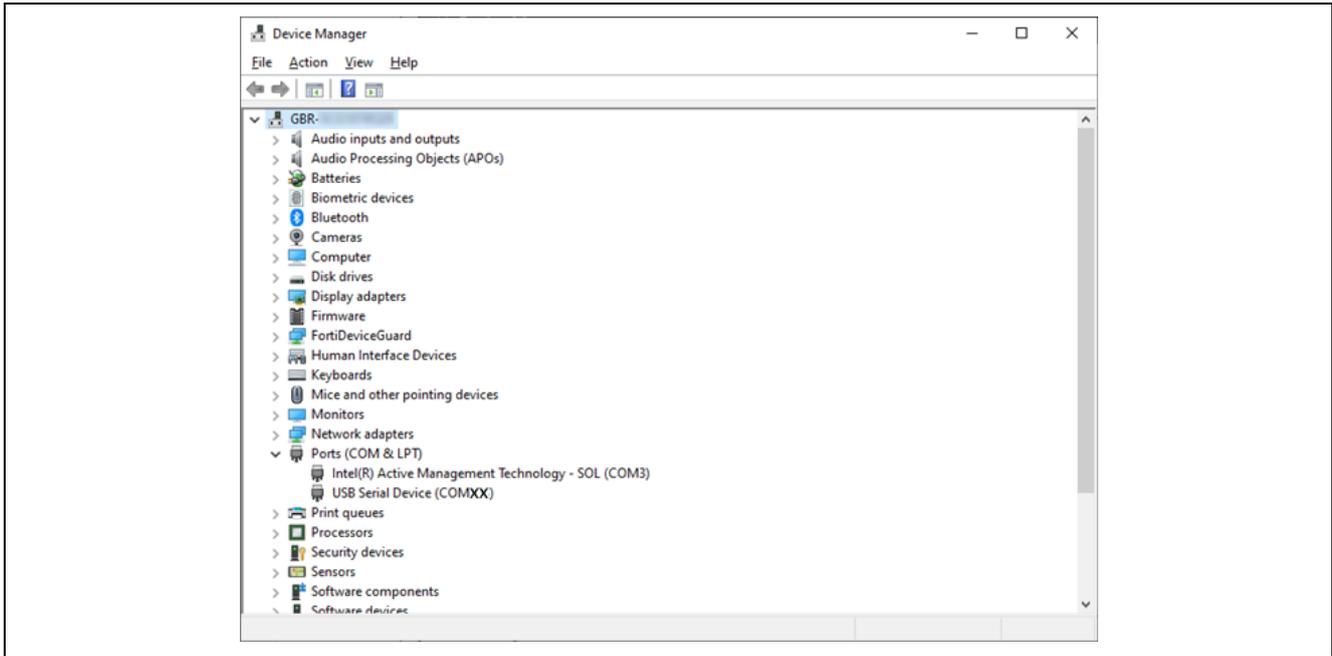


Figure 6. USB Serial Device in Windows Device Manager

6. Open Tera Term, select **Serial** and **COMxx: USB Serial Device (COMxx)** and click **OK**.

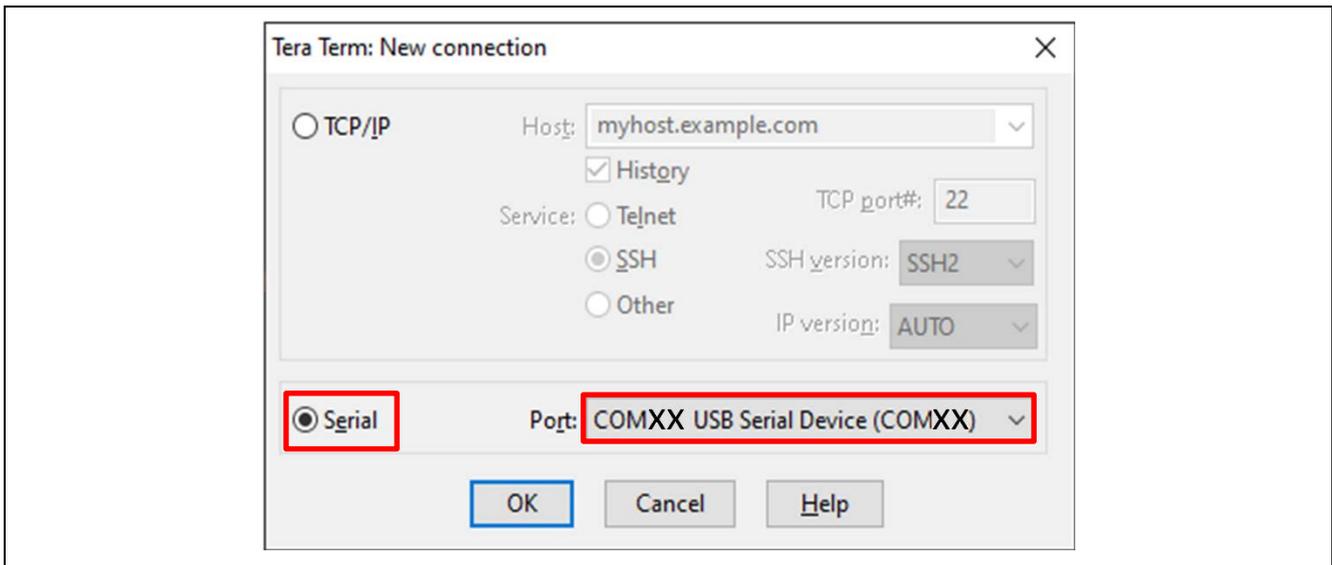


Figure 7. Selecting the Serial Port on Tera Term

7. Using the **Setup** menu pull-down, select **Serial Port...** and ensure that the speed is set to **115200**, as shown below.

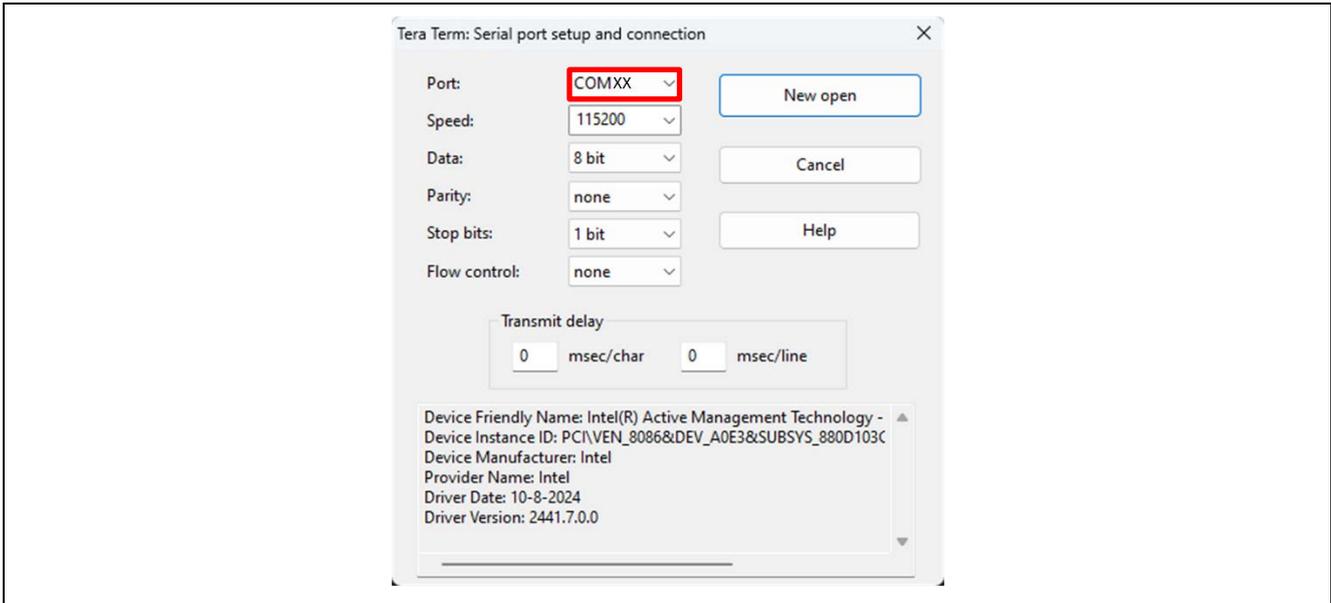


Figure 8. Select 115200 on the Speed Pulldown

8. Complete the connection. The ‘welcome and main menu’ screen will be displayed. If no text appears, press the **RESET** switch on the EK-RA8D2.

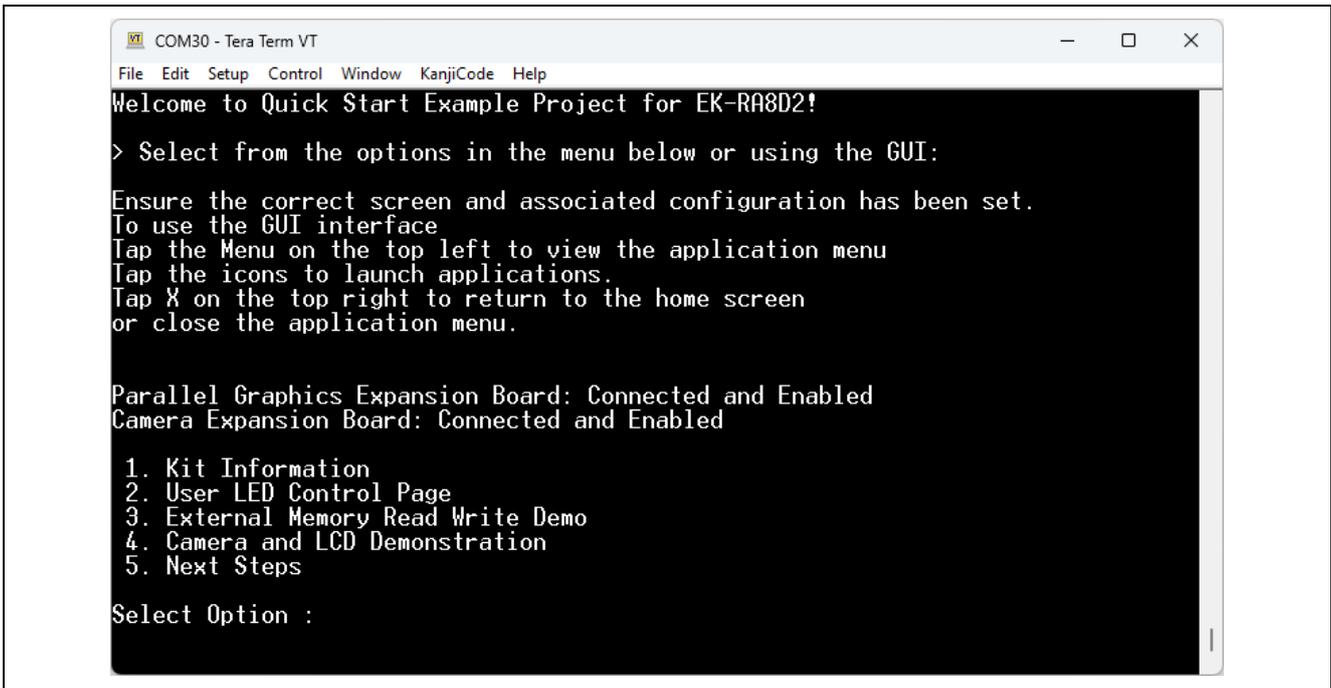


Figure 9. Serial Terminal: Welcome and Main Menu

Note: If the Display and Camera are not connected the menu options will be reduced to three steps and the confirmation messages will identify the missing items.

9. Press the 'X' on the top right corner of the parallel display to return to the home screen.



Figure 10. Parallel Display: Home Screen

10. Press the 'Menu' icon on the top left corner of the Parallel display to open the Application Menu.

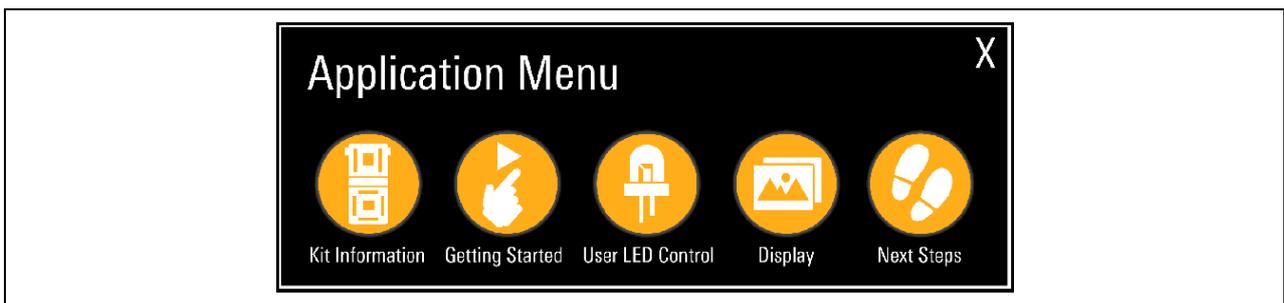


Figure 11. Parallel Display: Application menu

11. The Parallel display can be used to navigate the QSEP, however, if not already done, it is recommended to open a serial console before proceeding to experience the full QSEP.
12. Press **1** on the serial console or navigate using the display menu to display the **Kit Information** including the kit name, ordering part number, RA device part number, MCU ID, MCU die temperature, and the user LED's current blinking frequency and intensity.

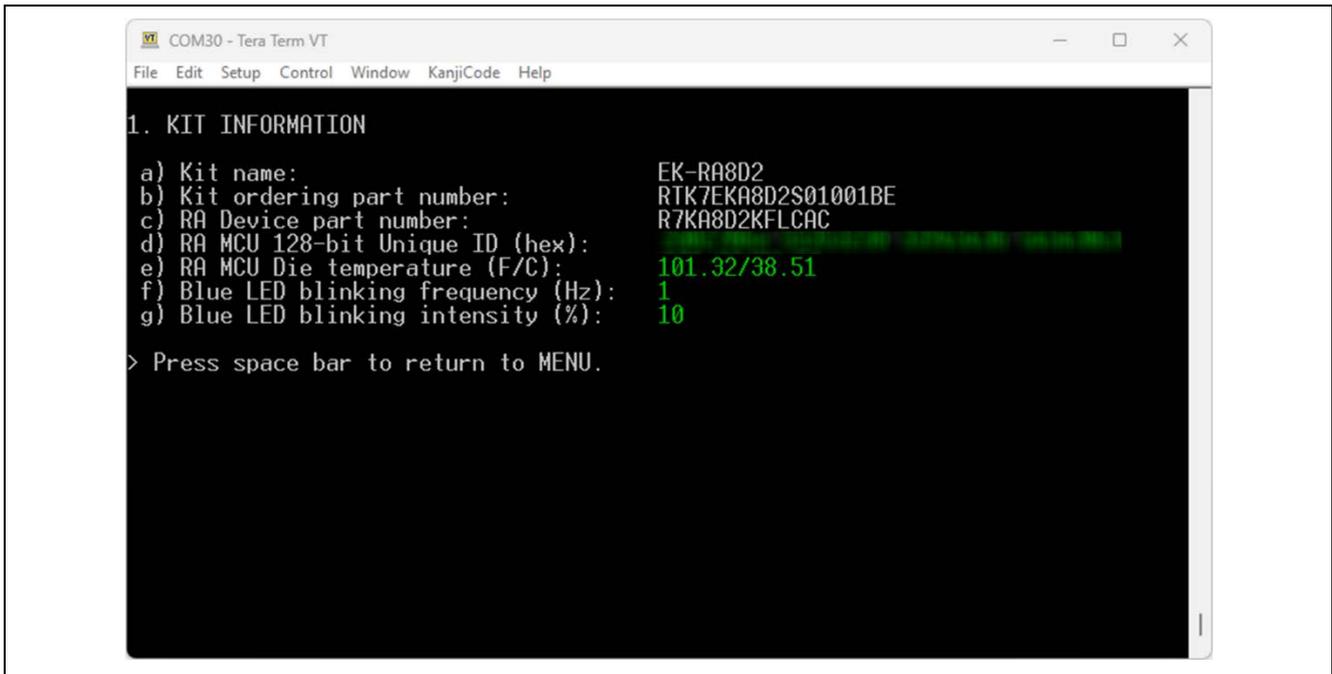


Figure 12. Serial Terminal: Kit Information

13. The following screen appears on the Parallel display

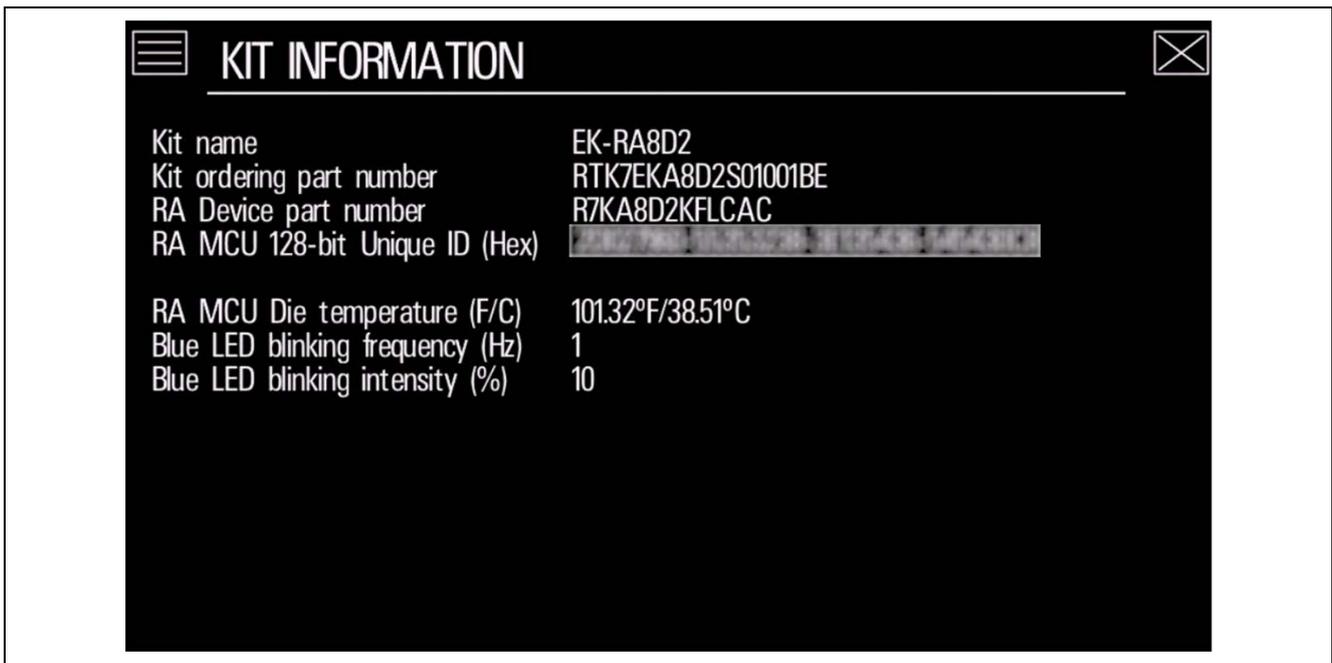


Figure 13. Parallel Display: Kit Information

14. Press **space** or press '**X**' on the Parallel display to return to the 'welcome and main menu' screen.
15. Press **2** or navigate using the Parallel display menu to open the **User LED Demo Page**. This application allows the user to control the blinking frequency and intensity of each user LED using the Parallel display.

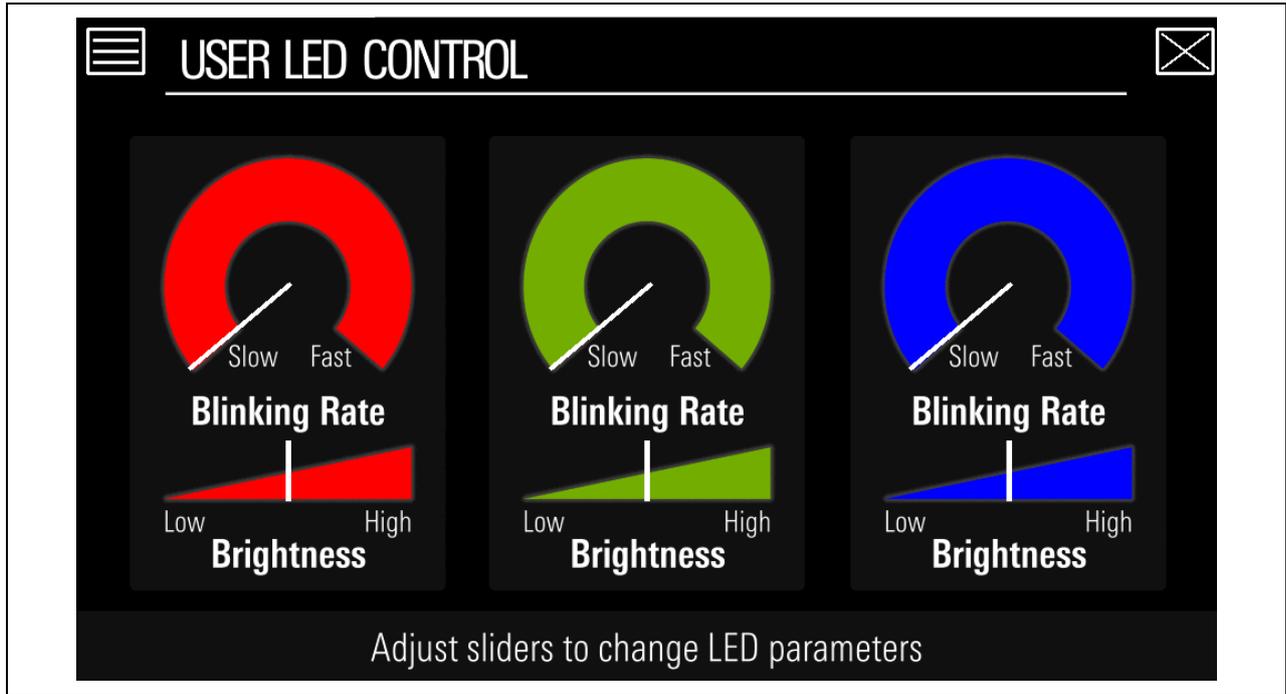


Figure 14. Parallel Display: User LED Control

- Use the sliders on the Parallel display to change the blinking frequency and intensity of each user LED. The values of the blinking frequency and intensity for each user LED are displayed on the serial console.

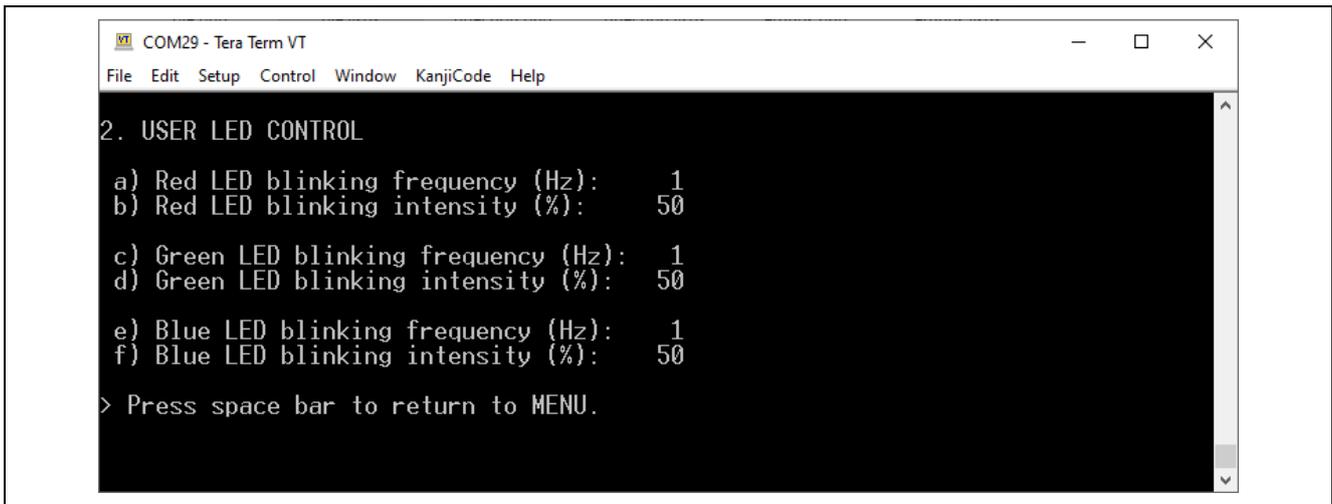


Figure 15. Serial Terminal: User LED Control

- Press **space** or press **'X'** on the Parallel display to return to the 'welcome and main menu' screen.
- Press **3** on the serial console to open the **External Memory Read Write Demo**. This application demonstrates the write and read performance to and from the Octo-SPI flash memory.
 Note: There is no option to navigate to the External Memory Read Write Demo using the Parallel display. The results are displayed on the parallel screen if present.

```

COM98 - Tera Term VT
File Edit Setup Control Window KanjiCode Help
Compares the write and read times to and from external Octo-SPI
flash memory

> Enter the text block size
(in multiples of 2 KB, max 64 KB) and press tab to continue :

```

Figure 16. Serial Terminal: External Memory Read Write Demo

19. Enter the block size in the range of 2 KB to 64 KB and press **tab**.

Note: If invalid input characters (keys other than 0 to 9 and **tab**) or out of range values are entered, an error message will appear. Re-enter valid inputs and press **tab**. Pressing **space** will display the 'welcome and main menu' screen.

```

COM98 - Tera Term VT
File Edit Setup Control Window KanjiCode Help
Compares the write and read times to and from external Octo-SPI
flash memory

> Enter the text block size
(in multiples of 2 KB, max 64 KB) and press tab to continue : 64

Generated a text block of 64 KB in SRAM

Writing the text block to external Octo-SPI flash memory ...
Writing to flash completed

Reading the text block from external Octo-SPI flash memory ...
Reading from flash completed

-----
Operation/Flash          Octo-SPI
-----
Write                    58169
Read                     5803
-----

Note: Times are in microseconds

> Press space bar to return to MENU.

```

Figure 17. Serial Terminal: External Memory Read Write Demo Results

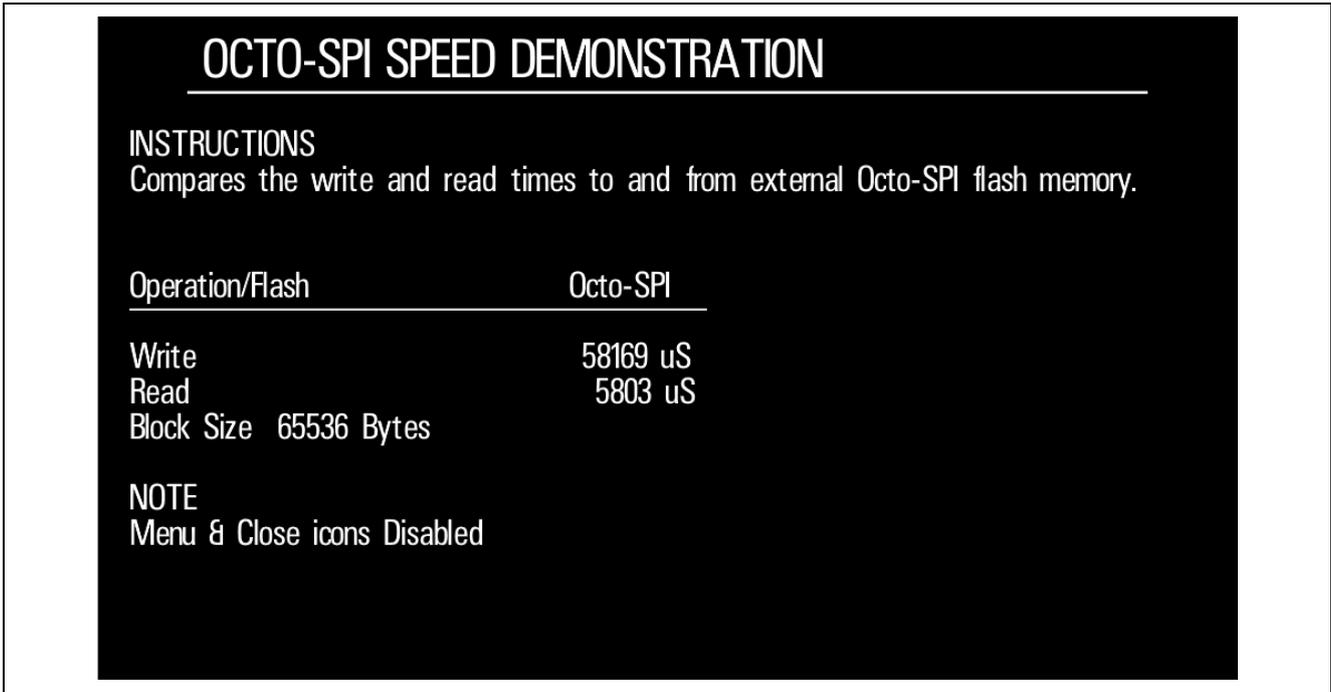


Figure 18. Parallel Display: External Memory Read Write Demo Results

- 20. Press **space** or press '**X**' on the Parallel display to return to the 'welcome and main menu' screen.
- 21. Press **4** to display the **Camera and LCD Demonstration**. This application streams the camera image to the screen. While streaming, the touchscreen is active and a circle will be drawn under each touch point on the LCD, overlaid on the camera image.

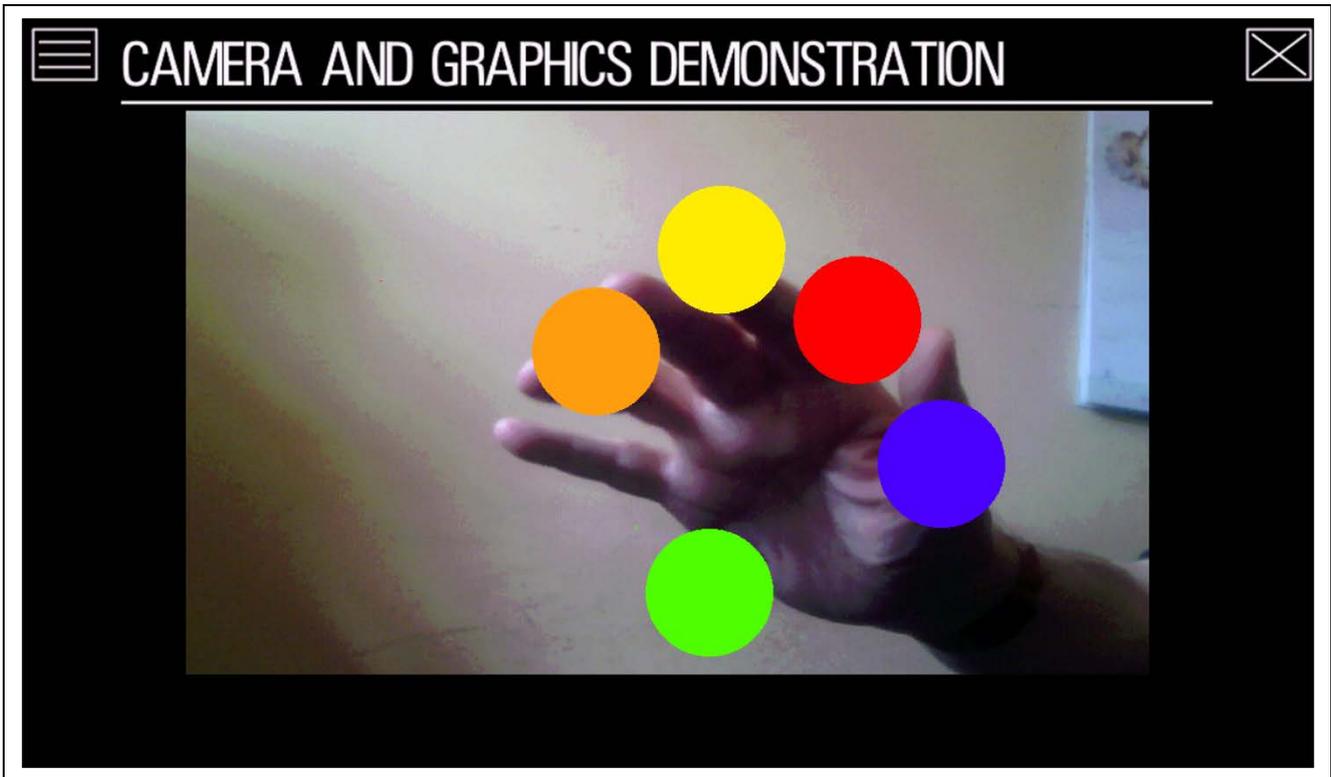
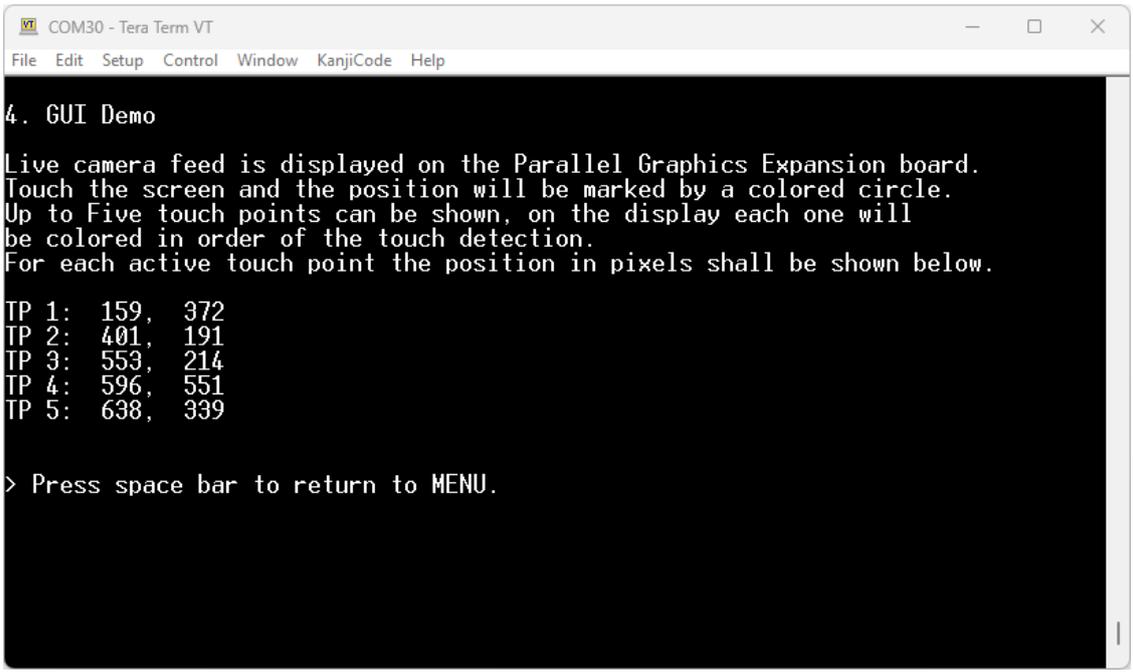


Figure 19. Parallel Display: Camera and LCD Demonstration

22. Up to five touch points can be displayed at a time with their coordinates output on the serial console.



```
COM30 - Tera Term VT
File Edit Setup Control Window KanjiCode Help

4. GUI Demo

Live camera feed is displayed on the Parallel Graphics Expansion board.
Touch the screen and the position will be marked by a colored circle.
Up to Five touch points can be shown, on the display each one will
be colored in order of the touch detection.
For each active touch point the position in pixels shall be shown below.

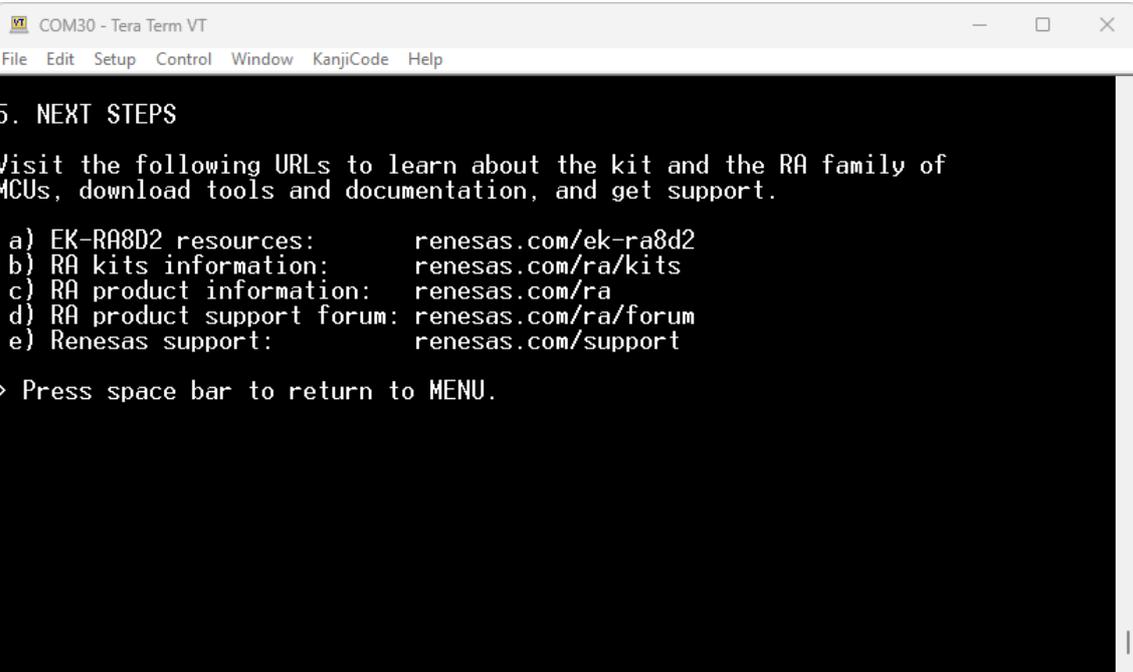
TP 1: 159, 372
TP 2: 401, 191
TP 3: 553, 214
TP 4: 596, 551
TP 5: 638, 339

> Press space bar to return to MENU.
```

Figure 20. Serial Terminal: Camera and LCD Demonstration

23. Press **space** press '**X**' on the parallel display to return to the 'welcome and main menu' screen.

24. Press **5** or navigate using the parallel display menu to display the Next Steps menu.



```
COM30 - Tera Term VT
File Edit Setup Control Window KanjiCode Help

5. NEXT STEPS

Visit the following URLs to learn about the kit and the RA family of
MCUs, download tools and documentation, and get support.

a) EK-RA8D2 resources:      renesas.com/ek-ra8d2
b) RA kits information:    renesas.com/ra/kits
c) RA product information: renesas.com/ra
d) RA product support forum: renesas.com/ra/forum
e) Renesas support:       renesas.com/support

> Press space bar to return to MENU.
```

Figure 21. Serial Terminal: Next Steps

25. The following screen will appear on the Parallel display.



Figure 22. Parallel Display: Next Steps

26. Press **space** to return to the 'welcome and main menu' screen.

6. Customizing the Quick Start Example Project

This section lists the requirements and instructions for customizing the Quick Start example project.

Hardware Requirements

- EK-RA8D2 board
- USB-A / USB-C to USB-C cable
- A PC with at least one USB port

Software Requirements

- Windows® 11 operating system (or later)
- e² studio IDE 2025-12 (or later)
- SEGGER J-Link® USB drivers
- FSP v6.4.0 (or later)
- Quick Start example project

6.1 Downloading and Installing Software and Development Tools

Before the Quick Start example project can be modified, it is necessary to download and install software and development tools on the host PC.

The FSP, J-Link USB drivers, and e² studio are bundled in a downloadable platform installer available on the FSP webpage at renesas.com/ra/fsp. New users are recommended to use the **Quick Install** option provided in the installation wizard, to minimize the amount of manual configuration needed.

There is no need to download and install software, development tools, and drivers separately.

6.2 Downloading and Importing the Quick Start Example Project

1. Download the Quick Start example project to a local directory on the host PC.
 - The Quick Start example project (source code and project files) is available in the **Sample code** section of the **Design & Development tab** on the EK-RA8D2 webpage at renesas.com/ek-ra8d2. Download the file (`quickstart_ek_ra8d2_ep.zip`) to a local directory on the host PC.
2. Launch e² studio.
3. Browse to the Workspace where the project file is to be imported. Enter the name in the Workspace dialog box to create a new workspace.

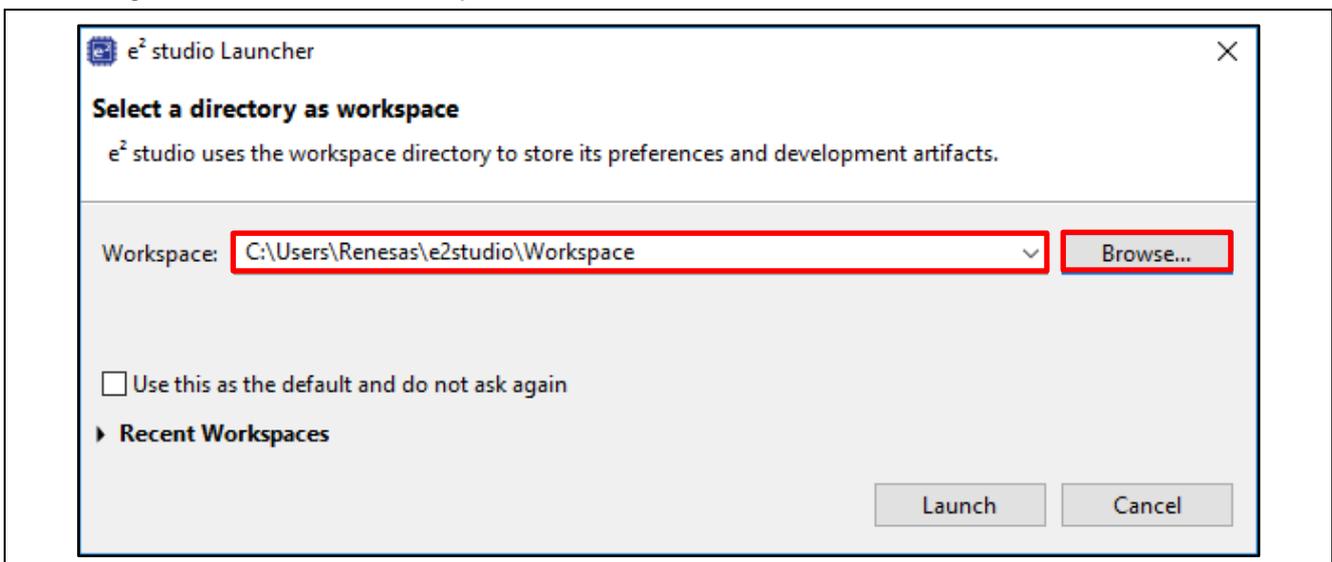


Figure 23. Creating a New Workspace

4. Click **Launch**.

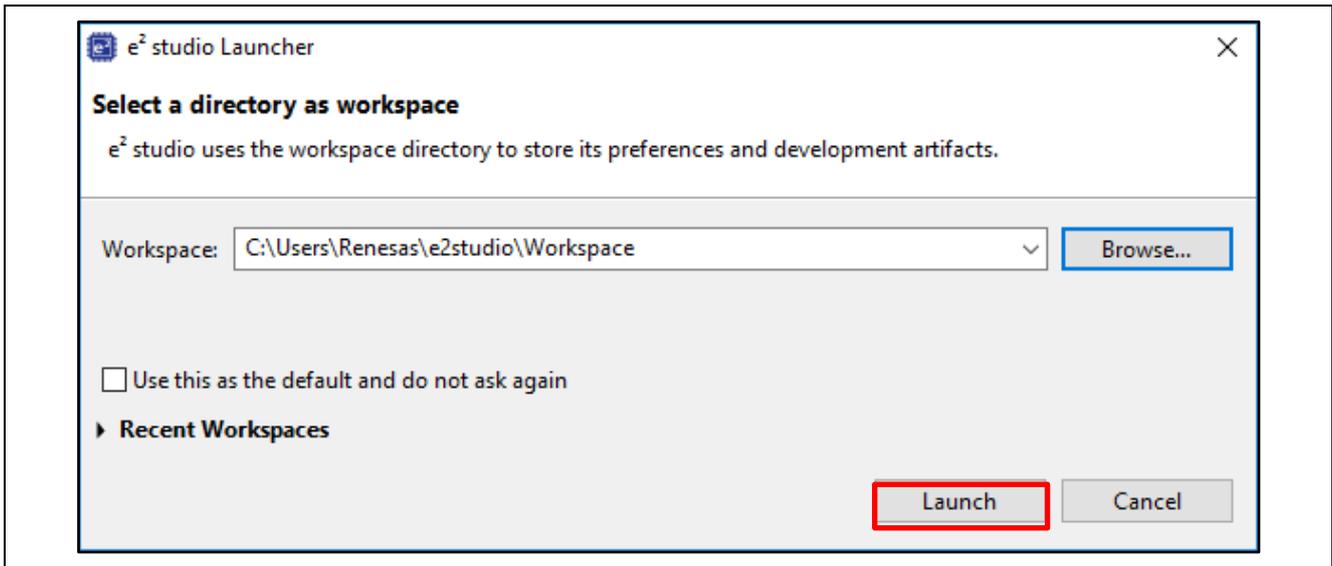


Figure 24. Launching the Workspace

5. Click **Import** from the **File** drop-down menu.

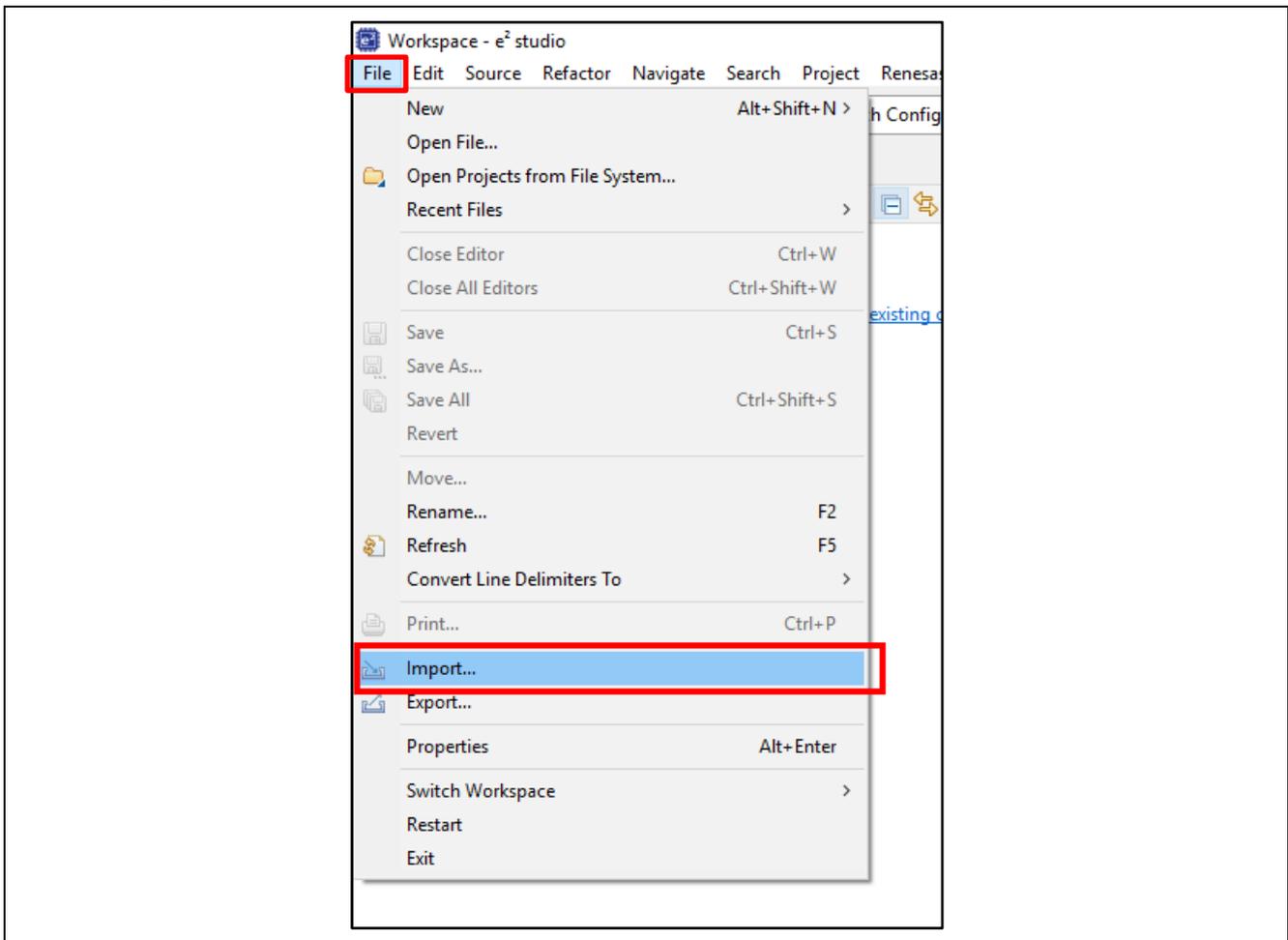


Figure 25. Importing the Project

6. In the **Import** dialog box, select **General**, and then select **Existing Projects into Workspace**.

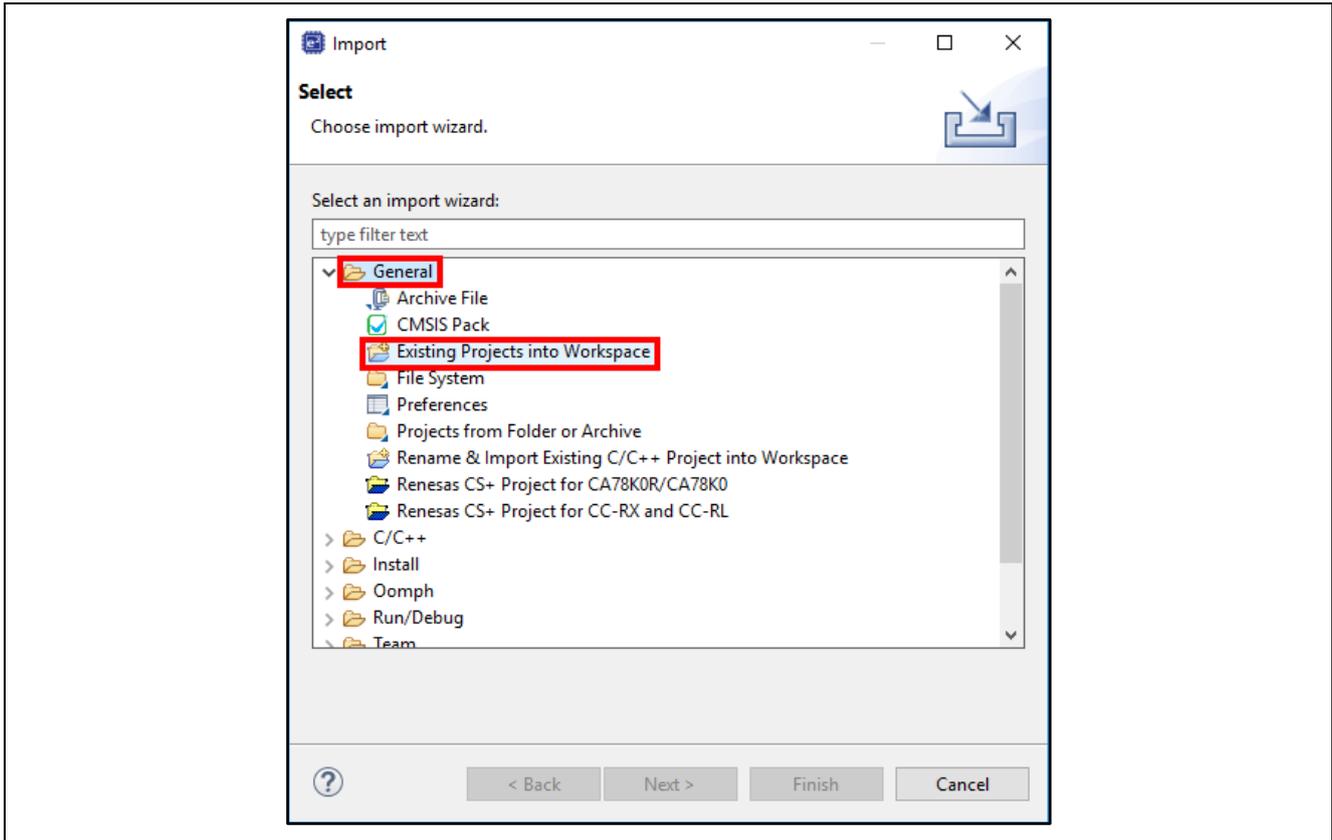


Figure 26. Importing Existing Projects into the Workspace

7. Click **Next**.

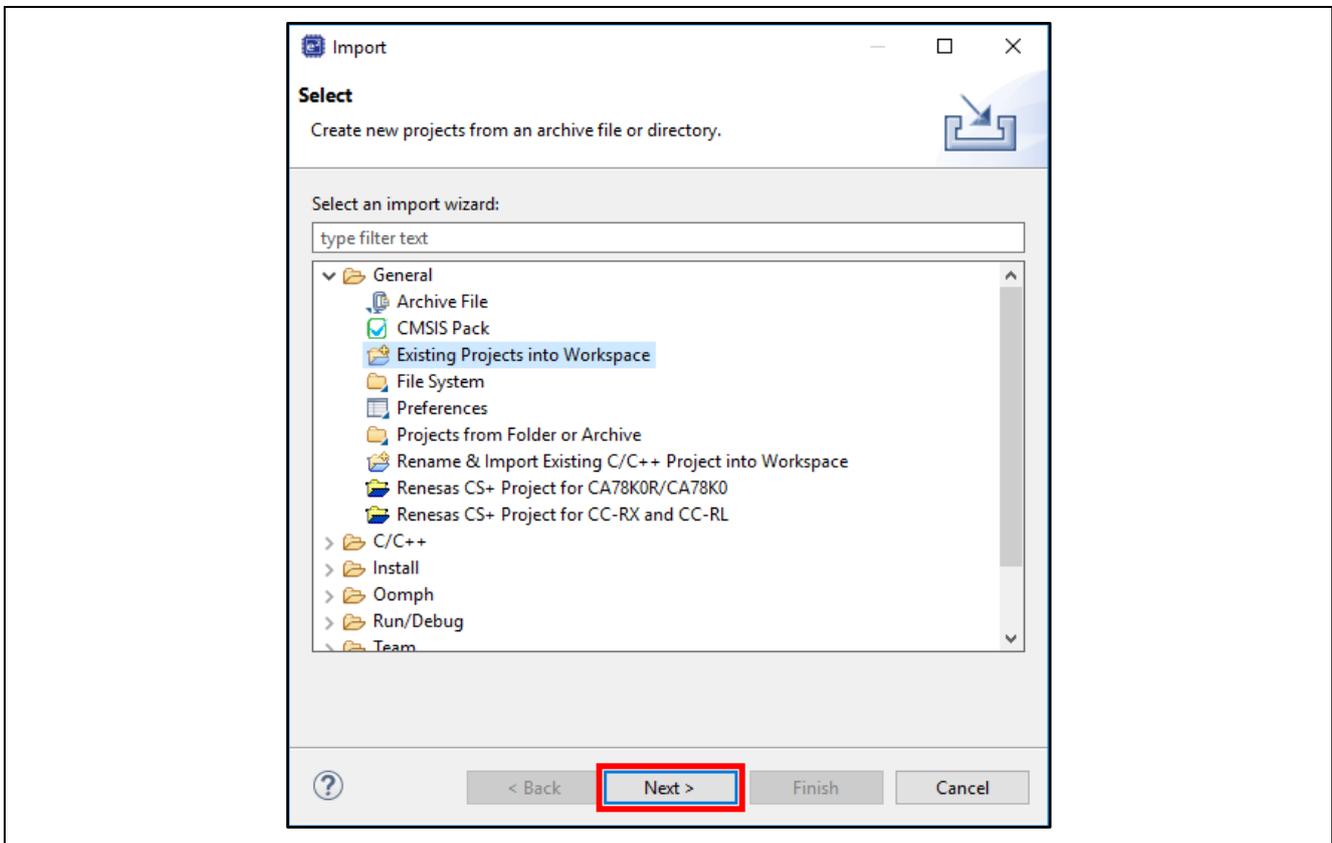


Figure 27. Clicking Next to Import Existing Projects into the Workspace

8. Click **Select archive file** and click **Browse** to go to the location of the Quick Start example project.

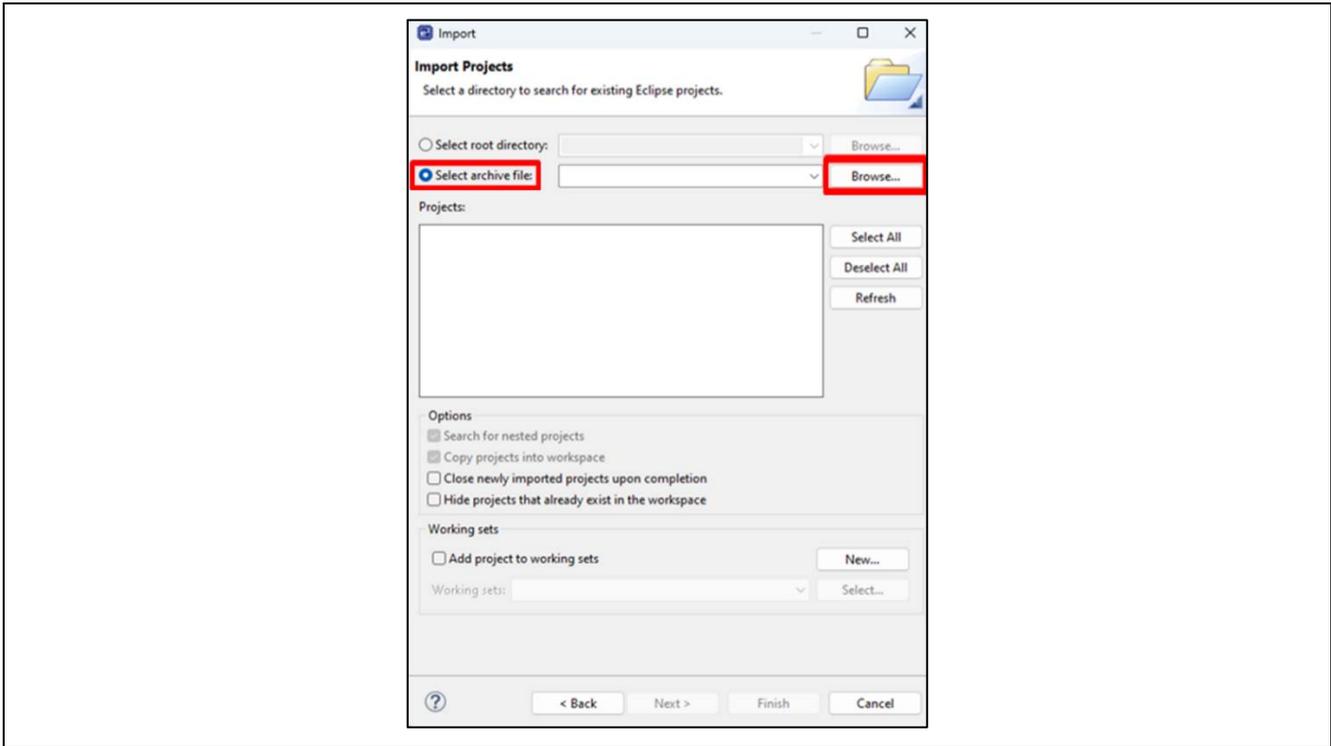


Figure 28. Selecting the Archive File

9. Select the Quick Start example project and click **Finish**.

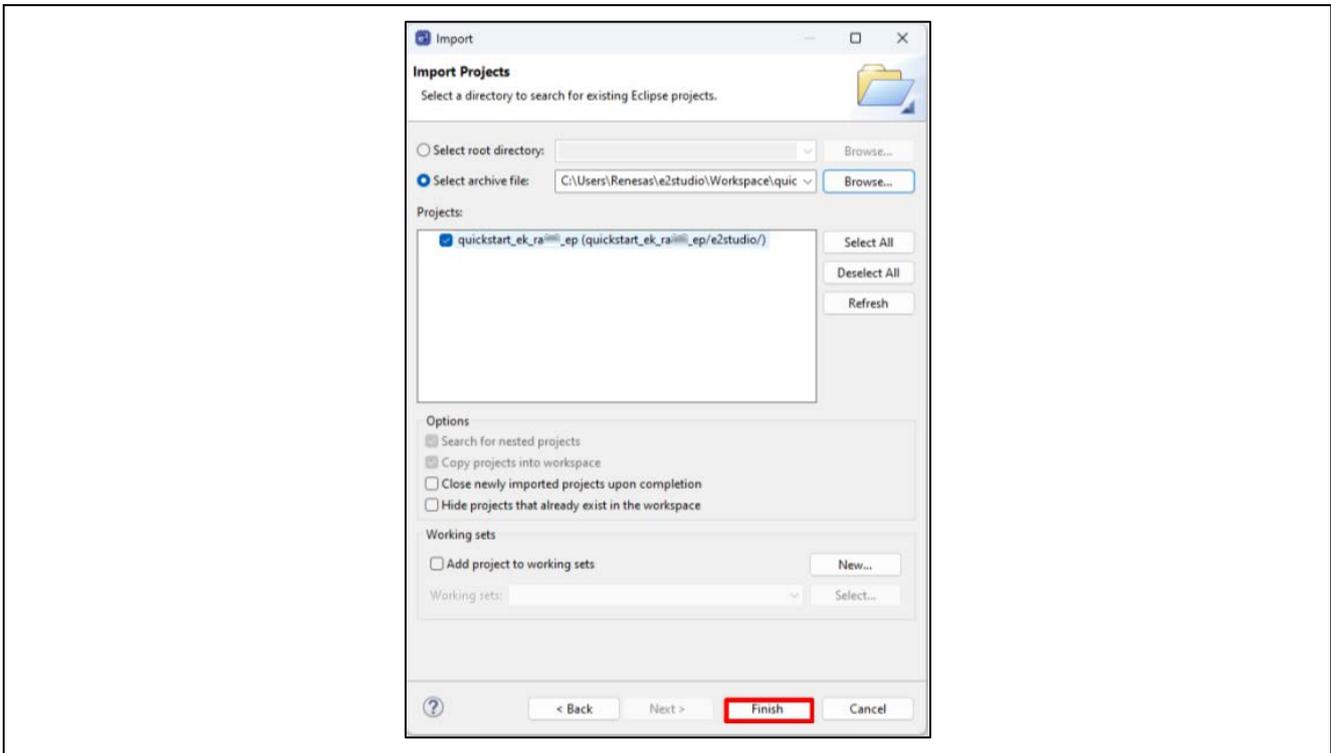


Figure 29. Finish Importing the Quick Start Example Project

6.3 Modifying, Generating, and Building the Quick Start Example Project

This section provides instructions to modify the Quick Start example project. The Quick Start example project can be modified by editing the source code and reconfiguring the properties of the MCU peripherals, pins, clocks, interrupts, and so forth.

Note: The specific modifications that can be performed to the Quick Start example project are not described in this QSG.

1. Once the Quick Start example project is imported, click the configuration.xml file to open the FSP configuration window. The FSP configuration window provides an easy-to-use interface to configure the properties of the MCU peripherals.

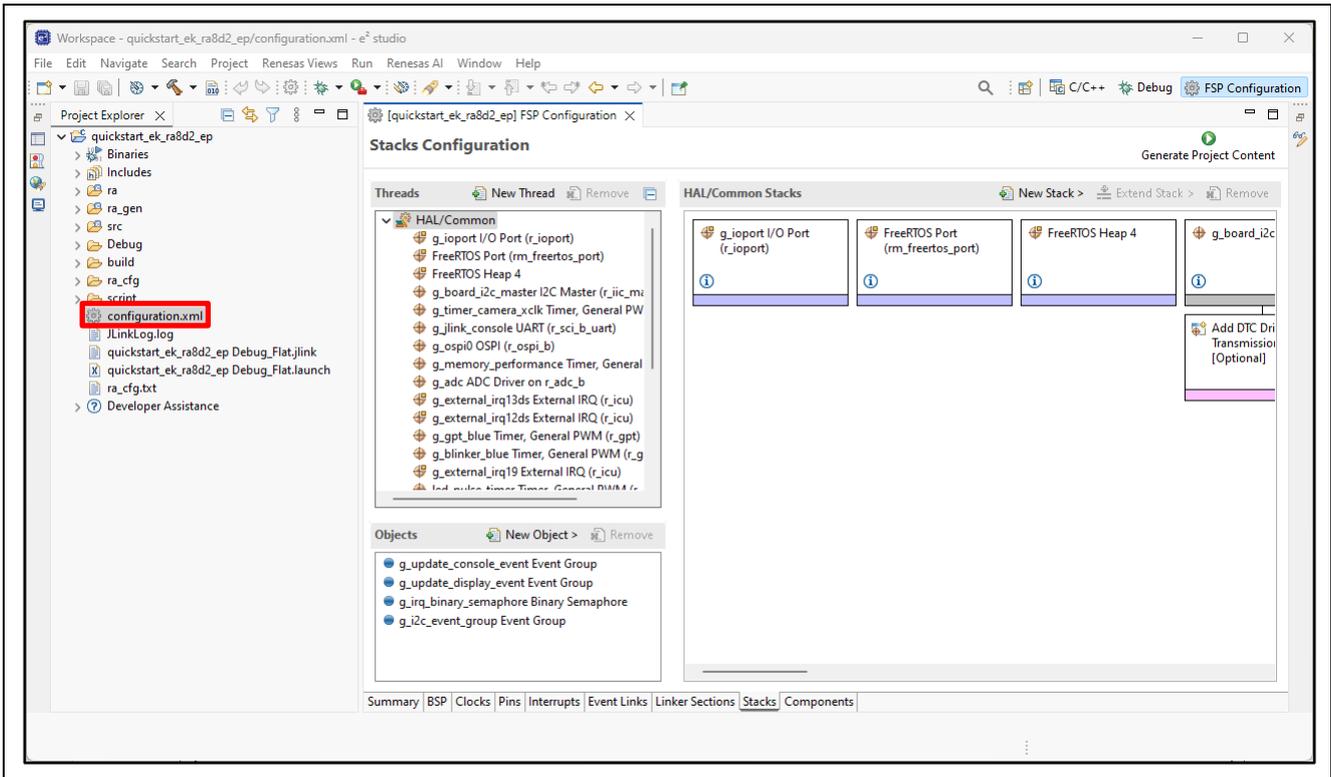


Figure 30. Opening the FSP configuration

2. For example, in the **Stacks** tab of the FSP configuration, the user can click to select modules to modify the configuration settings, as required, in the **Properties** tab. Figure 32 illustrates modifying the **ADC** driver configuration.

Note: To access the stack component properties, the **view** must be set to **FSP Configuration**. Use the **Open Perspective** button, if necessary.

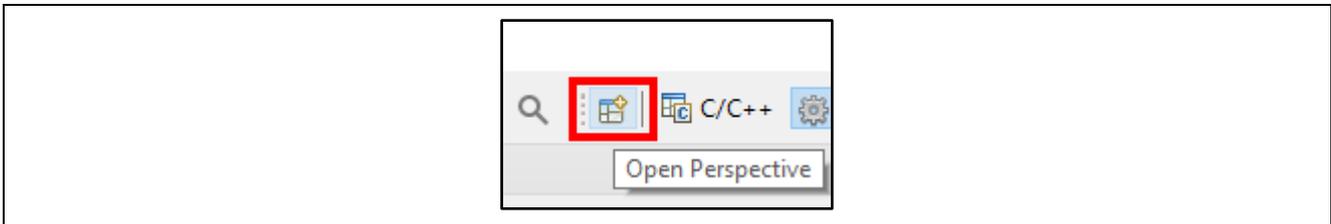


Figure 31. Open Perspective

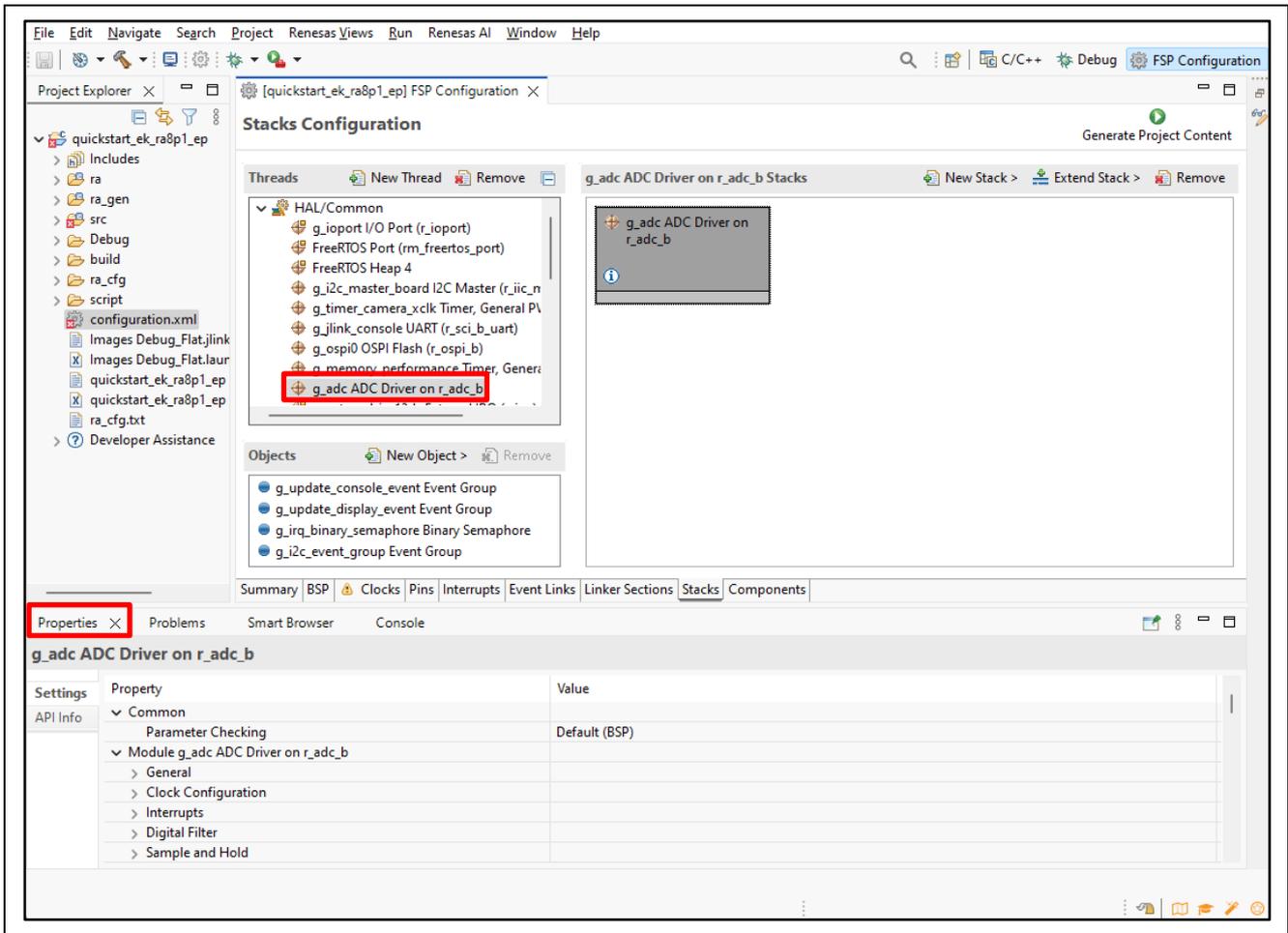


Figure 32. Modifying the Configuration Settings

- After the desired modifications are made, click **Generate Project Content**. A dialog box may appear with an option of saving the configuration changes. Click **Proceed**.

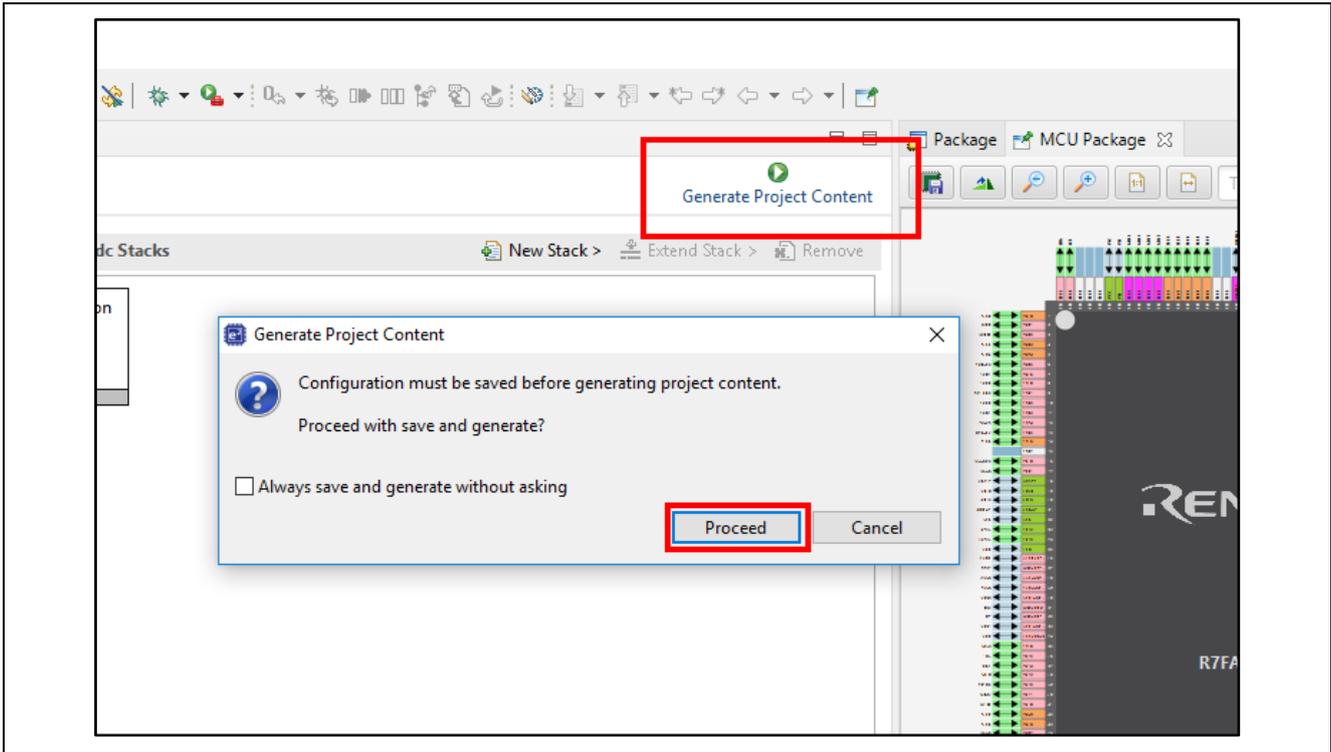


Figure 33. Saving the Configuration Changes

- Modify the source files in the `/src` folder as needed and save the changes.
- Build the project by clicking the build icon.



Figure 34. Building the Project

- A successful build produces an output as follows.
 Note: Warnings are acceptable for a successful build whereas errors are not.

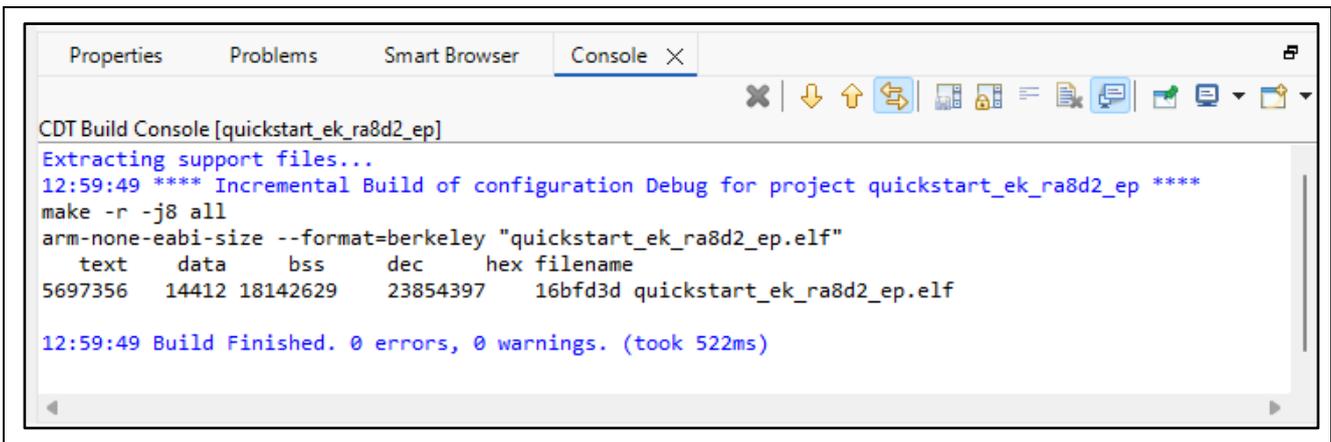


Figure 35. Successful Build Output

6.4 Setting Up Debug Connection between the EK-RA8D2 board and Host PC

To program the modified Quick Start example project on to the EK-RA8D2 board, a debug connection is necessary between the EK-RA8D2 board and host PC.

1. Connect the type-C end of the USB-C cable to USB Debug port (J10) of the EK-RA8D2 board. Connect the other end of this cable to the USB port of the host PC*.

* This board requires significant current to run the display, use a root host PC port, or a powered hub.

Note: The EK-RA8D2 board supports 3 debugging modes. In this section and the following sections, default debugging mode, Debug On-Board, is used. More information on debugging modes is available in EK-RA8D2 user's manual.

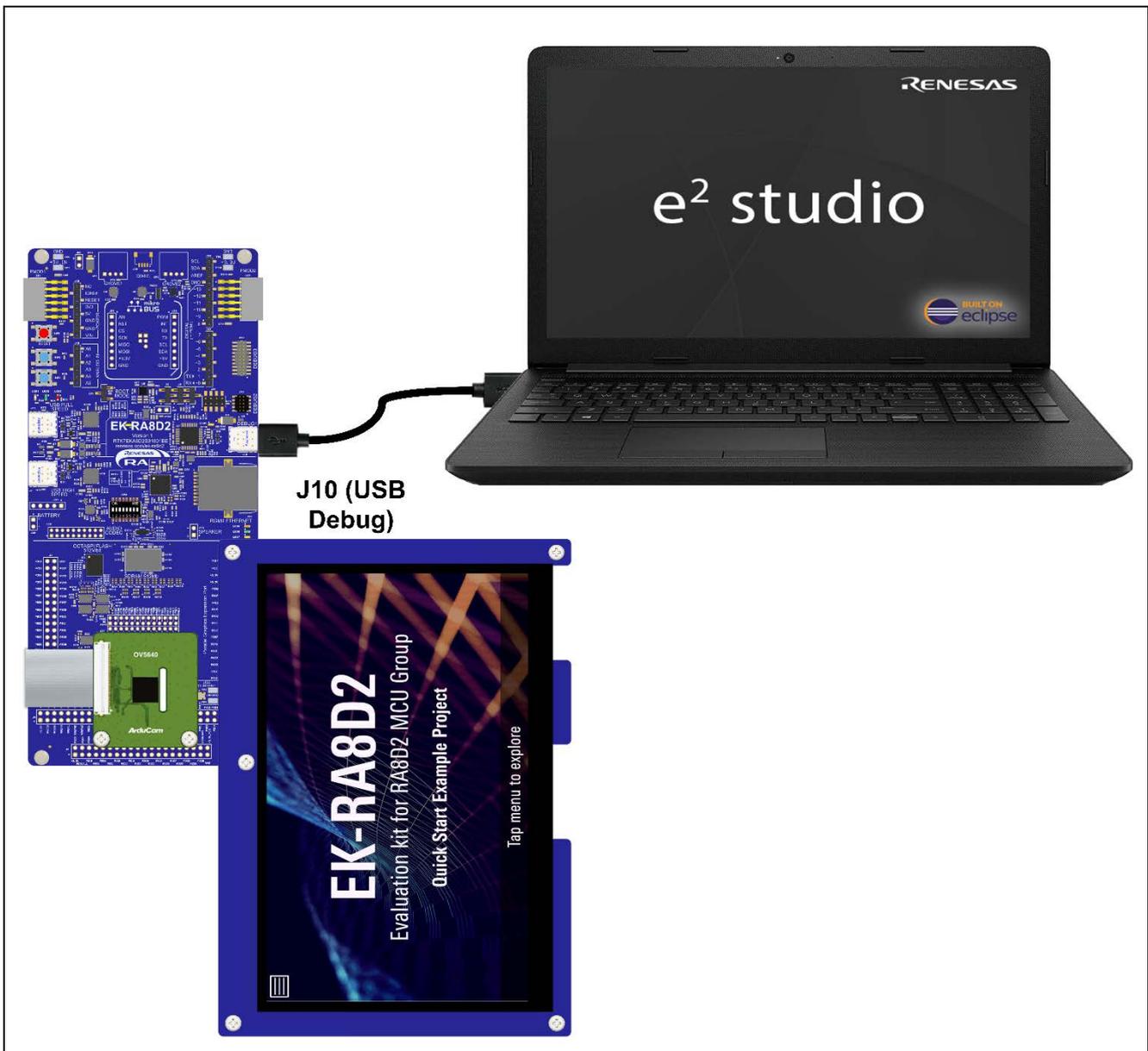


Figure 36. Connecting the EK-RA8D2 Board to the Host PC via USB Debug Port

2. Verify that the debug LED (LED5) stops blinking and lights up orange indicating that the J-Link drivers are detected by the EK-RA8D2 board.

Note: The debug LED (LED5) continues to blink if J-Link drivers are not detected by the EK-RA8D2 board. In that case, make sure that the EK-RA8D2 board is connected to the host PC through the type-C USB debug port (J10) and that J-Link drivers are installed on the host PC by checking in the Windows Device Manager (expand **Ports (COM & LPT)**, and locate **JLink CDC UART Port**).

6.5 Downloading and Running the Modified Quick Start Example Project

1. In e² studio, click the drop-down menu for the debug icon, select **Debug Configurations** option.

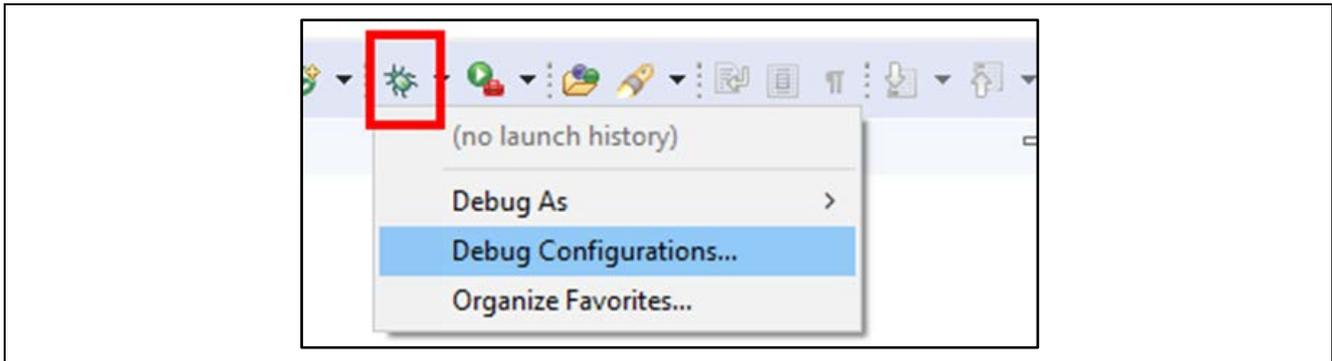


Figure 37. Selecting the Debug Option

2. In the dialog on the left-hand pane, expand the **Renesas GDB Hardware Debugging** and select the built image to debug. In this case, the **quickstart_ek_RA8D2_ep Debug_Flat**.

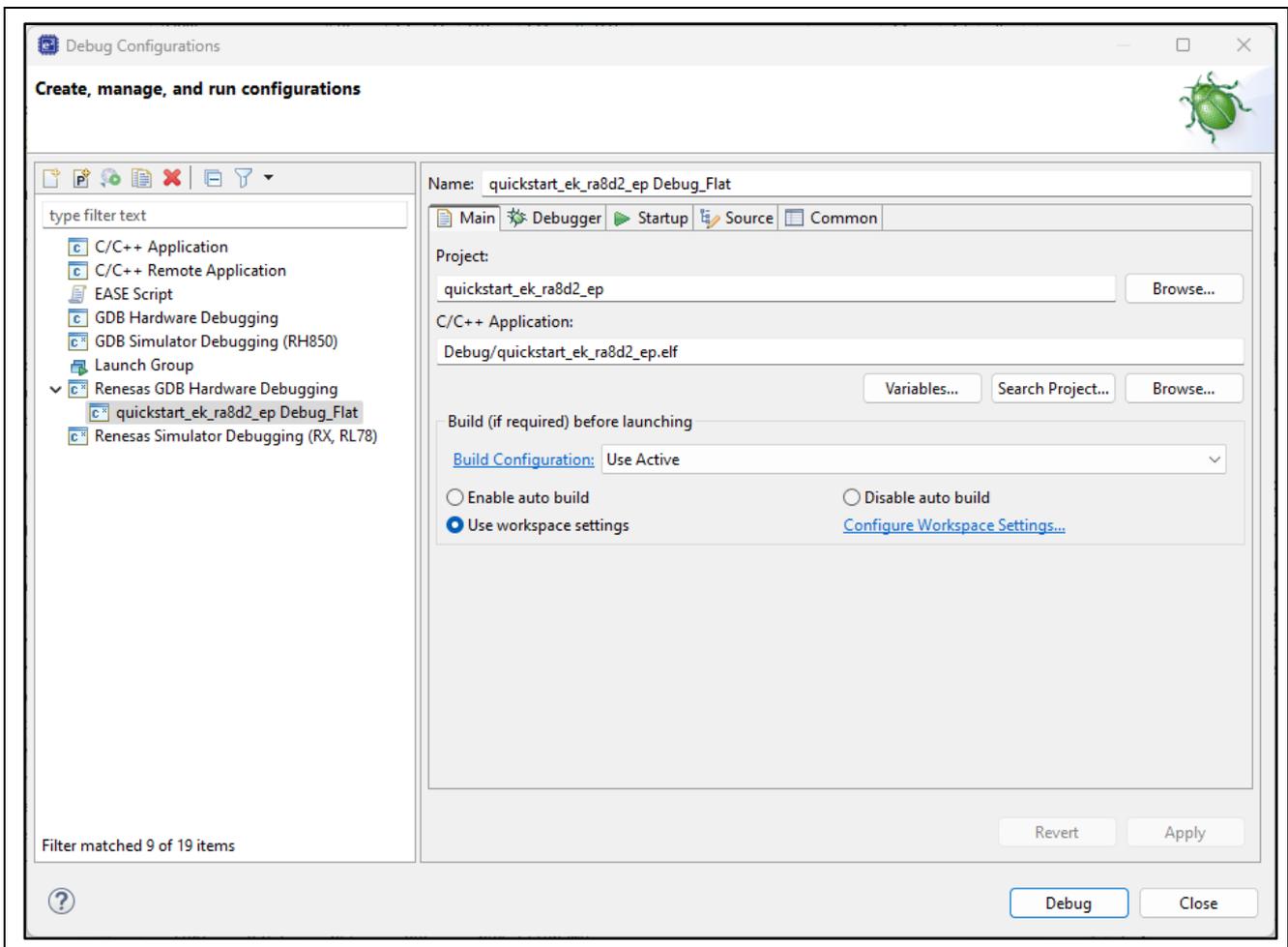


Figure 38. Selecting the Debug Image

3. Click Debug.

6.6 Firewall Dialog

1. A firewall warning may be displayed for 'e2- server-gdb.exe'. Check the 'Private networks, such as my home or work network' box and click 'Allow access'.
2. A user account control dialog may be displayed. Enter the administrator password and click Yes.
3. A dialog box may appear. Click **Switch**.

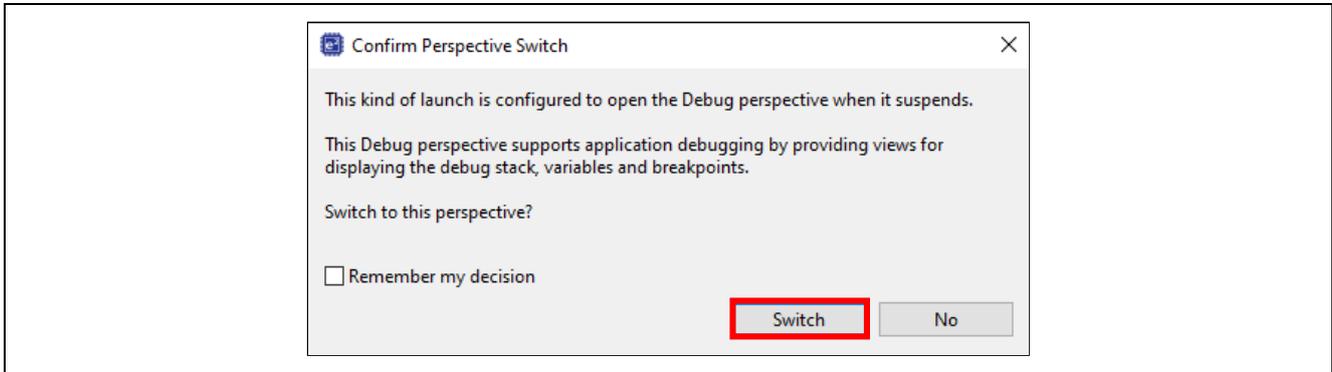


Figure 39. Opening the Debug Perspective

4. Press **F8** or click **Resume** icon to begin executing the project.



Figure 40. Executing the Project

5. The modified Quick Start example project is programmed into the EK-RA8D2 board and is running. The project can be paused, stopped, or resumed using the debug controls.

7. Next Steps

1. To learn more about the EK-RA8D2 kit, refer to the EK-RA8D2 user's manual and design package available in the Documents and Download tabs respectively of the EK-RA8D2 webpage at renesas.com/ek-ra8d2
2. Renesas provides several example projects that demonstrate different capabilities of the RA MCUs. These example projects can serve as a good starting point for users to develop custom applications. Example projects (source code and project files) for EK-RA8D2 kit are available in the EK-RA8D2 Example Projects Bundle. The example projects bundle is available in the Downloads tab of EK-RA8D2 webpage.
 - Download and extract the example projects bundle (xxxxxxxxxxxxxxxx-ek-ra8d2-exampleprojects.zip) to a local directory on the host PC.
 - Refer to the list of all example projects (xxxxxxxxxxxxxxxx-ek-ra8d2-exampleprojects.pdf) available inside the example projects bundle.
 - Browse to the desired example project (for example: adc_ek_ra8d2_ep) in the example projects bundle (xxxxxxxxxxxxxxxx-ek-ra8d2-exampleprojects\ek_ra8d2\adc\adc_ek_ra8d2_ep)
 - For help on using example projects, refer to Example Project Usage Guide.pdf in the RA Example Repository on GitHub at: github.com/renesas/ra-fsp-examples/tree/master/example_projects
 - The archived versions of the source code of the example projects are available in the example project repository.

8. Website and Support

Visit the following URLs to learn about the kit and the RA family of microcontrollers, download tools and documentation, and get support.

EK-RA8D2 Resources	renesas.com/ek-ra8d2
RA Kits Information	renesas.com/ra/kits
RA Product Information	renesas.com/ra
RA Product Support Forum	renesas.com/ra/forum
RA Videos	renesas.com/ra/videos
Renesas Support	renesas.com/support
RA Flexible Software Package (FSP)	renesas.com/fsp

Revision History

Rev.	Date	Description	
		Page	Summary
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