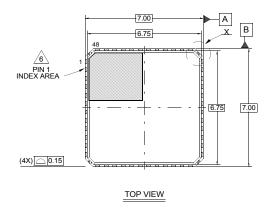
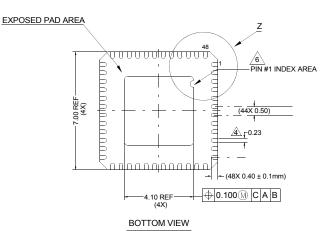
Plastic Packages for Integrated Circuits

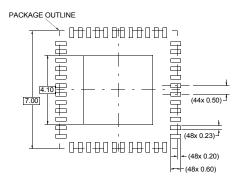
Package Outline Drawing

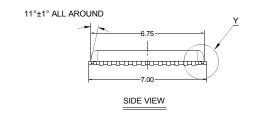
L48.7x7C

48 LEAD QUAD FLAT NO-LEAD PLASTIC PACKAGE (PUNCH QFN) Rev 0, 1/08

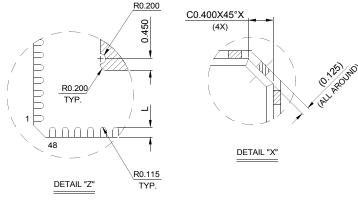


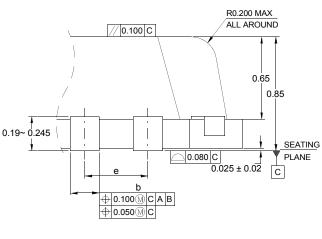












DETAIL "Y"

NOTES:

- Dimensions are in millimeters.
 Dimensions in () for Reference Only.
- 2. Dimensioning and tolerancing conform to JESD-MO220.
- 3. Unless otherwise specified, tolerance : Decimal \pm 0.05, body tolerance \pm 0.1
- Dimension b applies to the metallized terminal and is measured between 0.18mm and 0.28mm from the terminal tip. Frame base metal thickness 0.203mm.
- 5. Tiebar shown (if present) is a non-functional feature.
- The configuration of the pin #1 identifier is optional, but must be located within the zone indicated. The pin #1 identifier may be either a mold or mark feature.