

RENESAS SEMICONDUCTOR RELIABILITY REPORT

GROUP : RA8D2

DEVICE : R7KA8D2XXX

QUALITY GRADE : Standard

Quality Assurance Div.
Renesas Electronics Corporation

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Summary of Reliability Test Results for BGA Package (MSL3)

Test Item	Reference	Test Conditions	Results Failure/Size	Comment
High Temperature Operating Life (HTOL)	JESD22-A108	Tj=125 °C, Vccmax, 1000 hrs	0/231	3 lots or more
High Temperature Storage Life (HTSL)	JESD22-A103	Ta=150 °C, 1000 hrs	0/75	3 lots or more
Temperature Humidity bias (HAST) (*1)	JESD22-A110	Ta=110 °C, RH=85 %, Vccmax, 264 hrs	0/75	3 lots or more
Temperature Cycling (TC) (*1)	JESD22-A104	Ta=-55 °C to 125 °C , 700 cycles	0/75	3 lots or more
Latch-Up (LU)	JESD78	Pulse Current Injection, I=+/-150 mA	0/3	
Electrostatic discharge (ESD-HBM)	JS-001	1.5 kΩ, 100 pF, +/-2000 V, 1 time	0/3	Class: 2
Electrostatic discharge (ESD-CDM)	JS-002	+/-500 V, 1 time	0/3	Class: C2a
Resistance to Soldering Heat (MSL3)	JESD22-A113, J-STD-020	MSL3(Moisture Sensitivity Level 3) Peak Reflow Temp=260 °C	Pass	Verified after MSL preconditioning; applied to applicable reliability tests (*1).

Note :

- (*1) With preconditioning per JESD22-A113, MSL 3
- It is tested to confirm that all the samples are satisfied with an individual product specification.
- Basically qualification tests were performed using a representative product with the same wafer process and the same package structure .

The failure rate of the device in an actual use condition can be estimated by the below procedure.

•Equation for the failure rate estimation (λ)

$$\lambda = \lambda b \times \pi T \text{ (FIT)}$$

①Unique failure rate (λb)
 $\lambda b = 0.2 \text{ FIT}$
Unique failure rate at $T_a = 55 \text{ }^{\circ}\text{C}$ using 60 % confidence level.

②Temperature term (πT)
$$\pi T = \exp\{11600 \times E_a \times (1/(273+55) - 1/(273+T_a))\}$$

E_a : Activation energy (eV)
 T_a : Ambient temperature ($^{\circ}\text{C}$)

πT simplified chart as $E_a = 0.7 \text{ eV}$												
T_a ($^{\circ}\text{C}$)	40	50	55	60	65	70	75	80	85	90	100	110
πT	0.31	0.68	1	1.45	2.08	2.95	4.15	5.77	7.96	10.88	19.82	34.99

•MTTF (Mean Time To Failure)

$$MTTF = 1/\lambda$$

Reference about Renesas package code

Package type		Package code *1
Lead type plastic package	QFP	PxQP
	SOP	PxSP
Non-lead type plastic package	QFN	PxQN
Grid array type plastic package	BGA	PxBG
	LGA	PxLG
Wafer level chip scale package	WLCSP	SxBG

*1. First four digit

Table. Product list

No	Product part number	Package code	MSL	No	Product part number	Package code	MSL
1	R7KA8D2KFLCAB	PLBG0224J*	MSL3	51			
2	R7KA8D2KFLCAC	PLBG0289J*	MSL3	52			
3				53			
4				54			
5				55			
6				56			
7				57			
8				58			
9				59			
10				60			
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47				97			
48				98			
49				99			
50				100			

Note: MSL stands for Moisture Sensitivity Level.