
June 2, 2014

Product Specifications of the USB PD Controller, the uPD720250

- Based on the Universal Serial Bus Power Delivery Specification released by the USB 3.0 Promoter Group
 - Supports cable detection sequence to distinguish maximum current capability of the cable
 - Supports Dual Role operation and Role Swap protocol
 - Supports dead battery operation
- Supports Energy Star and EuP specifications for low-power PC peripheral systems
- System clock: 25 MHz crystal or oscillator
- Package: 32-pin QFN (5 mm × 5 mm, 0.5 mm pitch) (contemplating 48-pin QFN, 28-pin SOP)
- Single 3.3 V power supply
- Supports SMBus slave interface
- Supports I²C master interface
- Supports external SPI ROM

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