RENESAS

BIG IDEAS FOR EVERY SPACE

Separate Sheet

Product Specifications of the R-Car V3H

• Arm® Cortex®-A53 (1000MHz) CPU × 4

• Dual Lockstep Arm Cortex-R7 (800MHz) CPU × 2

• Memory controller for LPDDR4-3200 with 32 bits × 1 channel

• Image recognition engine (IMP-X5-V3H)

• Dedicated hardware accelerator for CNN, Dense Optical Flow, Stereovision and Object Classification

• Dual Image Signal Processor (ISP)

• Video Output (4 lanes × 1 channel LVDS, 1 channel digital)

• Video Input (4 lanes × 2 channels MIPI-CSI2, 2 channels digital)

• CAN-FD interface　2 channels

• FlexRay interface 1 channel

• Gigabit Ethernet and AVB Ethernet 1 channel

• PCI Express interface 1 channel

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