

August 30, 2011

Product Specifications of the PD720230 SoC

| | Specification |
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| Part number | μPD720230 (μPD720230K8-621-BAE-A) |
| Power supply voltage | 2.5 V |
| Temperature range | 0 to +85°C |
| RAM | 64 KB instruction, 16 KB work |
| USB | SuperSpeed UASP, SuperSpeed BOT, HighSpeed BOT, FullSpeed BOT |
| USB SATA | Control Endpoint × 1, Bulk IN/OUT Endpoint × 2, Interrupt Endpoint × 1 |
| | Supports Bulk IN/OUT Endpoint 15 burst at SuperSpeed |
| | READ/WRITE/WRITE AND VERIFY(10,16), HW accelerator |
| | Supports Revision 3.0, Supports speed data rate of 1.5Gbps, 3.0Gbps, 6.0Gbps |
| SATA Security | Host Initiated power management |
| | Backend SATA RAID/Port multiplier |
| | IEEE1619 AES 128/256 XTS mode |
| Security On-chip regulator | IEEE1667 Authentication |
| | 2.5 V-1.2 V switching regulator, 5 V-3.3 V LDO |
| Crystal | 30 MHz or 20 MHz |
| Package | 48-pin QFN (6 mm × 6 mm), 0.4 mm pitch |