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Main Specifications of the RX63T MCU

	RX63T Group
CPU core	RX CPU <ul style="list-style-type: none"> • General registers: 32-bit x 16 • Multiplier: 32-bit multiplier • Divider: Yes • Multiply-accumulator: Yes (two types: memory-to-memory operations and register-to-register operations)
Maximum operating frequency	80 MHz
Power supply voltage	2.7 to 3.6 V
Floating-point processing unit	Single-precision floating-point processing unit (Supports add /subtract /compare /multiply /divide and other instructions)
Flash ROM (for program storage)	Max. 64 KB
Flash ROM (for data storage)	Max. 8 KB
RAM	Max. 8 KB
On-chip peripheral functions	<ul style="list-style-type: none"> • DMA controller (DMACA) x 4 channels • Data transfer controller (DTC) x 1 channel • Multifunction pin controller (MPC) x 1 channel • Multifunction timer pulse unit (MTU3) x 8 channels • Port output enable (POE3) • General PWM timer (GPT) x 4 channels • Compare match timer (CMT) x 4 channels • Watchdog Timer (WDT) • Independent Watchdog Timer (IWDT) • Serial communication interface (SC1c) x 2 channels • Serial communication interface (SC1d) x 1 channel

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	<ul style="list-style-type: none"> • I2C bus interface × 1 channel • Serial Peripheral Interface (RSPI) × 1 channel • A/D converter (12-bit) 64-pin: 8 channels; 48-pin: 6 channels • Clock Frequency Accuracy Measurement Circuit (CAC) • CRC calculator (CRC) • Data Operation Circuit (DOC) • Register write protection function • Option setting memory area • Power-On Reset (POR) / Voltage Detection Circuit (LVD)
On-chip debugging function	Yes (with trace function)
Low power consumption modes	4 modes <ul style="list-style-type: none"> • Sleep mode • All-module clock stop mode • Software standby mode • Deep software standby mode
Packages	LQFP64, LQFP48

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