
PRODUCT ADVISORY NOTICE

Data Sheet Specification Change for Intersil Product ISL6446IAZ*

**Refer to:
PA13054**

Date: October 16, 2013

October 16, 2013

To: Our Valued Intersil Customer

Subject: **Data Sheet Specification Change for Intersil Product ISL6446IAZ***

This notice is to inform you that Intersil has updated the data sheet specification for the listed ISL6446IAZ* products. The change to the ESD Rating aligns the data sheet with the product characteristics. Details regarding the change are contained on the following page. The updated data sheet is available on the Intersil web site at <http://www.intersil.com/content/dam/Intersil/documents/fn79/fn7944.pdf>.

Products affected:

ISL6446IAZ	ISL6446IAZ-T	ISL6446IAZ-T7
ISL6446IAZ-T7A	ISL6446IAZ-TK	ISL6446IAZ-TKS2734

There have been no changes made to the die/silicon. There will be no change in external marking of the packaged parts.

Intersil will take all necessary actions to conform to agreed upon customer requirements and to ensure the continued high quality and reliability of Intersil products being supplied. Customers may expect to continue receiving product processed to the same established conditions and systems used for manufacturing of material supplied today.

If you have concerns with this notice, Intersil must hear from you promptly. Please contact the nearest Intersil Sales Office or call the Intersil Corporate line at 1-888-468-3774, in the United States, or 1-321-724-7143 outside of the United States.

Regards,



Jeffrey Touvell
Intersil Corporation

PA13054

CC: J. Glover D. Singh J. Xiao

PA13054 Data Sheet Updates

From:

ISL6446

Absolute Maximum Ratings (Note 4)

SS1/EN1, SS2/EN2, COMP1, COMP2 to SGND	-0.3V to +6.0V
VCC, FB1, FB2, RT, PGOOD to SGND	-0.3V to +6.0V
LCDR, LCFB to SGND	-0.3V to +6.0V
VIN, OCSET1, and OCSET2 to PGND	-0.3V to +28V
BOOT1 and BOOT2 to PGND	-0.3V to +33V
BOOT1 to PHASE1, and BOOT2 to PHASE2	-0.3V to +6.0V
UGATE1 to PHASE1	-0.3V to (BOOT1 +0.3V)
UGATE2 to PHASE2	-0.3V to (BOOT2 +0.3V)
LGATE1, LGATE2 to PGND	-0.3V to (VCC+0.3V)
PHASE1, PHASE2 to PGND	-1V to 28V
SGND to PGND	-0.3V to 0.3V
ESD Rating	
Human Body Model (Tested per JESD22-A114E)	2000V
Machine Model (Tested per JESD22-115-A)	200V
Latch Up (Tested per JEDEC-78B Level II Class A)	±100mA @ +85°C

CAUTION: Do not operate at or near the maximum ratings listed for extended periods of time. Exposure to such conditions may adversely impact product reliability and result in failures not covered by warranty.

NOTES:

- All voltages are measured with respect to GND.
- θ_{JA} is measured with the component mounted on a high effective thermal conductivity test board in free air. See Tech Brief [TB379](#) for details.
- For θ_{JC} , the "case temp" location is taken at the package top center.

Thermal Information

Thermal Resistance (Typical)	θ_{JA} (°C/W)	θ_{JC} (°C/W)
QSO8 Package (Notes 5, 6)	75	36
Maximum Junction Temperature (Plastic Package)	-55°C to +150°C	
Maximum Storage Temperature Range	-65°C to +150°C	
Temperature Range	-40°C to +85°C	
Pb-Free Reflow Profile	see link below	
	http://www.intersil.com/pbfree/Pb-FreeReflow.asp	

Recommended Operating Conditions

VCC Supply Voltage	.5V ±10%
VIN Supply Voltage	.5.5V to 24V

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LCDR, LCFB to SGND	-0.3V to +6.0V
VIN, OCSET1, and OCSET2 to PGND	-0.3V to +28V
BOOT1 and BOOT2 to PGND	-0.3V to +33V
BOOT1 to PHASE1, and BOOT2 to PHASE2	-0.3V to +6.0V
UGATE1 to PHASE1	-0.3V to (BOOT1 +0.3V)
UGATE2 to PHASE2	-0.3V to (BOOT2 +0.3V)
LGATE1, LGATE2 to PGND	-0.3V to (VCC+0.3V)
PHASE1, PHASE2 to PGND	-1V to 28V
SGND to PGND	-0.3V to 0.3V
ESD Rating	
Human Body Model (Tested per JESD22-A114E)	2500V
Machine Model (Tested per JESD22-115-A)	150V
Latch Up (Tested per JEDEC-78B Level II Class A)	±100mA @ +85°C

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