

# **PRODUCT ADVISORY**

## **Data Sheet Specification Change for Intersil Product ZL2008\***

**Refer to:  
PA10060**

**Date: June 15, 2010**

June 15, 2010

To: Our Valued Intersil Customers

Subject: **Data Sheet Specification Change for Intersil Product ZL2008\***

This advisory is to inform you that Intersil has updated the data sheet specification for the listed ZL2008\* products. The changes correct the component selections for the Phase Offset Resistor settings (Table 26) and the Current Sense Position settings (Table 28) for DCR and RDS modes of operation. The DCR and RDS labels in the Current Sense section of the tables were reversed. Modifications to the data sheet specification incorporating the changes are complete. The updated data sheet, FN6859 dated June 15, 2010, is available on the Intersil web site at <http://www.intersil.com/data/fn/fn6859.pdf>.

Products affected: **ZL2008ALAF1** **ZL2008ALAF1**

Intersil will take all necessary actions to conform to customer requirements and to ensure the continued high quality and reliability of Intersil products being supplied. Customers may expect to continue receiving product processed to the same established conditions and systems used for screening of material supplied today.

If you have concerns with this change notice, Intersil must hear from you immediately. Please contact the nearest Intersil Sales Office or call the Intersil Corporate line at 1-888-468-3774, in the United States, or 1-321-724-7143 outside of the United States.

*Jon Brewster*

Jon Brewster  
Intersil Corporation

PA10060

CC: J. Touvell M. Pandola J. Glover T. Wolter

## PA10060 - Table 26 – Phase Offset Resistor Settings

### OLD

TABLE 26. Phase Offset Resistor Settings

R <sub>CFG2</sub> (kΩ)	Phase Offset (°)	Current Sense
10	22.5	RDS
11	45	
12.1	67.5	
13.3	90	
14.7	112.5	
16.2	135	
17.8	157.5	
19.6	180	
21.5	202.5	
23.7	225	
26.1	247.5	
28.7	270	
31.6	292.5	
34.8	315	
38.3	337.5	
42.2	22.5	DCR
46.4	45	
51.1	67.5	
56.2	90	
61.9	112.5	
68.1	135	
75	157.5	
82.5	180	
90.9	202.5	
100	225	
110	247.5	
121	270	
133	292.5	
147	315	
162	337.5	

### NEW

TABLE 26. Phase Offset Resistor Settings

R <sub>CFG2</sub> (kΩ)	Phase Offset (°)	Current Sense
10	22.5	DCR
11	45	
12.1	67.5	
13.3	90	
14.7	112.5	
16.2	135	
17.8	157.5	
19.6	180	
21.5	202.5	
23.7	225	
26.1	247.5	
28.7	270	
31.6	292.5	
34.8	315	
38.3	337.5	
42.2	22.5	RDS
46.4	45	
51.1	67.5	
56.2	90	
61.9	112.5	
68.1	135	
75	157.5	
82.5	180	
90.9	202.5	
100	225	
110	247.5	
121	270	
133	292.5	
147	315	
162	337.5	

## PA10060 - Table 28 – Current Share Position Settings

### OLD

TABLE 28. Current Share Position Settings

R <sub>CFG2</sub> (kΩ)	Current Share Position	Current Sense
10	0	RDS
11	1	
12.1	2	
13.3	3	
14.7	4	
16.2	5	
17.8	6	
19.6	7	
42.2	0	DCR
46.4	1	
51.1	2	
56.2	3	
61.9	4	
68.1	5	
75	6	
82.5	7	

### NEW

TABLE 28. Current Share Position Settings

R <sub>CFG2</sub> (kΩ)	Current Share Position	Current Sense
10	0	DCR
11	1	
12.1	2	
13.3	3	
14.7	4	
16.2	5	
17.8	6	
19.6	7	
42.2	0	RDS
46.4	1	
51.1	2	
56.2	3	
61.9	4	
68.1	5	
75	6	
82.5	7	