

intersil	
TITLE:	REV: A
WR<< HSP45314EVAL2 >>	DATE: 01/07/00
ENGINEER: PHILIP PRATT	DRAWN BY: PHILIP PRATT
SHEET: 1 OF 3	

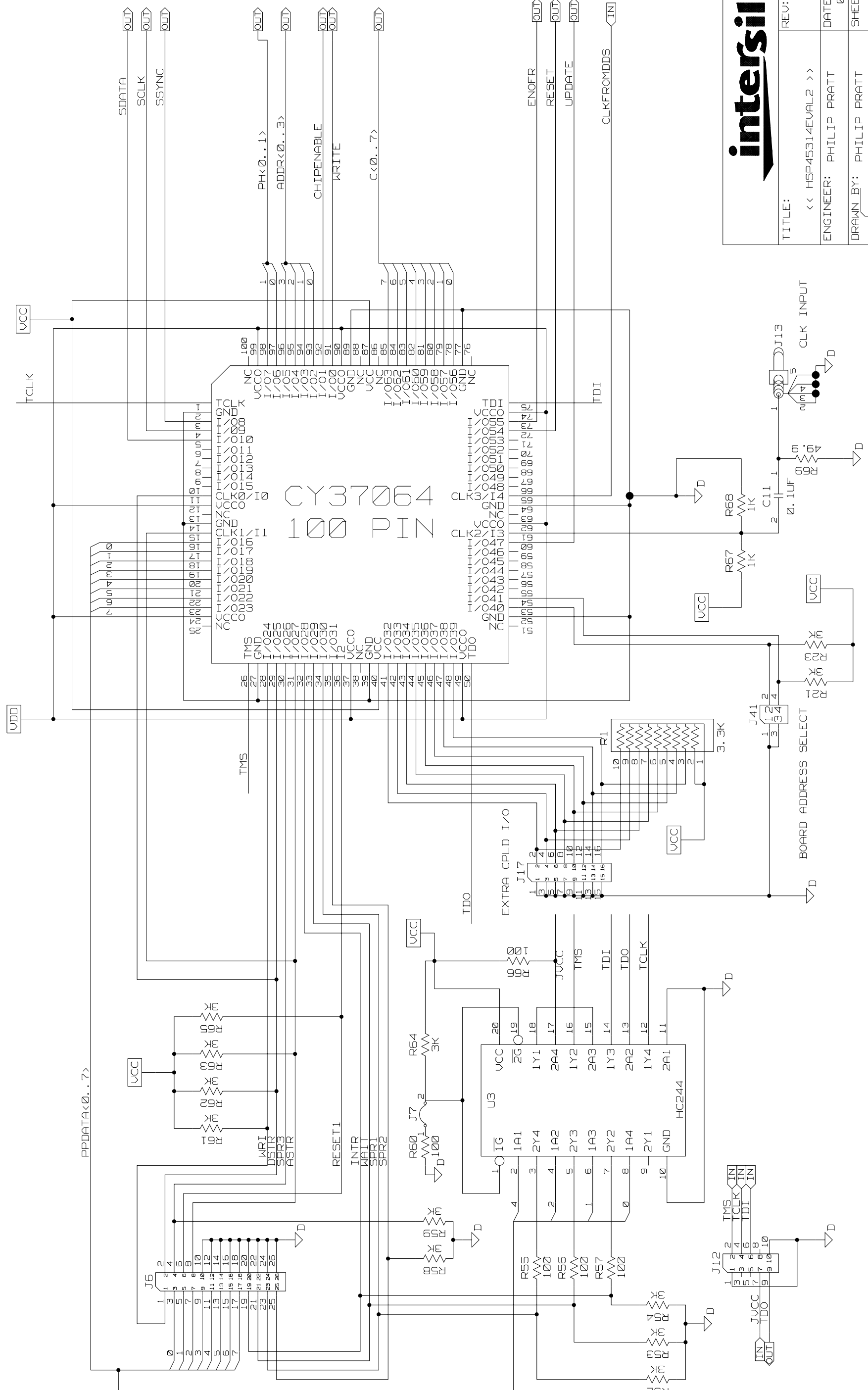
DDS AND ANALOG FUNCTIONS

STATIC PULL-UP OPTIONS
ALL PULL-UP RESISTORS
REPRESENT THE DEFAULT
HIGH POSITION

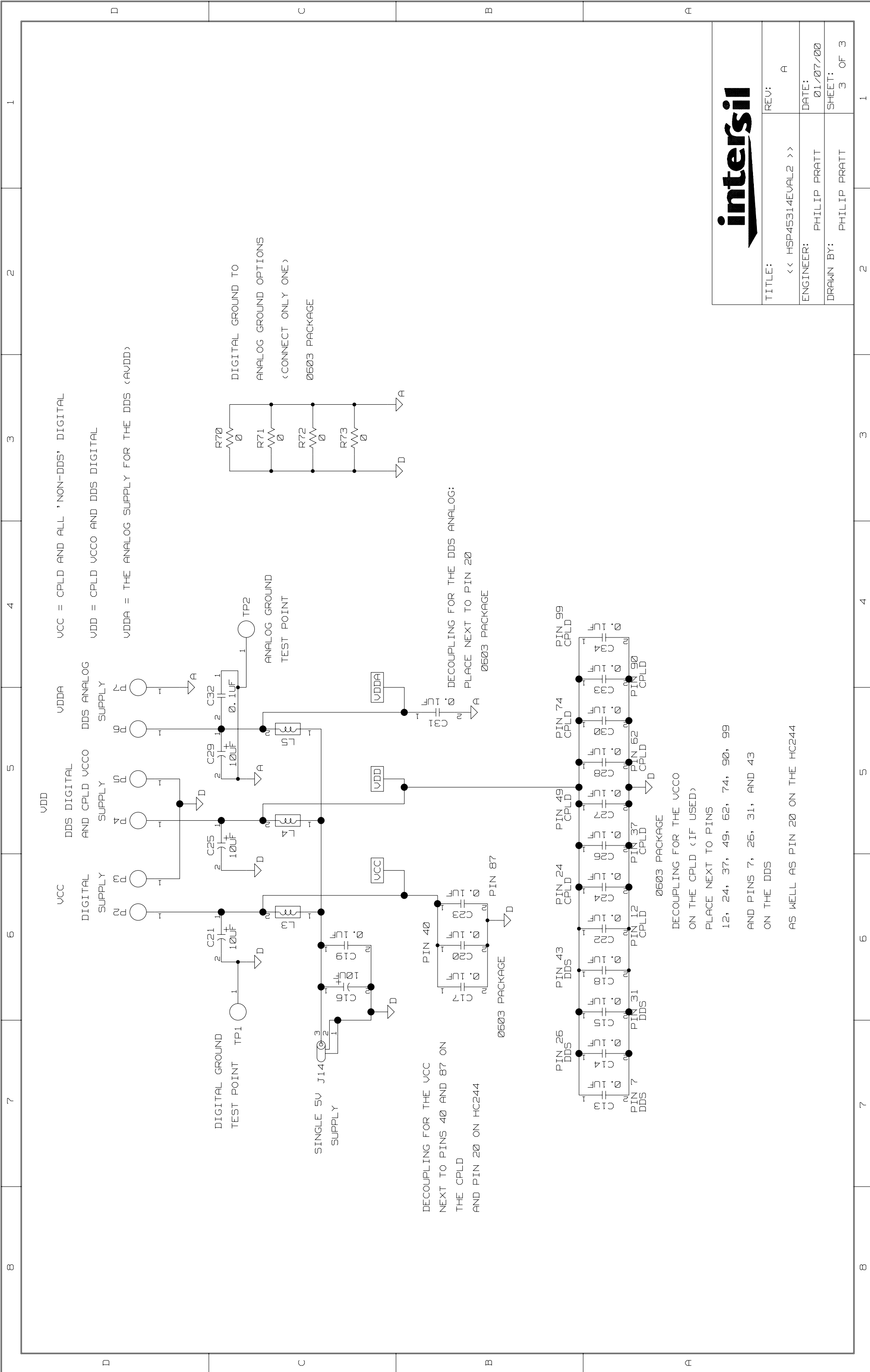
ALL PULL DOWN RESISTORS
REPRESENT THE DEFAULT
LOW POSITION

STATIC PULL-DOWN OPTIONS

PARALLEL PORT/CPLD FUNCTION



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