

Integrated Device Technology, Inc. 6024 Silver Creek Valley Road, San Jose, CA - 95138

PRODUCT/PROCESS CHANGE NOTICE (PCN)

		Dec 1/1 Roci			`	,	
PCN #: U08	803-01	DATE: 03/18/08 MEANS OF DISTINGUISHING CHANGED DEVICES:					
Product Affects			☐ Product Mark				
		FI 5V9885NLGI 5V98851		☐ Back Mark			
		FI 5V9888NLGI 5V98881	NLI [☐ Date Code			
Date Effective:	Immediate			Other	Note: continue	ous improvement	
Contact:	PS Tow					_	
Title:	QA Director		A	Attachment::	Yes	∐ No	
Phone #:	(408) 284-8206				37/4		
Fax #:	(408) 284-8362		2	samples:	N/A		
E-mail:	pstow@idt.com						
DESCRIPTION AND PURPOSE OF CHANGE:							
■ Die Technol			ous improv	ement on vield and	device operation	, IDT has decided to change	
1				licon to improve yield and device functionalities. See attachment			
☐ Assembly Process for more details.							
□ Equipment							
□ Material							
□ Testing							
☐ Manufacturing Site							
□ Data Sheet							
☐ Other							
RELIABILITY/QUALIFICATION SUMMARY:							
RELIABILIT I/QUALIFICATION SUMMART.							
No qualification is needed. This is a minor die change.							
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CUSTOMER ACKNOWLEDGMENT OF RECEIPT:							
IDT records indicate that you require written notification of this change. Please use the acknowledgement below or E-Mail							
to grant approval or request additional information. If IDT does not receive acknowledgement within 30 days of this notice							
it will be assumed that this change is acceptable.							
IDT reserves the right to ship either version manufactured after the process change effective date until the inventory							
on the earlier version has been depleted.							
Customer: Approval for shipments prior to effective date.							
Name/Date: E-I				ail Address:			
Title: Phone# /Fax#:							
CUSTOMED COMMENTS.							
CUSTOMER COMMENTS:							
IDT ACKNOWLEDGMENT OF RECEIPT:							
DECD DV.							
RECD. BY: DATE:							

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ATTACHMENT - PCN # U0803-01

Change from

- No pull-down resistors on the control inputs, and the device can accidentally select an un-intended configuration when the input pins are left floating.
- The device VCO speed may out-run the counters momentarily and cause the PLL to un-lock when the device is programmed on-the-fly.

Change to

- Added pull-down resistors on the control input pins so that they can be left floating when unused.
- Reduced the VCO speed so that it will not out-run the counters. The datasheet VCO max spec is unchanged at 1.2GHz.
- Fixed the LOSS_CLKIN operation to match the operation stated in the datasheet.