

## Product Change Notice (PCN)

**Subject:** Datasheet Specification Change for Listed Intersil KAD5510P-\* Products

**Publication Date:** 2/8/2016

**Effective Date:** 5/9/2016

**Revision Description:**

Initial Release

**Description of Change:**

This notice is to inform you that Intersil has changed the electrical specification table for 1.8V Analog Supply Current and Nap Mode parameters for the following products.

Intersil Product Number	Intersil Product Number	Intersil Product Number	Intersil Product Number
KAD5510P-25Q48	KAD5510P-21Q48	KAD5510P-12Q48	KAD5510P-17Q48

**Reason for Change:**

The change aligns the data sheet with the product characteristics and is necessary to maintain product manufacturability in support of customer delivery requirements. Details regarding the change are contained on the following page. The updated data sheet is available on the Intersil web site at:

<http://www.intersil.com/content/dam/intersil/documents/kad5/kad5510p.pdf>

**Product Identification:**

There have been no changes to the die/silicon or product itself. There will be no change in the external marking of the packaged parts.

**Qualification status:** Complete

**Sample availability:** 2/8/2016

**Device material declaration:** Available upon request

*Questions or requests pertaining to this change notice, including additional data or samples, must be sent to Intersil within 30 days of the publication date.*

For additional information regarding this notice, please contact your regional change coordinator (below)			
Americas: <a href="mailto:PCN-US@INTERSIL.COM">PCN-US@INTERSIL.COM</a>	Europe: <a href="mailto:PCN-EU@INTERSIL.COM">PCN-EU@INTERSIL.COM</a>	Japan: <a href="mailto:PCN-JP@INTERSIL.COM">PCN-JP@INTERSIL.COM</a>	Asia Pac: <a href="mailto:PCN-APAC@INTERSIL.COM">PCN-APAC@INTERSIL.COM</a>

From:

**Electrical Specifications** All specifications apply under the following conditions unless otherwise noted: AVDD = 1.8V, OVDD = 1.8V, T<sub>A</sub> = -40°C to +85°C (typical specifications at +25°C), A<sub>IN</sub> = -1dBFS, f<sub>SAMPLE</sub> = Maximum Conversion Rate (per speed grade). **Boldface limits apply over the operating temperature range, -40°C to +85°C.**

PARAMETER	SYMBOL	CONDITIONS	KAD5510P-25			KAD5510P-21			KAD5510P-17			KAD5510P-12			UNITS
			MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	
1.8V Analog Supply Current	I <sub>AVDD</sub>			90	<b>96</b>		83	<b>89</b>		77	<b>82</b>		69	<b>74</b>	mA

**Electrical Specifications** All specifications apply under the following conditions unless otherwise noted: AVDD = 1.8V, OVDD = 1.8V, T<sub>A</sub> = -40°C to +85°C (typical specifications at +25°C), A<sub>IN</sub> = -1dBFS, f<sub>SAMPLE</sub> = Maximum Conversion Rate (per speed grade). **Boldface limits apply over the operating temperature range, -40°C to +85°C. (Continued)**

PARAMETER	SYMBOL	CONDITIONS	KAD5510P-25			KAD5510P-21			KAD5510P-17			KAD5510P-12			UNITS
			MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	
1.8V Digital Supply Current (DDR) (Note 5)	I <sub>OVDD</sub>	3mA LVDS		39	<b>45</b>		38	<b>45</b>		36	<b>40</b>		35	<b>40</b>	mA
Power Supply Rejection Ratio	PSRR	30MHz, 200mV <sub>p-p</sub> signal on AVDD		-36			-36			-36			-36		dB
<b>Total Power Dissipation</b>															
Normal Mode (DDR)	P <sub>D</sub>	3mA LVDS		234	<b>254</b>		219	<b>242</b>		204	<b>220</b>		189	<b>205</b>	mW
Nap Mode	P <sub>D</sub>			84	<b>95</b>		80	<b>91</b>		78	<b>88</b>		74	<b>84</b>	mW

To:

**Electrical Specifications** All specifications apply under the following conditions unless otherwise noted: AVDD = 1.8V, OVDD = 1.8V, T<sub>A</sub> = -40°C to +85°C (typical specifications at +25°C), A<sub>IN</sub> = -1dBFS, f<sub>SAMPLE</sub> = Maximum Conversion Rate (per speed grade). **Boldface limits apply across the operating temperature range, -40°C to +85°C. (Continued)**

PARAMETER	SYMBOL	TEST CONDITIONS	KAD5510P-25			KAD5510P-21			KAD5510P-17			KAD5510P-12			UNIT
			MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	
1.8V Analog Supply Current	I <sub>AVDD</sub>			90	<b>101.5</b>		83	<b>94.5</b>		77	<b>87.5</b>		69	<b>79.5</b>	mA
1.8V Digital Supply Current (DDR) (Note 5)	I <sub>OVDD</sub>	3mA LVDS		39	<b>45</b>		38	<b>45</b>		36	<b>40</b>		35	<b>40</b>	mA
Power Supply Rejection Ratio	PSRR	30MHz, 200mV <sub>p-p</sub> signal on AVDD		-36			-36			-36			-36		dB
<b>Total Power Dissipation</b>															
Normal Mode (DDR)	P <sub>D</sub>	3mA LVDS		234	<b>254</b>		219	<b>242</b>		204	<b>220</b>		189	<b>205</b>	mW
Nap Mode	P <sub>D</sub>			84	<b>104</b>		80	<b>100</b>		78	<b>97</b>		74	<b>93</b>	mW