PRODUCT CHANGE NOTICE

Data Sheet Specification Change for Intersil Product ISL21090BFB812Z-TK

Refer to:
PCN13021

Date: March 1, 2013

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March 1, 2013

To: Our Valued Intersil Customer

Subject: **Data Sheet Specification Change for Intersil Product ISL21090BFB812Z-TK**

This notice is to inform you that Intersil has changed the data sheet specification for the ISL21090BFB812Z-TK product. The changes remove the *ISC- Short Circuit Current* and *ΔVOUT/ΔIOUT Load Regulation - Sinking* parameters in the Electrical Specifications table for the 1.25V option and are necessary to maintain product manufacturability in support of customer delivery requirements. Details regarding the changes are contained on the following page. The updated data sheet is available on the Intersil web site at http://www.intersil.com/content/dam/Intersil/documents/fn69/fn6993.pdf.

There have been no changes made to the die/silicon. There will be no change in external marking of the packaged parts. Parts affected by this change are identifiable via Intersil's internal traceability system.

Intersil will take all necessary actions to conform to agreed upon customer requirements and to ensure the continued high quality and reliability of Intersil products being supplied. Customers may expect to receive product electrically screened to the revised data sheet beginning *ninety* days from the date of this notification or earlier with approval.

If you have concerns with this notice, Intersil must hear from you promptly. Please contact the nearest Intersil Sales Office or call the Intersil Corporate line at 1-888-468-3774, in the United States, or 1-321-724-7143 outside of the United States.

Regards,

Jon Brewster

Jon Brewster
Intersil Corporation

PCN13021

CC: J. Touvell K. Mouloua M. Carmody P. Lee

PA13021 Data Sheet Updates

From:

Electrical Specifications V_{IN} = 5V (1.25V option), I_{OUT} = 0, C_L = 0.1µF and C_C = 0.01µF, unless otherwise specified. **Boldface limits apply over the operating temperature range, -40°C to +125°C.**

PARAMETER	DESCRIPTION	CONDITIONS	MIN (Note 7)	TYP	MAX (Note 7)	UNIT
V_{OUT}	Output Voltage	V_{IN} = 5V,		1.25		V
V_{OA}	V_{OUT} Accuracy @ T_A = +25°C (Note 6)	V_{OUT} = 1.25V	-0.03		+0.03	%
TC V_{OUT}	Output Voltage Temperature Coefficient (Note 8)	ISL21090 B grade			**7**	ppm/°C
V_{IN}	Input Voltage Range	V_{OUT} = 1.25V	**3.7**		**36**	V
I_{IN}	Supply Current			0.750	**1.28**	mA
$\Delta V_{OUT}/\Delta V_{IN}$	Line Regulation	V_{IN} = 3.7V to 36V, V_{OUT} = 1.25V		6	**17**	ppm/V
$\Delta V_{OUT}/\Delta I_{OUT}$	Load Regulation	Sourcing: 0mA ≤ I_{OUT} ≤ 20mA		2.5	**17**	ppm/mA
		Sinking: -10mA ≤ I_{OUT} ≤ 0mA		2.5	**17**	ppm/mA
V_D	Dropout Voltage (Note 9)	V_{OUT} = 1.25V @ 10mA		1.7	**2.15**	V
I_{SC+}	Short Circuit Current	T_A = +25°C, V_{OUT} tied to GND		53		mA
I_{SC-}	Short Circuit Current	T_A = +25°C, V_{OUT} tied to V_{IN}		-23		mA
t_R	Turn-on Settling Time	90% of final value, C_L = 1.0µF, C_C = open		150		µs
	Ripple Rejection	f = 120Hz		90		dB
e_{np-p}	Voltage Noise	0.1Hz ≤ f ≤ 10Hz, V_{OUT} = 1.25V		1.0		μV_{P-P}
V_n	Broadband Voltage Noise	10Hz ≤ f ≤ 1kHz, V_{OUT} = 1.25V		1.2		μV_{RMS}
e_n	Noise Voltage Density	f = 1kHz, V_{OUT} = 1.25V		35.4		nV/√Hz
$\Delta V_{OUT}/\Delta t$	Long Term Stability	T_A = +25°C		20		ppm

To:

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e_n	Noise Voltage Density	f = 1kHz, V_{OUT} = 1.25V		25		nV/√Hz
$\Delta V_{OUT}/\Delta t$	Long Term Stability	T_A = +25°C		20		ppm