

# **PRODUCT CHANGE NOTICE**

**Alternate Manufacturing Site  
for Intersil ISL59121IIZ\*  
Product**

**Refer to:  
PCN10057**

**Date: June 9, 2010**

June 9, 2010

To: Our Valued Intersil Customer, Apple

Subject: **Alternate Manufacturing Site for Intersil ISL59121IIZ\* Product –  
Advanced Semiconductor Engineering (ASEKH)**

This notice is to inform you that Intersil has qualified the Advanced Semiconductor Engineering (ASEKH) facility located in Kaohsiung, Taiwan as an alternate site for performing assembly, final electrical test, tape and reel, packing, and shipment of ISL59121IIZ\* product. This action will expand current capabilities and capacities to optimize Intersil's ability to meet customer's delivery requirements. The product and site-specific qualification activities are complete.

Products Affected: **ISL59121IIZ-T7 ISL59121IIZ-T7S2378**

The Advanced Semiconductor Engineering (ASEKH) facility is ISO 9001:2008 and ISO/TS 16949:2009 certified. The following summarizes the activities that were conducted prior to production release to confirm there is no impact to the form, fit, function, or interchangeability of the product.

- Intersil supplied test software and product specific hardware is used at *ASEKH*.
- The test equipment validation plan involves the testing product at the current and the *ASEKH* facility.
  - Wafers were tested, their performance recorded, and the results analyzed.
  - Continuous monitoring and comparison of the data from lots processed at multiple sites are an ongoing function of the responsible product engineers.
- The visual/mechanical inspection and tape and reel operations are compliant to JEDEC industry standards used by all Intersil subcontractors.
- There will be no change in the package outline drawing (POD).
- The assembly qualification plans are designed using JEDEC and other applicable industry standards. A summary of the assembly bumping qualification results is included for reference.

Product affected by this change is identifiable via Intersil's internal traceability system. In addition, product assembled at *ASEKH* may also be identified by the assembly site code (country of assembly) when marked on the devices. The assembly site code for the *ASEKH* facility is "Y".

Intersil has created a custom part number (**R5417**) to identify product manufactured at either the *ASEKH* or SCS (STATS ChipPAC Singapore) facilities. This letter is to request that Apple begin using custom part number **ISL59121IIZ-T7R5417** for future product procurement needs.

If you have concerns with this change notice, Intersil must hear from you immediately. Please contact the nearest Intersil Sales Office or call the Intersil Corporate line at 1-888-468-3774, in the United States, or 1-321-724-7143 outside of the United States.



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Intersil Corporation

PCN10057

CC: J. Touvell S. Wu B. Galleher K. Tucker

## PCN10057 – ASEKH WLCSP Assembly Bumping Qualification

Test Name	Conditions	Duration	# of Lots	Sample Size	Rejects	Comments
Storage Life	+150°C	1000 hrs	3	234	0	Passed 234/0.
Temperature Cycle	-40°C TO +125°C	500 Cycles	3	234	0	Passed 234/0.