



Integrated Device Technology, Inc.
2975 Stender Way, Santa Clara, CA - 95054

PRODUCT/PROCESS CHANGE NOTICE (PCN)

PCN #: SM-0211-01	Date: December 2, 2002	MEANS OF DISTINGUISHING CHANGED DEVICES: <input type="checkbox"/> Product Mark <input type="checkbox"/> Back Mark <input checked="" type="checkbox"/> Date Code Top mark will have "4" following the die revision. Refer to page 2 <input type="checkbox"/> Other
Product Affected: 70914 Family		
Manufacturing Location Affected: Salinas, California		
Date Effective: March 3, 2003		

Contact: George Snell Title: Quality Assurance Manager Phone #: (831) 754-4556 Fax #: (831) 754-4672 E-mail: george.snell@idt.com	Attachment: <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No Samples: Available upon request.
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DESCRIPTION AND PURPOSE OF CHANGE:

<input type="checkbox"/> Die Technology <input type="checkbox"/> Wafer Fabrication Process <input type="checkbox"/> Assembly Process <input type="checkbox"/> Equipment <input type="checkbox"/> Material <input type="checkbox"/> Testing <input checked="" type="checkbox"/> Manufacturing Site <input type="checkbox"/> Data Sheet <input type="checkbox"/> Other	To consolidate Wafer Fab production from Salinas, California (Fab 2) to Hillsboro, Oregon (Fab 4) these qualified products will be transferred to IDT's Wafer Fab facility in Hillsboro, Oregon. Cmos 8 is an existing qualified process at Fab 4.
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RELIABILITY/QUALIFICATION SUMMARY:

Qualification has been completed that verifies no change to the product reliability. Qualification details are attached.

CUSTOMER ACKNOWLEDGMENT OF RECEIPT:

IDT records indicate that you require written notification of this change. Please use the acknowledgement below or E-Mail to grant approval or request additional information. If IDT does not receive acknowledgement within 30 days of this notice it will be assumed that this change is acceptable.
 IDT reserves the right to ship either version manufactured after the process change effective date until the inventory on the earlier version has been depleted.

Customer: _____	<input type="checkbox"/> <i>Approval for shipments prior to effective date.</i>
Name/Date: _____	E-Mail Address: _____
Title: _____	Phone# /Fax# : _____

CUSTOMER COMMENTS:

IDT ACKNOWLEDGMENT OF RECEIPT:

RECD. BY: _____ DATE: _____



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PCN Type: Fab Site Change

Data Sheet Change None

Detail of Change

Transfer existing qualified products from Salinas, California Wafer Fab Facility (Fab 2) to Hillsboro, Oregon Wafer Fab Facility (Fab 4).

Part Name	Current Wafer Fab				Transfer Wafer Fab			
	Manufacturing Site	Technology	Wafer Size	Die Revision	Manufacturing Site	Technology	Wafer Size	Die Revision
70914Y	Salinas, CA	Cmos 8	6 inch	Y	Hillsboro, OR	Cmos 8	8 inch	Y4

Conversion schedule (Estimated)

Base Device

Production Shipments and Sample availability

70914Y4

3/2/03

Now

IDT70914 Family of Parts						
Part Number	Die Rev.	Interface	Vcc	Bus	Depth	Density
IDT70914	Y4	Sync	5	x9	4K	36K
IDT7014	Y4	Async	5	x9	4K	36K
ID!709149	Y4	Sync	5	x9	4K	36K



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PRODUCT: 70914Y

Qualification Test Results

Test Description	Test Methods	Sample size/# Fails	
Operating Life Test: Dynamic at +135°C, Vcc=6v for 750 hours	JESD22-A108	116/0	
High Temp. Storage Life Test (Unbiased, 1000 hours @ +150°C)	JESD22-A103	77/0	
Bake & Ballshear Test @ 200°C	EIA/JESD22-B116	5/0	
Temperature Cycling: (-65°C to +150°C, 500 cycles)	JESD22-A104, Condition C	45/0	
HAST: Biased, 100 Hrs. @+135°C, +85%RH, 3 Atm.)	JEDEC STD 22, Method A110	45/0	
Autoclave: (Unbiased, 2 Atm Saturated Steam, +121°C, 168 Hrs	EIA/JESD22-B102	45/0	Voltage Rating
ESD Human Body Model	EIA/JESD22-A114	3/0	2500V
ESD Charged Device Model	JESD 22-C101	3/0	1000V
Latch up: (Tested to 1.5X Vcc)	EIA/JESD78	10/0	

Characterization Data: Characterization has been completed as part of product qualification and data available upon request. Characterization verified that there is no change to existing datasheet parameters.