



Integrated Device Technology, Inc.  
6024 Silver Creek Valley Road, San Jose, CA - 95138

## PRODUCT/PROCESS CHANGE NOTICE (PCN)

<b>PCN #:</b> <b>A1506-01</b> <b>DATE:</b> <b>18-Aug-2015</b> <b>Product Affected:</b> VFQFPN-100 Refer to Attachment II for the affected part numbers  <b>Date Effective:</b> <b>18-Nov-2015</b>	<b>MEANS OF DISTINGUISHING CHANGED DEVICES:</b> <input type="checkbox"/> Product Mark                      Lot # will have: <input checked="" type="checkbox"/> Back Mark                            "U" prefix for UTAC, Thailand <input type="checkbox"/> Date Code <input type="checkbox"/> Other
---	--

<b>Contact:</b> IDT PCN DESK  <b>E-mail:</b> <a href="mailto:pcndesk@idt.com">pcndesk@idt.com</a>	<b>Attachment:</b> <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No  <b>Samples:</b> Please contact your local sales representative for sample request.
---	--

**DESCRIPTION AND PURPOSE OF CHANGE:**

<input type="checkbox"/> Die Technology <input type="checkbox"/> Wafer Fabrication Process <input type="checkbox"/> Assembly Process <input type="checkbox"/> Equipment <input type="checkbox"/> Material <input type="checkbox"/> Testing <input checked="" type="checkbox"/> Manufacturing Site <input type="checkbox"/> Data Sheet <input type="checkbox"/> Other	<p>This notification is to advise our customers that IDT is transferring the affected products assembled at Amkor, Korea (ATK) to UTAC, Thailand (UTL) as ATK will discontinue their assembly tooling for these products.</p> <p>There is no change to the moisture performance.</p> <p>Attachment I details the qualification results and Attachment II shows the affected list of part numbers.</p>
--	---

**RELIABILITY/QUALIFICATION SUMMARY:**  
Refer to qualification data shown in Attachment I.

**CUSTOMER ACKNOWLEDGMENT OF RECEIPT:**

IDT records indicate that you require written notification of this change. Please use the acknowledgement below or E-Mail to grant approval or request additional information. If IDT does not receive acknowledgement within 30 days of this notice it will be assumed that this change is acceptable.

IDT reserves the right to ship either version manufactured after the process change effective date until the inventory on the earlier version has been depleted.

<b>Customer:</b> _____ <b>Name/Date:</b> _____ <b>Title:</b> _____	<input type="checkbox"/> <b><i>Approval for shipments prior to effective date.</i></b> <b>E-Mail Address:</b> _____ <b>Phone# /Fax# :</b> _____
--	---

**CUSTOMER COMMENTS:** \_\_\_\_\_

**IDT ACKNOWLEDGMENT OF RECEIPT:**

**RECD. BY:** \_\_\_\_\_                      **DATE:** \_\_\_\_\_



## PRODUCT/PROCESS CHANGE NOTICE (PCN)

### ATTACHMENT I - PCN # : A1506-01

**PCN Type:** Manufacturing Site - Transfer Assembly Location

**Data Sheet Change:** None

No change in moisture sensitivity level (MSL)

#### Detail Of Change:

This notification is to advise our customers that IDT is transferring the affected products assembled at Amkor, Korea (ATK) to UTAC, Thailand (UTL) as ATK will discontinue their assembly tooling for these products.

The material set details of the current and new assembly location is as shown in Table 1. The die attach and mold compound used at the new assembly are qualified IDT materials. There is no change from the existing qualified lead frame material, lead finish, and wire for the new assembly location.

There is no change to the moisture performance.

Table 1: Assembly Material Sets for The Existing and New Assembly Location

	Existing Assembly (ATK, Korea)	New Assembly (UTL, Thailand)
Die Attach	CRM1085A	8600
Wire	Cu wire	Cu wire
Mold Compound	G700	G770HCD



Integrated Device Technology, Inc.  
6024 Silver Creek Valley Road, San Jose, CA - 95138

## PRODUCT/PROCESS CHANGE NOTICE (PCN)

### ATTACHMENT I - PCN # : A1506-01

#### Qualification Information and Qualification Data:

**Affected Packages:** VFQFPN-100

**Assembly Material:** The affected package type is using UTL standard materials shown on page 2 of this attachment.

**Qual Plan & Results:** Tests are in accordance with JEDEC47 recommended tests.

**Qualification Vehicle:** VFQFPN-100

Test Description	Test Method	Test Results (Rej / SS)		
		Lot 1	Lot 2	Lot 3
* Temperature Cycling (-55°C to 125°C, 700 cycles)	JESD22-A104	0/30	0/30	0/30
* HAST - biased (130 °C/85% RH, 264 Hrs)	JESD22-A110	0/30	0/30	0/30
High Temperature Storage Bake (150°C, 1000 Hrs)	JESD22-A103	0/30	0/30	0/30

\* Tests were subjected to Preconditioning per JESD22-A113 prior to stress test



Integrated Device Technology, Inc.  
6024 Silver Creek Valley Road, San Jose, CA - 95138

## PRODUCT/PROCESS CHANGE NOTICE (PCN)

### ATTACHMENT II - PCN # : A1506-01

#### Affected Part Numbers

Part Number	Part Number	Part Number	Part Number
8C54916NLGI	8V54816ANLG	8V54816ANLGI	8V54816NLG
8C54916NLGI8	8V54816ANLG8	8V54816ANLGI8	8V54816NLG8

Attachment III- Package Comparizons

		Current Site	New Site
Pkg and Si Attribute		ATK, Korea	UTL, Thailand
Pkg	Pkg type	NLG100 (Punch version)	NLG100 (Sawn version)
	Pkg x & y (mm)	12 X 12	12 X 12
	Pkg z (mm)	min - 0.80; max - 0.90	min - 0.80; max - 0.90
	Max Voltage	NA	NA
	Capacitors	NA	NA
Silicon & FLI	Si Process	No change	No change same wafer
	Wafer Size	No change	No change same wafer
	Die size (mm2)	No change	No change same wafer
	Die Aspect Ratio	No change	No change same wafer
	Die thickness (mils)	No change	No change same wafer
	Polyimide (Y/N)	No change	No change same wafer
	Silicon Metal Layers	No change	No change same wafer
	Scribe Width (um)	No change	No change same wafer
	UBM source	No change	No change same wafer
	Silicon UBM Stack-up	No change	No change same wafer
	Bump source	No change	No change same wafer
	Bump pitch	No change	No change same wafer
	I/O & Core (um)	No change	No change same wafer
	Total Bump count	No change	No change same wafer
	Bump Diameter	No change	No change same wafer
	Bump Height	No change	No change same wafer
	Bump Metallurgy	No change	No change same wafer
	Wafer Bump Flux	No change	No change same wafer
CAM Flux	No change	No change same wafer	
Underfill Material	No change	No change same wafer	
Silicon UBM/SRO	No change	No change same wafer	
Substrate	Halogen Free ?	NA	NA
	Substrate Layers	NA	NA
	Substrate thickness	NA	NA
	Core thickness (um)	NA	NA
	Core Material	NA	NA
	Outer layer Lines/space (um)	NA	NA
	Bump Pre-solder (SOP)	NA	NA
	Bump presolder (SOP) height/diameter	NA	NA
	Bump Capture Pad/SRO IO (um)	NA	NA
	Substrate Ball Capture Pad/SRO (um)	NA	NA
	Number of PTH/M1-M2 uVias	NA	NA
	Core PTH/Capture pad (um)	NA	NA
	Substrate Design Rule & BOM	NA	NA
	Substrate Supplier	NA	NA
	Build up layer (thickness)	NA	NA
	Solder mask (thickness)	NA	NA
	C1 & C4 thickness (plate)	NA	NA
	C2 & C3 thickness (foil + plate)	NA	NA
Surface finish (thickness)	NA	NA	
SLI	2nd level Ball count	NA	NA
	2nd level BA Flux	NA	NA
	2nd Ball Dia (mm)	NA	NA
	2nd level metallurgy	NA	NA
	2nd level ball pitch (mm)	NA	NA